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## **Information Technology - Scheduled Transfer Protocol (ST)**

Secretariat : National Committee for Information Technology Standardization (NCITS)

This is an internal working draft of T11.1, a Task Group of Technical Committee T11 of Accredited Standards Committee NCITS. As such, this is not a completed standard. The contents are actively being modified by T11.1.

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### **ABSTRACT**

This document describes a data transfer protocol that uses small control messages to pre-arrange data movement. Buffers are allocated at each end before the data transmission, allowing full-rate, non-congesting data flow between the end devices. The control and data may use different physical media or may share a single physical medium. Procedures are provided for moving data over HIPPI and other media.

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## Comments on Rev 1.45 and 1.5 (changes for 1.5 marked with double margin bars)

This is a preliminary document undergoing lots of changes. Many of the additions are just place holders, or are put there to stimulate discussion. Hence, do not assume that the items herein are correct, or final – everything is subject to change. This page tries to outline where we are; what has been discussed and semi-approved, and what has been added or changed recently and deserves your special attention. This summary relates to changes since the previous revision. Also, previous open issues are outlined with a single box, new open issues ones are marked with a double bar on the left edge of the box.

Changes are marked with margin bars so that changed paragraphs are easily found, and then highlights mark the specific changes. The list below just describes the major changes, for detail changes please compare this revision to the previous revision. **The major technical changes are printed in bold.**

Please help us in this development process by sending comments, corrections, and suggestions to the Technical Editor, Don Tolmie, of the Los Alamos National Laboratory, at det@lanl.gov. If you would like to address the whole group working on this document, send the comment(s) to hippo@network.com.

1. Added page numbers to the cross references, e.g., (see xx.x page yy). These are not marked with margin bars or highlights. All of the cross references should have been done, please inform the editor if you find some missing ones.
2. In 3.1.9, changed the definition of Opaque from "Six bytes..." to "Four bytes..." and from "...the Scheduled Header..." to "...a Data operation's Schedule Header...".
3. In 3.1.30, added a definition for "segment".
4. In 3.2, added "Operations contained within <...> are conditional, and may not occur".
5. In 3.3, added acronyms for ATM, FC, LLC, and VC.
6. In 4.2, second paragraph, changed the first sentence from "...the Virtual Connection should not become congested" to "...the Virtual Connection end devices should not become congested".
7. In 4.2, the paragraph immediately preceding Figure 4, added "(with Op = Data)" and

"(with Op ≠ Data)" in the first and second sentence. **Added the last sentence reading "If a received operation is not a legal length, then it shall be discarded".**

8. In Figure 5, deleted "Max\_Block Size" from "Parameters for remote end" and "Parameters for local end". **Added "Max\_Block" to the "Transfer Descriptor" and to the note beside it.**
9. In 5.1, changed "...checksum..." to "...optional checksum..." and changed "...not included in this clause" to "...not mentioned further...".
10. In all of the detailed operations for the different sequences, e.g., in 5.1.1, reordered the parameters of an operation to match the new layout of the Schedule Header, i.e., to match the way the Schedule Header fields were shuffled. These changes are not marked with highlights or margin bars.
11. In 5.1.1, under Request\_Connection, deleted "- I-Max\_Block specifies the maximum Block size that the Initiator will send (see 5.2.5)".
12. In 5.1.1, under Connection\_Answer, deleted "- R-Max\_Block specifies the maximum Block size that the Responder will send (see 5.2.5)".
13. In 5.2.1, split the second paragraph into two paragraphs.
14. In 5.2.2, added the last sentence of the first paragraph reading "The locally assigned Key value shall monotonically increase for each new Virtual Connection to a specific host.". In Rev 1.5, changed this sentence to read: "The locally assigned Key value for each new Virtual Connection to a specific host shall not duplicate a local Key value to this same host in use within the last 10 minutes".
15. In 5.2.3, decreased the maximum Buffer size from  $2^{63}$  to  $2^{32}$ .
16. In 5.2.4, increased the maximum STU size from  $2^{31}$  to  $2^{32}$ .
17. Deleted the original 5.2.5 titled "Maximum Block size (Max\_Block)". This parameter was moved from the Virtual

- Connection setup to the Transfer setup in 6.2.5.**
18. In 5.2.5, last paragraph, deleted the sentence duplicated from the previous paragraph reading "A received Slots value of x'FFFF' indicates that the remote end device cannot supply an update to the Slots value now."
  19. In 6.1, changed "...checksum..." to "...optional checksum..." and changed "...not included in this clause" to "...not mentioned further..."
  20. In 6.1.2, under "CTS\_req" of "Request\_To\_Send", changed "...number of Blocks..." to "...number of outstanding Blocks..." and changed "...would like continuously exposed..." to "...would like exposed at any given time...". This same change was made in the other places using "CTS\_req".
  21. In 6.1.2, under "Request\_To\_Send", added "- Max\_Block specifies the maximum Block size that the Initiator would like to send for this Transfer (see 6.2.5)."
  22. In 6.1.2, under "Request\_Answer", changed "...Request\_To\_Send has been accepted but the subsequent Clear\_To\_Send may be delayed..." to "...Request\_To\_Send has been recognized; final action is pending (i.e., a Clear\_To\_Send or Request\_Answer with R = 1)...". Similar changes were made to the other Request\_Answer operations.
  23. In 6.1.3, under "Request\_Answer", changed "...Request\_To\_Receive has been accepted but the subsequent Request\_To\_Send may be delayed..." to "...Request\_To\_Receive has been recognized; final action is pending (i.e., a Request\_To\_Send or Request\_Answer with R = 1)...".
  24. In 6.1.3, under "CTS\_req" of "Request\_To\_Send", changed "...number of Blocks..." to "...number of outstanding Blocks..." and changed "...would like continuously exposed..." to "...would like exposed at any given time...". Added "- Max\_Block specifies the maximum Block size that the Responder would like to send for this Transfer (see 6.2.5)."
  25. In 6.1.3, under the second "Request\_Answer", changed "...Request\_To\_Send has been accepted but the subsequent Clear\_To\_Send may be delayed..." to "...Request\_To\_Send has been recognized; final action is pending (i.e., a Clear\_To\_Send or Request\_Answer with R = 1)...".
  26. In 6.1.4.1, under "Request\_Answer", changed "...Request\_Memory\_Region has been accepted but the subsequent Memory\_Region\_Available may be delayed..." to "...Request\_Memory\_Region has been recognized; final action is pending (i.e., a Memory\_Region\_Available or Request\_Answer with R = 1)...".
  27. In 6.1.4.3, under "Get", changed "...by the Initiator to specify..." to "...by the Initiator. The Get specifies...".
  28. In 6.1.4.3, under "Request\_Answer", changed "...Get has been accepted but the subsequent Data operation may be delayed..." to "...Get has been recognized; final action is pending (i.e., a Data operation or Request\_Answer with R = 1)...".
  29. In 6.1.4.3, under "Request\_Answer", and under "Data", added "- R-id echoes the Responder's Transfer identifier (see 6.2.1)."
  30. In 6.1.4.4, in the first paragraph, added the sentence reading "Since the length is fixed at 64 bits, no T\_len parameter is used.". Deleted the "T\_len" parameter under "FetchOp". Under "FetchOp", changed "...by the Initiator to specify..." to "...by the Initiator. The FetchOp specifies...".
  31. In 6.1.4.4, under "Request\_Answer", deleted "...T\_len 1 x'0008'...". Also changed "...FetchOp has been accepted but the subsequent Data operation may be delayed..." to "...FetchOp has been recognized; final action is pending (i.e., a Data operation or Request\_Answer with R = 1)...".
  32. In 6.1.4.4, under "Request\_Answer", and under "Data", added "- R-id echoes the Responder's Transfer identifier (see 6.2.1)."

33. In 6.2.1, in the first paragraph, **changed the parameters from 16-bit to 32-bit.** Changed "...x'FFFF' is a reserved value" to "...x'FFFFFFFF' is a reserved value for I-id and R-id". **Added "Each Transfer identifier shall monotonically increase (to avoid aliasing)."** In the last paragraph, **changed "...x'FFFF'..." to "...x'FFFFFFFF'..." in two places.**
34. In 6.2.1, changed the title from "Transfer identifiers..." to "Sequence identifiers...". Changed "Transfer identifier" to "sequence identifier" three times in the first paragraph. Did a global change of "Transfer identifier" to "sequence identifier" in all of the detailed sequences (these are marked with highlights but not margin bars).
35. In 6.2.3, second paragraph, deleted the sentence reading "The size of the persistent memory region is independent of the maximum Block size specified in 5.2.5."
36. In 6.2.3, last paragraph, changed "...Get and FetchOp sequences..." to "...Get sequences..." in two places since the T\_len parameter was removed from FetchOp operations. Deleted "and shall conform to the maximum Block size specification in 5.2.5" from the end of the last sentence.
37. In 6.2.4, third paragraph, added "No more than  $2^{16}$  Blocks shall be enabled at any one time." as the last sentence. This aids the aliasing problem. In Rev 1.5, changed the sentence to read: "A user sending more than  $2^{32}$  Blocks in a Transfer is advised to consider the possibility of B\_num aliasing (i.e., having two Blocks with the same B\_num outstanding simultaneously)."
38. **In 6.2.5, added the whole new clause defining "Maximum Block size (Max\_Block)", copying most of the text from the original 5.2.5. This moves the Max\_Block negotiation from the Virtual Connection setup to the Transfer setup.**
39. In 6.2.6, deleted "(and is not applicable for persistent memory regions)", and added "...in a Clear\_To\_Send operation..." to the first sentence. **Changed the maximum Blocksize value from  $2^{63}$  to  $2^{48}$ .** **Added "Blocksize shall be  $\leq$  Max\_Block (see 6.2.5)."**
40. **In 6.2.7, added the sentence reading "STU\_num shall not wrap within a Block."**
41. **In 6.2.10, changed the Opaque data from 6 bytes to 4 bytes, and from being carried in the Op\_len and Offset\_2 fields to the S\_id field.** Deleted the last sentence reading "The Opaque data shall not be counted in the length, tiling, or Bufx calculations."
42. In 6.11, changed "...number of Blocks..." to "...number of outstanding Blocks..." and changed "...would like continuously exposed..." to "...would like exposed at any given time...".
43. In 6.2.12, changed "...shall the same..." to "...shall be the same...".
44. In 6.3, changed the last sentence to that proposed by Ian Philp at the January meeting.
45. In 6.3, last set of bullets, added the parenthetical expressions referencing where in the document to find the parameter. Added "Blocksize  $\leq$  Max\_Block".
46. **Figure 12 was changed by moving the D\_id and S\_id fields to the end and making them 32-bit fields (replacing Cksum, Op\_len, and Offset\_2). Cksum replaced D\_id, and B\_id replaced S-id.**
47. In 8.2, under the NOTE following "Interrupt", added "...and do not consume a Slot (see 5.2.5)".
48. In 8.3, the first sentence was changed from "... (Cksum) shall be..." to "... (Cksum), or x'0000' in the absence of a checksum, shall be...". **Except for the NOTE in 8.3.1, the rest of 8.3 was deleted. The checksum text in the 8.3 sub-clauses is all new, as well as figure 14.** In Rev 1.5, the note in 8.1 had some rewording.
49. In 9, the title was changed from "Operations details" to "Operations summary". A title, "Operation sequence tables" was added for the original text of clause 9. **A whole new clause 9.2 was added describing aliasing.** 9.2 is really quite crude and may well change based on input from the committee.
50. **In Table 3, changed the Anti-aliasing comment for Key to: "32-bit Key for each**

new Virtual Connection to a specific host will not duplicate a local Key value to this same host in use within the last 10 minutes.". Changed item 2 under "B\_num" from "Cannot have more than 2<sup>16</sup> Blocks enabled at one time" to "The user should consider B\_num aliasing if a Transfer contains more than 2<sup>32</sup> Blocks".

51. Tables 4 - 8 were changed considerably. The field locations and names were changed to match the new Schedule Header layout in figure 12. For example, the D\_id and S\_id fields were moved all the way to the right. Cksum was moved to follow D\_Key, and the B\_id field follows Cksum. The Op\_len and Offset\_2 field were deleted. Parameter changes include:

- In C1, EtherType moved from the B\_num field to the B\_id field. I-Max\_Block and R-Max\_Block were removed from the Offset\_2 field and placed in the Request\_To\_Send operations (W1) and (R2).
- In W1, added Max\_Block in the B\_id field.
- In W2, Blocksize moved from the Sync field to the Param field; R-Mx moved from Param field to B\_id field; F\_Offset moved from Offset\_2 field to Sync field.
- In W3, the Opaque data shrunk from 6 bytes to 4 bytes.
- In R2, Max\_Block added in the B\_id field.
- In R3, Blocksize moved from the Sync field to the Param field; I-Mx moved from Param field to B\_id field; F\_Offset moved from Offset\_2 field to Sync field.
- In R4, the Opaque data was removed entirely.
- In PG2, moved R-Mx from the Param field to the B\_id field.
- In PG3, the Opaque data shrunk from 6 bytes to 4 bytes.
- In PG5, moved T\_len from the Op\_len field to the Param field; moved I-Mx from the Param field to the B\_id field; moved I-Bufx from the B\_num field to the Sync field; moved I-Offset from the Offset\_2

field to the B\_num field. Added R-id in the S\_id field of both the Request\_Answer and Data operations. Removed the Opaque data.

- In PG6, set Param field to \* ; moved I-Mx from the Param field to the B\_id field; moved I-Bufx from the B\_num field to the Sync field; moved I-Offset from the Offset\_2 field to the B\_num field. Added R-id in the S\_id field of both the Request\_Answer and Data operations. Removed the Opaque data.
52. In 10, added the sentence reading "The scheme for logging multiple errors in a single operation is implementation dependent."
53. In 10.1, first paragraph, changed "...include:" to "...may include, or be the sum of:". In the paragraph after the bullets, changed "...operations)..." to "...operations) associated with Read and Write sequences...". In the last paragraph, changed "...shall be..." to "...may be...".
54. In table 8, changed the title from "...with mandatory retry" to "...with retry".
55. In 10.4, replaced the first part of the paragraph with "If an erroneous checksum is detected (see 8.3.3), then...".
56. In 10.7.2, changed "...does match..." to "...does not match...".
57. In 10.7.6, changed the maximum Blocksize from 2<sup>63</sup> to 2<sup>48</sup> bytes.
58. In annex A, changed the first bullet from "– CCI information..." to "– Connection control information (CCI), ...". In the second paragraph after the first set of bullets, changed "...connection control information (CCI)..." to "...the CCI...".
59. In A.1, paragraph after the first set of bullets, deleted "...and carried in a Request\_To\_Send operation (see Q.2)" from the end of the paragraph.
60. In Figures A.1 through A.6, changed the Schedule Header field names (D\_id, S\_id, Cksum, Op\_len, and Offset\_2), to match the Schedule Header layout changes in Figure 12.
61. In A.2, deleted the next to last paragraph (which was a duplicate of the previous paragraph). In the previous paragraph,

- added "Small STUs may be used to avoid having a large STU delay a Control operation." as the next to last sentence.
62. In A.3, added "It may be desirable to use small STUs to avoid having a large STU delay a Control operation." as the last sentence of the next to last paragraph.
  63. **In Figure A.3, moved the 16-bit offset to the top of the figure instead of following the Schedule Header.**
  64. **All of A.4 (ST over ATM), is new to the ST document, but it has been available and reviewed before.** Only the changes since the review at the January meeting are marked with highlights and margin bars.
  65. **In A.4.2, changed the second paragraph to note that 8 bytes of zero Fill are placed ahead of the ST Header, instead of having to limit the minimum number of bytes in an operation.**
  66. **In Figure A.5, added an 8-byte fill of all zeros ahead of the ST Header; modified the text in the "ST payload" to remove the "6 byte pad (*in Control Operations without Optional payload*)" text.**
  67. **All of A.5 (ST over Fibre Channel), is new to the ST document. It is based on Jerry Leitherer's prior proposal, which we have seen before, but the text and figure A.7 are still in need of close review.**
  68. In B.2, second paragraph, deleted the second sentence reading "A Request\_To\_Send\_Response will be received, either as a discrete message or as part of a Clear\_To\_Send."
  69. In B.3, deleted "Request\_To\_Send\_Response, either as a discrete operation or as part of a" from the second sentence of the second paragraph.



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**Foreword** (This foreword is not part of American National Standard X3.xxx-199x.)

This American National Standard specifies a data transfer protocol that uses small control messages to pre-arrange data movement. Buffers are allocated at each end before the data transmission, allowing full-rate, non-congesting data flow between the end devices. The control and data may use different physical media or may share a single physical medium. Procedures are provided for moving data over HIPPI and other media.

This document includes annexes which are informative and are not considered part of the standard.

Requests for interpretation, suggestions for improvement or addenda, or defect reports are welcome. They should be sent to the National Committee for Information Technology Standards, 1250 Eye Street, NW, Suite 200, Washington, DC 20005.

This standard was processed and approved for submittal to ANSI by NCITS. Committee approval of the standard does not necessarily imply that all committee members voted for approval. At the time it approved this standard, the NCITS had the following members:

(List of NCITS members to be included in the published standard by the ANSI Editor.)

Technical Committee T11 on Device Level Interfaces, which reviewed this standard, had the following participants:

(List of T11 Committee members, and other active participants, at the time the document is forwarded for public review, will be included by the Technical Editor.)

Task Group T11.1 on the High-Performance Parallel Interface, which developed this standard, had the following participants:

(List of T11.1 Task Group members, and active participants, at the time of document is forwarded for public review will be included by the Technical Editor.)

## Introduction

This American National Standard specifies a data transfer protocol that uses small control messages to pre-arrange data movement. Buffers are allocated at each end before the data transmission, allowing full-rate, non-congesting data flow between the end devices. The control and data may use different physical media or may share a single physical medium. Procedures are provided for moving data over HIPPI and other media.

Characteristics of a ST include:

- A hierarchy of data units (Scheduled Transfer Units (STUs), Blocks, and Transfers).
- Support for flow-controlled Block Read and Write sequences.
- Support for single Block Get and Put sequences.
- Support for Fetch and modify sequences.
- Parameters exchanged between end devices for Port selection, transfer identification, and operation validation.
- Features supporting efficient mapping between the issuer's and receiver's natural buffer sizes.
- Features supporting Block striping.
- Provisions for resending partial Transfers for error recovery.
- Mappings onto HIPPI-6400-PH, HIPPI-FP (for HIPPI-800 traffic), Fibre Channel, and Ethernet lower-layer protocols.

# American National Standard for Information Technology –

## Scheduled Transfer Protocol (ST)

### 1 Scope

This American National Standard specifies a data transfer protocol that uses small control messages to pre-arrange data movement. Buffers are allocated at each end before the data transmission, allowing full-rate, non-congesting data flow between the end devices. The control and data may use different physical media or may share a single physical medium. Procedures are provided for moving data over HIPPI and other media.

Specifications are included for:

- Virtual Connection setup and teardown;
- determining the number of operations the other end can accept;
- determining the buffer size of the other end;
- exchanging Key, Port, transfer identifiers, and buffer size values specific to the end nodes;
- determining a maximum size transmission unit that will not overrun receiver buffer boundaries;
- acknowledging partial transfers so that buffers can be reused;
- providing means for resending partial Transfers for error recovery; and
- terminating transfers in progress.

Note that some Scheduled Transfer Protocol implementations work best with in-order delivery by the LLP, which may not be available on all media.

### 2 Normative references

The following standards contain provisions which, through reference in the text, constitute provisions of this standard. At the time of publication, the editions indicated were valid. All standards are subject to revision, and parties to agreements based on this standard are encouraged to investigate the possibility of applying the most recent editions of the standards listed below.

Copies of the following documents can be obtained from ANSI: Approved ANSI standards, approved and draft international and regional standards (ISO, IEC, CEN/CENELEC, ITUT), and approved and draft foreign standards (including BSI, JIS, and DIN). For further information, contact ANSI Customer Service Department at 212-642-4900 (phone), 212-302-1286 (fax) or via the World Wide Web at <http://www.ansi.org>. Additional availability contact information is provided below as needed.

## 2.1 Approved references

ANSI X3.183-1991, *High-Performance Parallel Interface – Mechanical, Electrical, and Signalling Protocol Specification (HIPPI-PH)*

ANSI X3.210-1992, *High-Performance Parallel Interface – Framing Protocol (HIPPI-FP)*

ANSI/IEEE Std 802-1990, *IEEE Standards for Local and Metropolitan Area Networks: Overview and architecture (formerly known as IEEE Std 802.1A, Project 802: Local and Metropolitan Area Network Standard — Overview and Architecture)*.

ISO/IEC 8802-2:1989 (ANSI/IEEE Std 802.2-1989), *Information Processing Systems – Local Area Networks – Part 2: Logical link control*.

## 2.2 References under development

At the time of publication, the following referenced standards were still under development. For information on the current status of the document, or regarding availability, contact the relevant standards body or other organization as indicated. For information about obtaining copies of this document or for more information on the current status of the document, contact National Committee for Information Technology Standards, 1250 Eye Street, NW, Suite 200, Washington, DC 20005, 202-626-5746.

ANSI X3.xxx-199x, *High-Performance Parallel Interface – 6400 Mbit/s Physical Layer (HIPPI-6400-PH)*

## 3 Definitions and conventions

### 3.1 Definitions

For the purposes of this standard, the following definitions apply.

**3.1.1 atomic:** An indivisible operation or transaction, i.e. it updates all of its defined state variables before another operation can take place on the same variables.

**3.1.2 Block:** An ordered set of one or more

STUs within a Scheduled Transfer. (See figure 3 page 6, and 6.2.4 page 24.)

**3.1.3 Buffer Index (Bufx):** A 32-bit parameter identifying the starting address of a data buffer. (See 6.2.8 page 25.)

**3.1.4 Bufsize:** An end device's buffer size. (See 5.2.3 page 10.)

**3.1.5 connection control information (CCI):** Media-dependent information, e.g., physical-layer addresses, passed from the ULP for use by the physical layer below ST.

**3.1.6 Control Channel:** The logical channel that carries the Control operations.

**3.1.7 Control operation:** A control function consisting of a Schedule Header and an optional 32-byte payload. (See figure 4 page 6.)

**3.1.8 Data Channel:** The logical channel that carries the data payload.

**3.1.9 Data operation:** A data movement consisting of a Schedule Header and up to 2 gigabytes of user payload. (See figure 4 page 6.)

**3.1.10 Destination:** The end device that receives an operation or data.

**3.1.11 Get:** An operation to read data from a persistent memory region on a remote end device. (See 6.1.4.3 page 21.)

**3.1.12 FetchOp:** An atomic operation to read data from a persistent memory region on a remote end device and execute some function on the persistent memory location, e.g., increment. (See 6.1.4.4 page 22.)

**3.1.13 Initiator:** The end device that starts a sequence of operations. This is typically a host computer system, but may also be a non-transparent translator, bridge, or router.

**3.1.14 intermediate device:** A non-transparent device (e.g., translator, bridge, or router), between the end device that generates the data payload and the end device that receives and operates on the data payload.

**3.1.15 Key:** A local identifier used to select and validate operations. (See 5.2.2 page 10.)

**3.1.16 log:** The act of making a record of an event for later use.

**3.1.17 lower-layer protocol (LLP):** A protocol below the Scheduled Transfer Protocol, e.g., a

physical layer.

**3.1.18 Memory Index (Mx):** A parameter identifying an area of memory. (See 6.2.2 page 24).

**3.1.19 Offset:** A parameter specifying the data's starting point relative to the start of a Bufx. (See 6.2.8 page 25.)

**3.1.20 Opaque data:** Four bytes of Source ULP to Destination ULP peer-to-peer information carried in a Data operation's Schedule Header separately from the data payload. (See 6.2.10 page 26)

**3.1.21 operation:** The procedure defined by the parameters in a Schedule Header, and any payload associated with that Schedule Header (see figure 4 page 6). The code in the Schedule Header's "Op" field identifies the operation's name/function (see table 2 page 29).

**3.1.22 optional:** Characteristics that are not required by ST. However, if any optional characteristic is implemented, it shall be implemented as defined in ST.

**3.1.23 persistent:** Memory that is maintained for multiple Put, Get, and FetchOp operations. (See 6.1.4 page 18, and 6.2.12 page 26.)

**3.1.24 Port:** A logical connection within an end device. (See 5.2.1 page 9.)

**3.1.25 Put:** An operation to write data into a persistent memory region on a remote end device. (See 6.1.4.2 page 20.)

**3.1.26 Request For Comment (RFC):** RFC (Request For Comment) documents are working standards documents from the TCP/IP internetworking community. Copies of these documents are available from numerous electronic sources (e.g., <http://www.ietf.org>) or by writing to Internet Engineering Task Force (IETF) Secretariat, c/o Corporation for National Research Initiatives, 1895 Preston White Drive, Suite 100 Reston, VA 20191-5434, USA.

**3.1.27 Responder:** The end device that responds to the sequence of operations started by the Initiator. This is typically a host computer system, but may also be a non-transparent translator, bridge, or router.

**3.1.28 Scheduled Transfer:** An information transfer, normally used for bulk data movement, where the end devices prearrange the transfer

using the protocol defined in this standard.

**3.1.29 Scheduled Transfer Unit (STU):** The data payload portion of a Data operation. STUs are the basic components of Blocks and are the smallest units transferred. (See figure 3 page 6, and 6.2.7 page 25.)

**3.1.30 segment:** That portion of a Block covered by a single checksum (see 8.3.2 page 30).

**3.1.31 sequence:** An ordered group of operations providing a particular function, e.g., Read, Write, Get, etc., between an Initiator and a Responder. The roles of Initiator and Responder are constant for all operations in the sequence.

**3.1.32 Slot:** A space reserved for a Control operation, or the Schedule Header portion of an STU, in the end device. (See 5.2.5 page 10.)

**3.1.33 Source:** The end device that sends an operation or data.

**3.1.34 Sync:** A parameter used to synchronize the state across a Virtual Connection. (See 5.2.5 page 10.)

**3.1.35 Transfer:** An ordered set of one or more Blocks within a Scheduled Transfer. (See 4.2 page 5, and figure 3 page 6.)

**3.1.36 Universal LAN MAC address (ULA):** A logical address that uniquely identifies a Source or Destination. The ULA conforms to the 48-bit MAC address specified by the IEEE 802 Overview Standard.

**3.1.37 upper-layer protocol (ULP):** The protocol above ST. A ULP could be implemented in hardware or software, or could be distributed between the two.

**3.1.38 Virtual Connection:** A bi-directional logical connection used for Scheduled Transfers between two end devices. A Virtual Connection contains a logical Control Channel and one or more logical Data Channels in each direction. (See 5 page 8.)

### 3.2 Editorial conventions

A number of conditions, sequence parameters, events, states, or similar terms are printed with the first letter of each word in uppercase and the rest lowercase (e.g., Block, Transfer). Any lowercase uses of these words have the normal technical English meaning.

The word *shall*, when used in this American National standard, states a mandatory rule or requirement. The word *should*, when used in this standard, states a recommendation.

Multiword parameters and field names are joined with an underscore, e.g., D\_Port. A parameter associated with a particular end device uses a single letter prefix and a hyphen as a joiner, e.g., I-Port denoting the Initiator's Port.

All numbers are represented as unsigned integers.

Operations contained within <...> are conditional, and may not occur.

#### 3.2.1 Binary notation

Binary notation is used to represent relatively short fields. For example a two-bit field containing the binary value of 10 is shown in binary format as b'10'. An "x" in a bit position indicates a "don't care" value.

#### 3.2.2 Hexadecimal notation

Hexadecimal notation is used to represent some fields. For example a two-byte field containing a binary value of b'1100010000000011' is shown in hexadecimal format as x'C403'.

### 3.3 Acronyms and other abbreviations

<b>Ack</b>	acknowledge indication
<b>ATM</b>	Asynchronous Transfer Mode
<b>CCI</b>	connection control information associated with the Destination
<b>D_</b>	
<b>DSAP</b>	Destination Service Access Protocol
<b>FC</b>	Fibre Channel
<b>FTP</b>	File Transfer Protocol
<b>HIPPI</b>	High-Performance Parallel Interface
<b>I-</b>	Prefix for an Initiator's parameter
<b>id</b>	identifier
<b>IP</b>	Internet Protocol
<b>IEEE</b>	Institute of Electrical and Electronic Engineers
<b>LAN</b>	local area network
<b>LLC</b>	Logical Link Control
<b>LLP</b>	lower-layer protocol
<b>MAC</b>	Media Access Control
<b>R-</b>	Prefix for a Responder's parameter
<b>RFC</b>	Request For Comment
<b>S_</b>	associated with the Source
<b>SNAP</b>	SubNetwork Access Protocol
<b>SSAP</b>	Source Service Access Protocol
<b>ST</b>	Scheduled Transfer Protocol
<b>STU</b>	Scheduled Transfer Unit
<b>TCP</b>	Transmission Control Protocol
<b>UDP</b>	User Data Protocol
<b>ULA</b>	Universal LAN address
<b>ULP</b>	upper-layer protocol
<b>VC</b>	Virtual Channel

*Open Issue – Currently the document text does not use the VC acronym; should it? Note that we do use VC as the acronym for Virtual Channel in HIPPI-6400.*

## 4 System overview

This clause provides an overview of the structure, concepts, and mechanisms used in Scheduled Transfers. Figure 1 gives an example of Scheduled Transfers being used to communicate between a local end device and a remote end device over some physical media. Annex C describes the steps in a typical Scheduled Transfer. Figure 2 shows ST being used over different media.

### 4.1 Control Channels and Data Channels

Each end device shall have a Control Channel and one or more Data Channels. Control operations shall be exchanged over the Control Channel. Scheduled Transfer Units (STUs), i.e., data payload, shall be exchanged over the Data Channel(s). The information volume on the Data Channel(s) will probably be many times the volume on the Control Channel; hence the available bandwidths should be balanced accordingly. For best performance, the Control Channel should have low latency.

### 4.2 System model

Scheduled Transfers between end devices are pre-arranged to decrease computational overhead during the Transfer by allocating buffers at each end device. The bi-directional path between the end devices is called a Virtual Connection. The end devices can be either

Initiators or Responders. The Initiator is the end device that starts a Read, Write, request persistent memory region, Put, Get, or FetchOp sequence. The Responder is at the other end. The end device sending an operation is the Source and the end device receiving the operation is the Destination. The end device sending data is the data Source and the end device receiving the data is the data Destination.

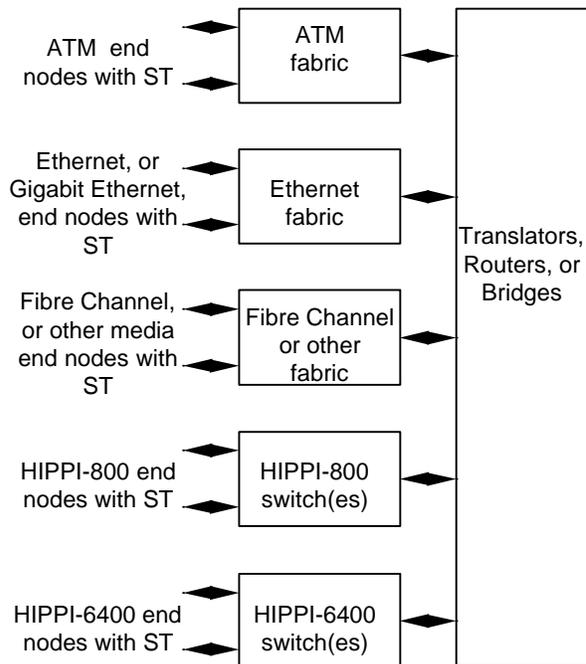


Figure 2 – ST over different media

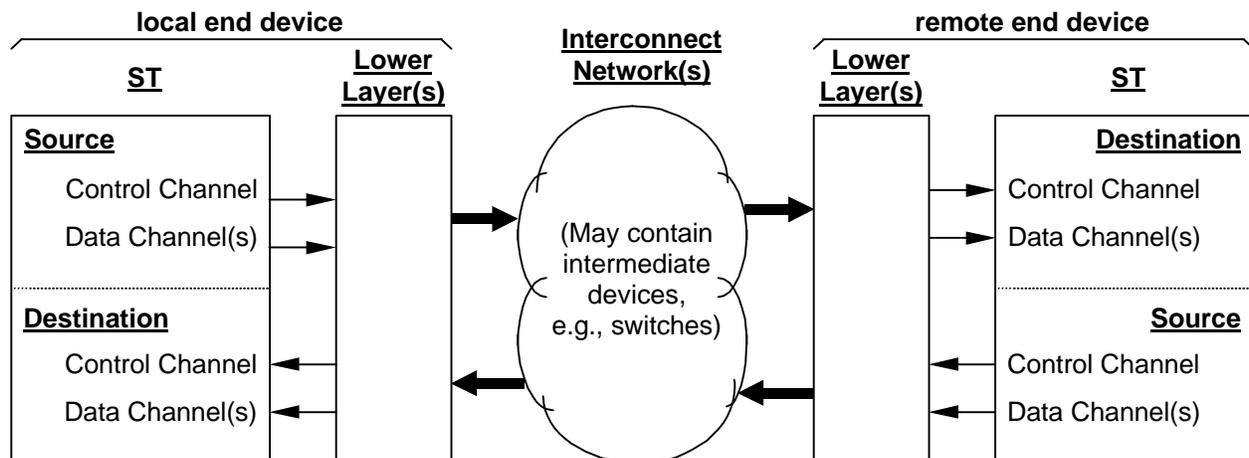
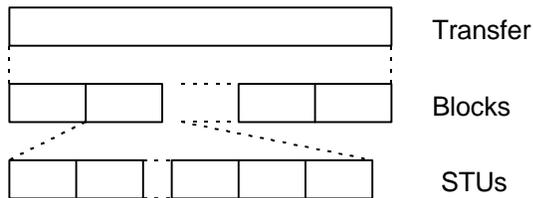


Figure 1 – System overview

Once the data Destination has indicated its ability to accept a Block, the Virtual Connection **end devices** should not become congested. In essence, the data Destination smoothly controls the flow. For comparison, without pre-arranging the buffers, the data Source would blindly send data into the interconnection network where it might have to wait for buffers to be assigned in the data Destination. On the down-side, Scheduled Transfers require additional Control operations and round-trip latency. Once established, a Virtual Connection may be used to carry multiple Read and Write Transfers, allocate multiple persistent memory regions, or execute multiple Put, Get, and FetchOp sequences. This Scheduled Transfer protocol does not handle network resource reservations.

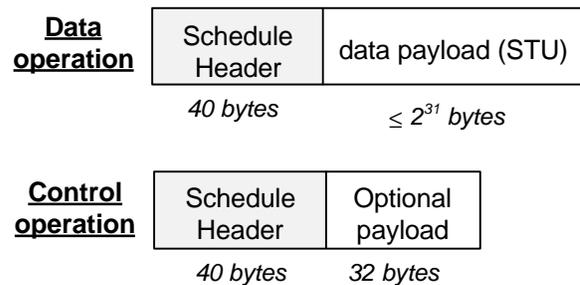
Multiple independent Write or Read sequences may be executed to move user data units, called Transfers, over Virtual Connections. As shown in figure 3, a Transfer is composed of one or more Blocks, and Blocks are composed of one or more STUs. The Scheduled Transfer protocol shall package the Transfer in Blocks and STUs for delivery using a lower layer protocol (LLP) and media. The Blocks for transmission and retransmission are enabled (flow-controlled), with Clear\_To\_Send operations.



**Figure 3 – User data hierarchy**

Multiple Put, Get, and FetchOp sequences may also be executed to move user data to/from a persistent memory region. A persistent memory region is similar to a region of memory exposed for the transfer of a Block (i.e., a starting Bufx and Offset), but is different in that the persistent memory region can be used for multiple Put, Get, or FetchOp sequences. The data unit exchanged in a Put, Get, or FetchOp sequence, without explicit flow control, is a Block (but the Block may be smaller than the persistent memory region).

As shown in figure 4, an STU shall be the data payload portion of a Data operation. A Data operation shall consist of a 40-byte Schedule Header (**with Op = Data**), and an STU of up to 2 gigabytes ( $2^{31}$  bytes). A Control operation shall consist of a 40-byte Schedule Header (**with Op ≠ Data**), and may contain an additional 32 bytes of optional payload. The optional payload can be used by a ULP entity for any purpose, e.g., passing file names. **If a received operation is not a legal length, then it shall be discarded.**



**Figure 4 – Transmission units**

Figure 5 shows a model of a local end device's Destination side data structures. An end device's Source side may be similar.

As Control operations and Data operations are received, the Schedule Header of each is placed in the Schedule Header queue for execution. State information about the number of empty Slots in the queue is available to the other end so that it can avoid overrunning the queue.

The Virtual Connection Descriptor (selected and validated by the ordered set remote-Port, local-Port, and local-Key), contains:

- static parameters identifying the Virtual Connection from the view of both the remote end device and local end device (the top portion of the Virtual Connection Descriptor box in figure 5). Since the values assigned to local-Port and local-Key are determined by the local end device, it is left up to the local end device as to which parameters to use to select and which to use to validate;
- current state information about the number of empty "Slots" for Schedule Headers associated

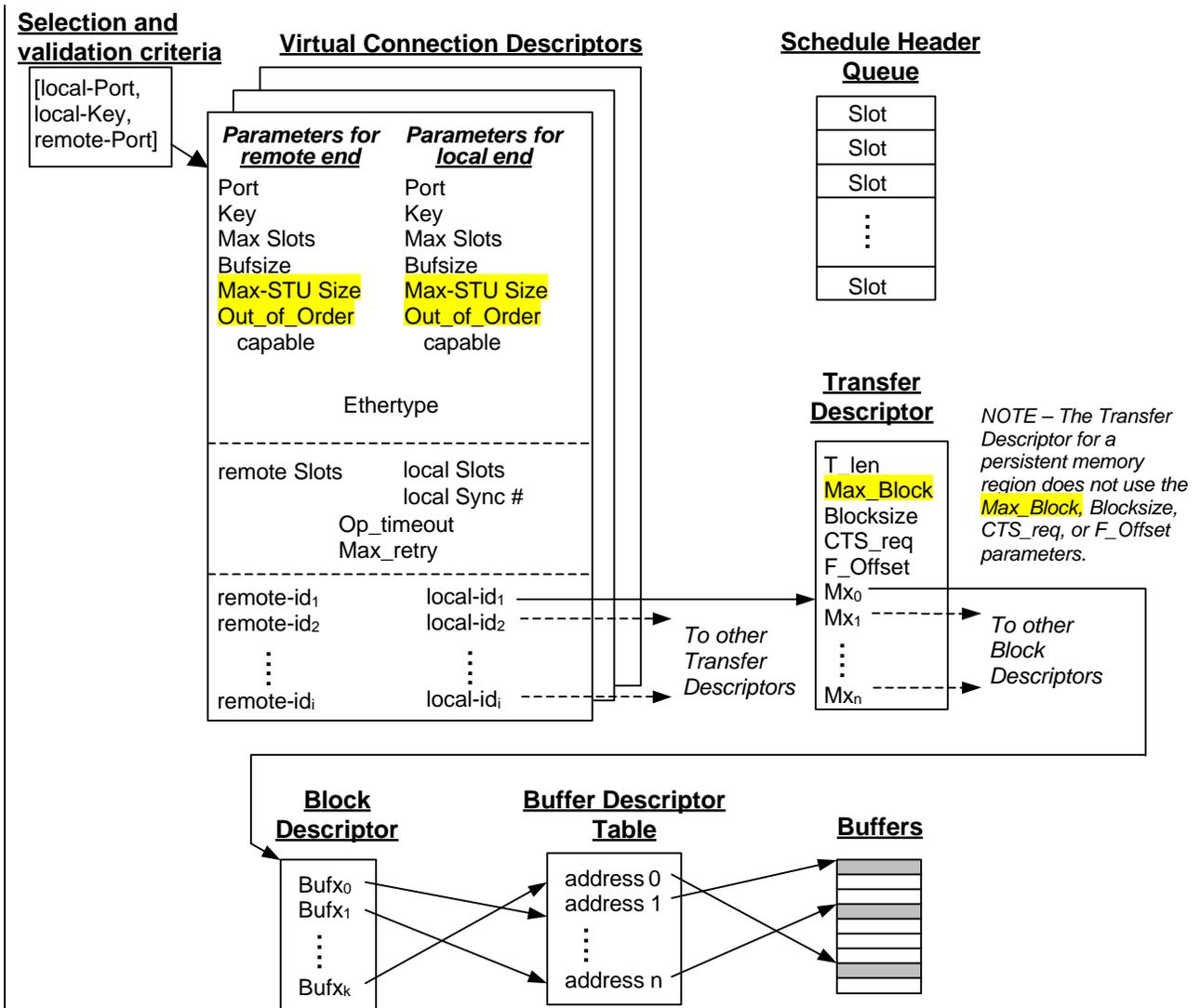
with this Virtual Connection, and Retry and Timeout parameters;

- identifiers for each of the Virtual Connection's Transfers or persistent memory regions.

A Transfer Descriptor for each Transfer, contains the Transfer length (T\_len, in bytes), the Block size (in bytes), and includes references (Mx<sub>n</sub>), to Block Descriptors. There is only one Block Descriptor for a persistent memory region. The Block Descriptors identify the set of contiguous Buffer Index (Bufx) values assigned to the Block

or persistent memory region. And finally, the Buffer Descriptor Table provides a base memory address for each Bufx.

In an effort to achieve maximum transfer rates and efficiency, the receiver's job is made as easy as possible, even at the expense of the transmit side. It is expected that after validating an operation in the receiving end, only a single lookup will be needed to derive the absolute memory address and correctly place the data.



NOTE - Additional parameters may be required for control of lower layers. (See annex A page 44.)

Figure 5 - A Destination side data structure model

## 5 Connection management

### 5.1 Connection management sequences

The connection management sequences are shown in figure 6 and detailed in table 4 page 34. An Op code (see 8.1 page 29), and an optional checksum (see 8.3 page 30), are part of every operation but are not mentioned further in this clause. Parameters transmitted as zeros, and not checked at the receiver, are marked in the tables as \*, and are not mentioned further in this clause.

The end device that starts a sequence is called the Initiator, and the other end device is called the Responder. The label of an end device as Initiator or Responder remains constant within a sequence. An "I-" prefix indicates that a parameter is associated with the Initiator. An "R-" prefix indicates that a parameter is associated with the Responder.

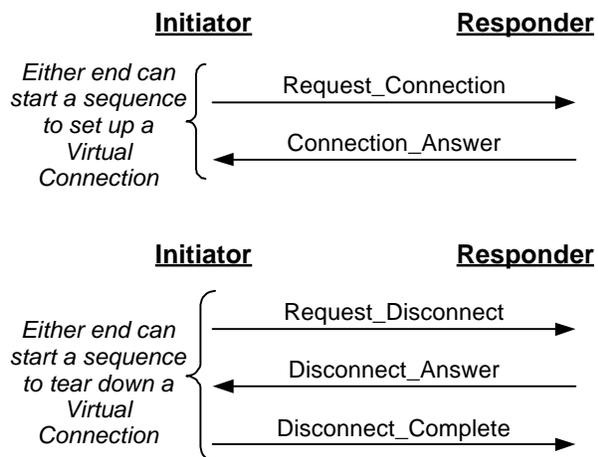


Figure 6 – Connection management example

#### 5.1.1 Virtual Connection setup

A sequence consisting of a Request\_Connection operation and a Connection\_Answer operation shall be used to construct a symmetric Virtual Connection between two end devices. Either end device can initiate the Virtual Connection setup sequence, and sequences that follow need not use the same Initiator and Responder. (See table 4 page 34, C1.)

**Request\_Connection** shall be issued by the Initiator to establish a Virtual Connection. Parameters are passed to inform the remote end of the Initiator's capabilities and preferences.

- Flags (see 8.2 page 29): F bits specify the Initiator's support for persistent operations and big/little endian ULP architecture. I = 1 specifies that an interrupt shall be generated at the Responder. O = 1 specifies that the Initiator can send and receive Blocks in any order (see 6.2.6 page 25).

- I-Slots specifies the initial number of Slots available in the Initiator for this Virtual Connection (see 5.2.5 page 10).

- R-Port specifies a "well-known Port" or other value to select an upper-layer protocol or service at the Responder (see 5.2.1 page 9).

- I-Port assigns the Initiator's Port value (see 5.2.1 page 9).

- EtherType identifies the protocol associated with this Virtual Connection (see 5.2.6 page 11).

- I-Bufsize specifies the Initiator's buffer size (see 5.2.3 page 10).

- I-Key assigns the Initiator's Key (see 5.2.2 page 10).

- I-Max\_STU specifies the maximum size STU the Initiator is prepared to receive (see 5.2.4 page 10).

**Connection\_Answer** shall be issued by the Responder upon receipt of a Request\_Connection. The Responder may either reject the request, or reply with parameters to inform the Initiator of the Responder's capabilities and preferences. If accepted, a Virtual Connection will have been established for use with subsequent sequences.

- Flags (see 8.2 page 29): F bits specify the Responder's support for persistent operations and big/little endian ULP architecture. I = 1 specifies that an interrupt shall be generated at the Initiator. O = 1 specifies that the Responder can send and receive Blocks in any order (see 6.2.6 page 25). R = 1 specifies that the Responder has rejected this Virtual Connection.

- R-Slots specifies the number of Slots available in the Responder for this Virtual Connection (see 5.2.5 page 10).
- I-Port echoes the Initiator's Port value (see 5.2.1 page 9).
- R-Port assigns the Responder's Port value, which may not be the same as the R-Port value in the Request\_Connection (see 5.2.1 page 9).
- I-Key echoes the Initiator's Key (see 5.2.2 page 10).
- R-Bufsize specifies the Responder's buffer size (see 5.2.3 page 10).
- R-Key assigns the Responder's Key (see 5.2.2 page 10).
- R-Max\_STU specifies the maximum size STU the Responder is prepared to receive (see 5.2.4 page 10).

The parameters assigned and specified during setup shall apply for the life of the Virtual Connection. Once established, the Virtual Connection is selected and validated as shown in figure 5 by the ordered set "remote-Port", "local-Port", and "local-Key".

### 5.1.2 Virtual Connection teardown

Either end device of a Virtual Connection can initiate a three-way handshake sequence to disconnect, or tear down, the Virtual Connection. (See table 4 page 34, C2.)

**Request\_Disconnect** shall be issued by the Initiator to tear down a Virtual Connection and release the resources assigned to the Virtual Connection. A Request\_Disconnect should only be issued when the Transfers are complete or appear to be stalled. Request\_Disconnect is the first step in the three-way teardown handshake that decreases timeout dependency for releasing resources.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.
- R-Port, I-Port, R-Key, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

**Disconnect\_Answer** shall be issued by the Responder to acknowledge receipt of a Request\_Disconnect. The Disconnect\_Answer issuer may release any buffers associated with this Virtual Connection, but shall retain (for at least twice the Op\_timeout period), the Port and Key values for use in further Disconnect operations. This delay allows for lost or damaged teardown operations to be re-issued. Disconnect\_Answer is the second step in the three-way teardown handshake.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.
- I-Port, R-Port, I-Key, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

**Disconnect\_Complete** shall be issued by the Initiator to complete the three-way teardown handshake, acknowledging that the actions associated with a Request\_Disconnect have been completed. After waiting an interval of at least twice the Op\_timeout period, the Initiator shall release the Virtual Connection's Port and Key values. This delay allows for lost or damaged teardown operations to be re-issued. Disconnect\_Complete is the last step in the three-way teardown handshake.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.
- R-Port, I-Port, R-Key, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

## 5.2 Connection management parameters

### 5.2.1 Ports

Ports identify upper-layer entities within an end device. The Port values shall be assigned by the local end device and have no meaning on the remote end device. For example, when the local end device requests a Virtual Connection, the local end device shall select the value for local-Port and shall send it to remote end device in the Request\_Connection operation. The remote end device shall store the value as remote-Port and shall return it to the local end device in every operation over this Virtual Connection. Likewise,

the remote end device shall select its Port value.

An exception to this is necessary when Virtual Connections are set up. The Destination Port (D\_Port) value in a Request\_Connection operation shall be a "well-known Port" or other value that selects an upper layer protocol or service.

The interpretation of Port numbers for establishing Virtual Connections shall be as defined by the Internet Assigned Numbers Authority (IANA) and as published at <http://www.isi.edu/div7/infra/iana.html>. For example, if an FTP application were to use ST rather than TCP, then it would send a Request\_Connection operation to Port 20 (the FTP Port) at the remote end system by specifying Port 20 in the D\_Port field. The subsequent Connection\_Answer operation would return a potentially different Port number in the S\_Port field to use in subsequent data transfer operations (see 6 page 11).

If the incoming local-Port, remote-Port, and local-Key values are not a valid combination, then the operation shall not be executed (see 10.6.1 page 41).

### 5.2.2 Keys

Like the Ports (see 5.2.1 page 9), each end device shall select its own 32-bit Key for use on the Virtual Connection. The Key shall be assigned by the local end device and have no meaning on the remote end device. The locally assigned Key value for each new Virtual Connection to a specific host shall not duplicate a local Key value to this same host in use within the last 10 minutes.

If the incoming local-Port, remote-Port, and local-Key values are not a valid combination, then the operation shall not be executed (see 10.6.1 page 41).

### 5.2.3 Buffer size (Bufsize)

Each end shall define its receiving buffer size, in bytes. Buffer sizes may be the same as host page sizes. The buffer sizes shall be  $\geq 256$  bytes and shall be an integral power of two, i.e.,  $2^{\text{Bufsize}}$  where  $8 \leq \text{Bufsize} \leq 32$ . Note that the buffer

sizes in each direction may be different.

Transmitting buffer sizes are not exchanged, and may be different from the receiving buffer sizes, except that Get and FetchOp operations require that the transmit and receive buffers be the same size (see 6.2.12 page 26).

### 5.2.4 Max\_STU size

The Max\_STU size, exchanged during Virtual Connection setup, establishes the maximum data payload size of an STU. Each end device declares the Max\_STU size it is prepared to receive. The Max\_STU size shall be no larger than its Bufsize. Intermediate devices with smaller buffer sizes may lower this value. Note that the Max\_STU size in each direction may be different.

Additionally, an STU's maximum data payload size shall be  $\geq 256$  bytes and an integral power of two i.e.,  $2^{\text{Max\_STU}}$  where  $8 \leq \text{Max\_STU} \leq 32$ .

*EDITOR'S NOTE – The original 5.2.5 "Maximum Block size (Max\_Block)" was moved to 6.2.5. This moves the use of this parameter from the Virtual Connection setup to the Transfer setup.*

### 5.2.5 Slots

The term Slot denotes memory at a Destination, associated with a specific Virtual Connection, reserved for storing the Schedule Header of an incoming operation. Each operation arriving at a Destination consumes one Slot, except Request\_Connection operations (see 5.1.1 page 8), or Data operations which have Silent = 1 (see 8.2 page 29). A Source shall control the flow of operations by sending no more operations than there are Slots available at the Destination for this Virtual Connection. Any operations that are sent in excess of the number of available Slots may be discarded by the Destination (see 10.6.2 page 41).

In order to avoid potential deadlocks that can happen if a Source consumes all of its allocated slots at the Destination, the Source shall never consume all of its slots with data movement operations. Instead, the Source shall hold at least one Slot in reserve for possible use for an End, Request\_State, Request\_State\_Response, or Request\_Disconnect sequence.

An end device learns the initial number of Slots available (Slots value) at the remote end device during the Virtual Connection setup (see 5.1.1 page 8). A received Slots value of x'FFFF' indicates that the remote end does not implement Slot accounting.

Later, an end device obtains the current Slots value for a specific Virtual Connection by reading the Slots parameter in a received Request\_State\_Response. An end device may solicit a Request\_State\_Response from the remote end by one of two methods: by setting the Send\_State flag in the Schedule Header of a Data operation, or by sending a Request\_State operation. A received Slots value of x'FFFF' indicates that the remote end device does not implement Slot accounting or cannot supply an update to the Slots value.

NOTE – Slot accounting may not be needed when the maximum number of Control operations is otherwise bounded or where dropped operations are acceptable.

The received Slot value is a snapshot of the number of Slots available at the remote end device for the specified Virtual Connection when the remote end device received the soliciting operation. The local end device may continue to send operations after soliciting a Request\_State\_Response and may also solicit multiple responses before receiving a reply. The lower bound on the number of available Slots at the remote end device is determined by the local end device which adjusts its vision of the number of Slots to account for outstanding operations. The adjustment consists of subtracting the number of Slot-consuming operations sent by the local end device from the number of Slots indicated in the received Request\_State\_Response operation after a Request\_State\_Response solicitation.

### 5.2.6 EtherType

EtherType parameter values shall be as assigned in the current "Assigned Numbers" RFC, e.g., RFC 1700 (see <http://www.iana.org/iana/>). The EtherType parameter value in a Request\_Connection operation shall identify the protocol associated with this ST Virtual Connection. For example, if ST is used to encapsulate TCP/IP, then this EtherType would

be x'0800'. If ST is being used to encapsulate legacy HIPPI-FP data, then this EtherType would be x'8180'. EtherType = x'0000' means no further encapsulation.

The other use of EtherType is in the IEEE 802.2 LLC/SNAP header of an LLP, where EtherType = x'8181' specifies that the LLP protocol is carrying ST information. This EtherType parameter may be supplied to an LLP as part of the CCI (see annex A page 44). For an example, see HIPPI-6400-PH.

## 6 Data movement

### 6.1 Data movement sequences

The data movement sequences are detailed in tables 5–8, page 36. An Op code (see 8.1 page 29), and an optional checksum (see 8.3 page 30), are part of every operation but are not mentioned further in this clause. Parameters transmitted as zeros, and not checked at the receiver, are marked in the tables as \*, and are not mentioned further in this clause.

The end device that starts a sequence is called the Initiator, and the other end device is called the Responder. The label of an end device as Initiator or Responder remains constant within a sequence. An "I-" prefix indicates that a parameter is associated with the Initiator. An "R-" prefix indicates that a parameter is associated with the Responder.

The data movement sequences rely on having a Virtual Connection established (see 5 page 8).

#### 6.1.1 Common sequences

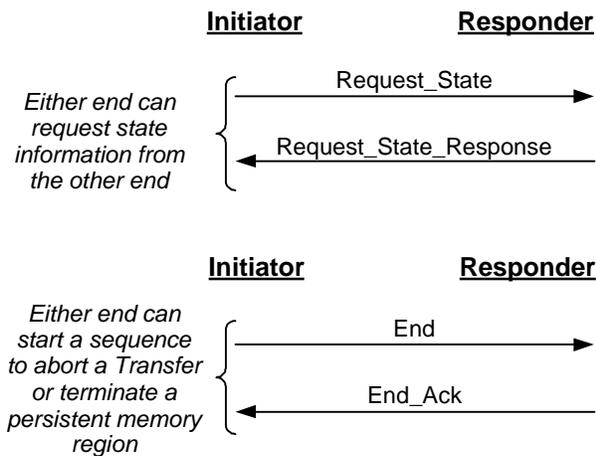
The Request\_State and End sequences, as shown in the figure 7 examples, and detailed in table 5 (see page 34), are available for use with other data movement sequences.

Request\_State sequences are used to find one or more pieces of remote end information:

- Slot state – number of available Slots for a specific Virtual Connection (see 5.2.5 page 10);

- Transfer state – same as Slot state, plus the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly (see 6.2.4 page 24);
- Block state – same as Transfer state, plus whether the specified Block was received correctly (see 6.2.4 page 24).

End sequences are used to abort a Transfer currently in progress, or terminate a persistent memory region, from either end device.



**Figure 7 – Common sequence examples**

### 6.1.1.1 Request Slot state

Request Slot state may be initiated by either end device to determine the number of available Slots at the other end device for this Virtual Connection. (See table 5 page 34, Com1.)

**Request\_State** may be issued by the Initiator to request the Responder's view of its current number of available Slots in the Responder for this Virtual Connection.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- Sync assigns the Initiator's Sync value (see 6.2.9 page 25).

- D\_id field = x'FFFF', i.e., we are only requesting Slot accounting information (see 6.2.1 page 23).

**Request\_State\_Response** shall be issued by the Responder in response to the Request\_State operation above. In this Request\_State\_Response, the Responder specifies its view of the number of currently available Slots in the Responder for this Virtual Connection.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- R-Slots specifies the Responder's number of currently available Slots for this Virtual Connection (see 5.2.5 page 10).
- Sync echoes the Initiator's Sync value (see 6.2.9 page 25).
- D\_id field echoes the D\_id field (see 6.2.1 page 23).

### 6.1.1.2 Request Transfer state

Request Transfer state may be initiated by the data Source to determine the general status of a Transfer and to get the number of Slots currently available at the data Destination for this Virtual Connection. (See table 5 page 34, Com2.)

**Request\_State** may be issued by the Initiator to request the Responder's view of its current number of available Slots in the Responder for this Virtual Connection and the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- Sync assigns the Initiator's Sync value (see 6.2.9 page 25).

- B\_num field = x'FFFFFFFF', i.e., we are not asking about a specific Block (see 6.2.4 page 24).
- R-id specifies the Responder's **sequence identifier**, i.e., asks about a specific Transfer (see 6.2.1 page 23).
- I-id specifies the Initiator's **sequence identifier** (see 6.2.1 page 23).

**Request\_State\_Response** shall be issued by the Responder in response to the Request\_State operation above. In this Request\_State Response, the Responder specifies its view of the number of currently available Slots in the Responder for Schedule Headers associated with this Virtual Connection, and the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.
- R-Slots specifies the Responder's number of currently available Slots for this Virtual Connection (see 5.2.5 page 10).
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- B\_seq specifies the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly (see 6.2.4 page 24).
- Sync echoes the Initiator's Sync value (see 6.2.9 page 25).
- B\_num field = x'FFFFFFFF' (see 6.2.4 page 24).
- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).
- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).

### 6.1.1.3 Request Block state

Request Block state may be initiated by the data Source to determine if the specified Block was received correctly, the general status of the Transfer, and the number of Slots available at the data Destination for this Virtual Connection. (See table 5 page 34, Com3.)

**Request\_State** may be issued by the Initiator to request the Responder's view of its current number of available Slots in the Responder for this Virtual Connection, the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly, and the status of a specific Block.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- Sync assigns the Initiator's Sync value (see 6.2.9 page 25).
- B\_num asks about a specific Block (see 6.2.4 page 24).
- R-id specifies the Responder's **sequence identifier**, i.e., asks about a specific Transfer (see 6.2.1 page 23).
- I-id specifies the Initiator's **sequence identifier** (see 6.2.1 page 23).

**Request\_State\_Response** shall be issued by the Responder in response to either: the Request\_State operation above, or to a Data operation with Send\_State = 1. In this Request\_State Response, the Responder specifies its view of: the number of currently available Slots in the Responder for Schedule Headers for the specified Virtual Connection, the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly, and if the Block specified (B\_num in the Request\_State or Data operation being responded to), was received correctly.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.
- R-Slots specifies the Responder's number of currently available Slots for this Virtual Connection (see 5.2.5 page 10).
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- B\_seq specifies the highest numbered Block of the specified Transfer received correctly

where all lower numbered Blocks are also received correctly (see 6.2.4 page 24).

- Sync echoes the Initiator's Sync value (see 6.2.9 page 25).

- B\_num indicates if the specified Block was received correctly (see 6.2.4 page 24).

- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).

- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).

#### 6.1.1.4 End sequence

End sequences allow either end device of the Virtual Connection to terminate a Transfer before it has completed, or to terminate a Transfer of unlimited size, or to terminate a persistent memory region. The end device receiving an End operation shall stop sending Control operations (other than End\_Ack), and STUs associated with this Transfer. An End kills a Transfer, but shall not affect the Virtual Connection. (See table 5 page 34, Com4.)

**End** may be issued by the Initiator.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.

- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- R-id specifies the Responder's **sequence identifier** (see 6.2.1 page 23).

- I-id specifies the Initiator's **sequence identifier** (see 6.2.1 page 23).

**End\_Ack** shall be issued by the Responder to confirm that the End operation has been seen and acted on, i.e., acknowledgement that the Transfer has been terminated.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).

- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).

#### 6.1.2 Write sequence

A Write sequence, as shown in the figure 8 example and detailed in table 6 (see page 36), moves a Transfer, which contains one or more Blocks, from an Initiator to a Responder. A Virtual Connection shall exist before a Write sequence is initiated. Multiple Write sequences can be active at one time in both directions on a single Virtual Connection. (See table 6 page 36, W1 through W4.)

**Request\_To\_Send** may be issued by the Initiator to request that space be exposed in the Responder for a data Transfer from the Initiator to the Responder.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder. D bits specify the Data Channel to be used.

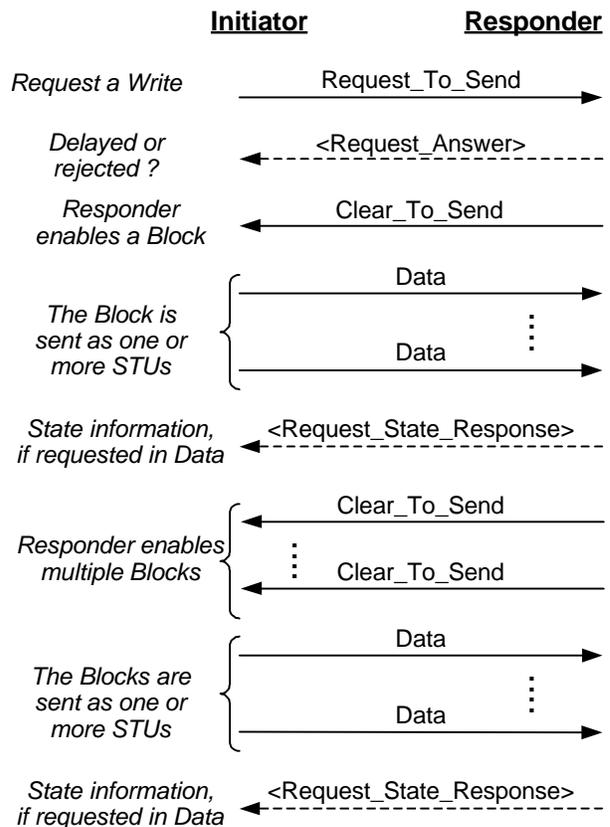


Figure 8 – Write example

- CTS\_req specifies the number of outstanding Blocks that the Initiator would like to see exposed at any given time (see 6.2.11 page 26).

- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- Max\_Block specifies the maximum Block size that the Initiator would like to send for this Transfer (see 6.2.5 page 24).

- T\_len assigns the Transfer length (see 6.2.3 page 24).

- I-id assigns the Initiator's sequence identifier (see 6.2.1 page 23).

**Request\_Answer** may be issued by the Responder to reject or pause the Request\_To\_Send. If rejected, then this is the end of the Write sequence.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator. R = 1 specifies that the Request\_To\_Send has been rejected. R = 0 specifies that the Request\_To\_Send has been recognized; final action is pending (i.e., a Clear\_To\_Send or Request\_Answer with R = 1).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- I-id echoes the Initiator's sequence identifier (see 6.2.1 page 23).

**Clear\_To\_Send** operations (one for each Block of the Transfer), shall be issued by the Responder to expose a non-persistent memory region to receive subsequent Data operations. One Block per Clear\_To\_Send is exposed for a single use.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.

- Blocksize assigns the Block size for this Block (see 6.2.6 page 25).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- R-Mx assigns the Responder's Memory Index for this Block (see 6.2.2 page 24).

- R-Bufx assigns the Responder's first Buffer Index for this Block (see 6.2.8 page 25).

- R-Offset assigns the Responder's initial Offset for this Block (see 6.2.8 page 25).

- F\_Offset specifies the initial Offset for the Transfer (see 6.2.8 page 25).

- B\_num specifies the Block number for this Block (see 6.2.4 page 24).

- I-id echoes the Initiator's sequence identifier (see 6.2.1 page 23).

- R-id assigns the Responder's sequence identifier (see 6.2.1 page 23).

**Data** operations (one for each STU of the Block), shall be issued by the Initiator to send the Block from the data Source (the Initiator), to the data Destination (the Responder). The data Destination shall place the STU data in the memory area pointed to by the Bufx and Offset parameters. The data Destination shall only accept data into pre-allocated memory regions. The data Destination is responsible for ensuring that all of the Blocks of a Transfer are received (see 10.7.8 page 42).

- Flags (see 8.2 page 29): T = 1 specifies that the data shall be delivered silently. I = 1 specifies that an interrupt shall be generated at the Responder. S = 1 specifies that the Responder shall reply with a Request\_State\_Response upon successful receipt of this STU. L = 1 marks the last STU of the Block. D bits echo the Data Channel assignment.

- STU\_num specifies the number for this STU (see 6.2.7 page 25).

- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- R-Mx echoes the Responder's Memory Index (see 6.2.2 page 24).

- R-Bufx assigns the Responder's Buffer Index for this STU (see 6.2.8 page 25). If this is the first STU of the Block, then R-Bufx echoes the R-Bufx value in the Clear\_To\_Send operation.

- R-Offset assigns the Responder's Offset for this STU (see 6.2.8 page 25). If this is the first STU of the Block, then R-Offset echoes the R-Offset in the Clear\_To\_Send operation.
- Sync assigns the Initiator's Sync value (see 6.2.9 page 25).
- B\_num echoes the Block number for this Block (see 6.2.4 page 24).
- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).
- Opaque contains the Opaque data for this STU (see 6.2.10 page 26).

**Request\_State\_Response** shall be issued by the Responder if  $S = 1$  in the previous Data operation.

- Flags (see 8.2 page 29):  $I = 1$  specifies that an interrupt shall be generated at the Initiator.
- R-Slots specifies the number of available Slots in the Responder for this Virtual Connection (see 5.2.5 page 10).
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- B\_seq specifies the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly (see 6.2.4 page 24).
- Sync echoes the Initiator's Sync value (see 6.2.9 page 25).
- B\_num indicates if the Block specified in the Data operation with  $S = 1$  was received correctly (see 6.2.4 page 24).
- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).
- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).

An **End** operation may be issued by either end device to abort the Transfer (the Write sequence), or terminate an unlimited size Transfer. (See 6.1.1.4 page 14, and table 5 page 34, Com4.)

A **Request\_State** operation may be issued by the Responder to determine the number of available Slots in the Initiator for this Virtual Connection (to know how many Clear\_To\_Sends the Responder

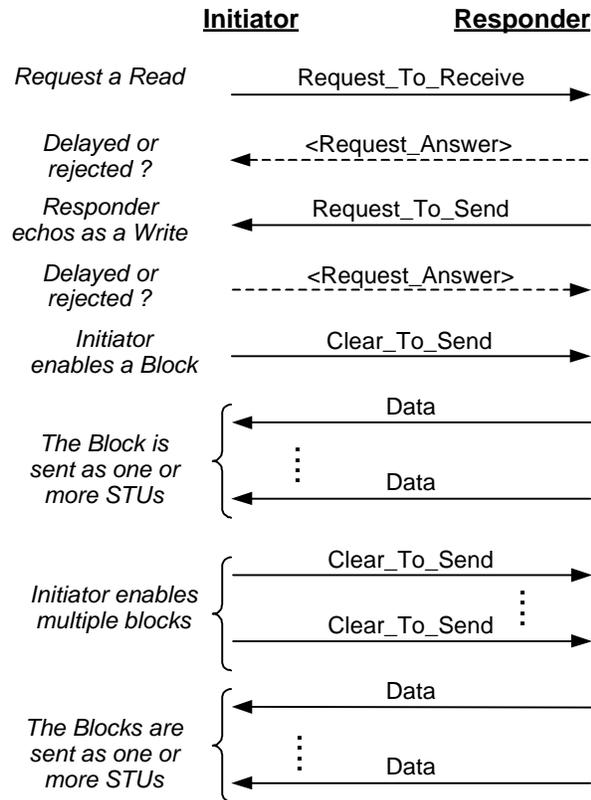
can issue). (See 6.1.1.1 page 12, and table 5 page 34, Com1.)

### 6.1.3 Read sequence

A Read sequence, as shown in the figure 9 example and detailed in table 7 (see page 36), moves a Transfer, which contains one or more Blocks, from a Responder to the Initiator. A Virtual Connection shall exist before a Read sequence is initiated. Multiple Read sequences can be active at one time in both directions on a single Virtual Connection. (See table 7 page 36, R1 through R4.)

**Request\_To\_Receive** may be issued by the Initiator to request a data Transfer from the Responder to the Initiator. The Responder shall echo the Request\_To\_Receive's parameters back in a Request\_To\_Send operation.

- Flags (see 8.2 page 29):  $I = 1$  specifies that an interrupt shall be generated at the Responder. D bits specify the Data Channel to be used.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- T\_len assigns the Transfer length (see 6.2.3 page 24).
- I-id assigns the Initiator's **sequence identifier** (see 6.2.1 page 23).



**Figure 9 – Read example**

**Request\_Answer** may be issued by the Responder to reject or pause the Request\_To\_Receive. If rejected, then this is the end of the Read sequence.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator. R = 1 specifies that the Request\_To\_Receive has been rejected. R = 0 specifies that the Request\_To\_Receive has been recognized; final action is pending (i.e., a Request\_To\_Send or Request\_Answer with R = 1).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- I-id echoes the Initiator's sequence identifier (see 6.2.1 page 23).

**Request\_To\_Send** shall be issued by the Responder to request that space be exposed in the Initiator for a data Transfer from the

Responder (the data Source), to the Initiator, (the data Destination).

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator. D bits echo the Data Channel to be used.

- CTS\_req specifies the number of outstanding Blocks that the Responder would like to see exposed at any given time (see 6.2.11 page 26).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- Max\_Block specifies the maximum Block size that the Responder would like to send for this Transfer (see 6.2.5 page 24).

- T\_len echoes the Transfer length (see 6.2.3 page 24).

- I-id echoes the Initiator's sequence identifier (see 6.2.1 page 23).

- R-id assigns the Responder's sequence identifier (see 6.2.1 page 23).

**Request\_Answer** may be issued by the Initiator to reject or pause the Request\_To\_Send. If rejected, then this is the end of the Read sequence.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder. R = 1 specifies that the Request\_To\_Send has been rejected. R = 0 specifies that the Request\_To\_Send has been recognized; final action is pending (i.e., a Clear\_To\_Send or Request\_Answer with R = 1).

- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).

**Clear\_To\_Send** operations (one for each Block of the Transfer), shall be issued by the Initiator to expose a non-persistent memory region to receive subsequent Data operations. One Block per Clear\_To\_Send is exposed for a single use.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder.

- Blocksize assigns the Block size for this Block (see 6.2.6 page 25).
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- I-Mx assigns the Initiator's Memory Index for this Block (see 6.2.2 page 24).
- I-Bufx assigns the Initiator's first Buffer Index for this Block (see 6.2.8 page 25).
- I-Offset assigns the Initiator's initial Offset for this Block (see 6.2.8 page 25).
- F\_Offset specifies the initial Offset for the Transfer (see 6.2.8 page 25).
- B\_num specifies the Block number for this Block (see 6.2.4 page 24).
- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).
- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).

**Data** operations (one for each STU of the Block), shall be issued by the Responder to send the Block from the data Source (the Responder), to the data Destination (the Initiator). The data Destination shall place the STU data in the memory area pointed to by the Bufx and Offset parameters. The data Destination shall only accept data into pre-allocated memory regions. The data Destination is responsible for ensuring that all of the Blocks of a Transfer are received (see 10.7.8 page 42).

- Flags (see 8.2 page 29): T = 1 specifies that the data shall be delivered silently. I = 1 specifies that an interrupt shall be generated at the Initiator. S = 1 specifies that the Initiator shall reply with a Request\_State\_Response upon successful receipt of this STU. L = 1 marks the last STU of the Block. D bits echo the Data Channel assignment.
- STU\_num specifies the number for this STU (see 6.2.7 page 25).
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- I-Mx echoes the Initiator's Memory Index (see 6.2.2 page 24).

- I-Bufx assigns the Initiator's Buffer Index for this STU (see 6.2.8 page 25). If this is the first STU of the Block, then I-Bufx echoes the I-Bufx value in the Clear\_To\_Send operation.
- I-Offset assigns the Initiator's Offset for this STU (see 6.2.8 page 25). If this is the first STU of the Block, then I-Offset echoes the I-Offset in the Clear\_To\_Send operation.
- Sync assigns the Responder's Sync value (see 6.2.9 page 25).
- B\_num echoes the Block number for this Block (see 6.2.4 page 24).
- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).

An **End** operation may be issued by either end device to abort the Transfer (the Read sequence), or terminate an unlimited size Transfer. (See 6.1.1.4 page 14, and table 5 page 34, Com4.)

A **Request\_State** operation may be issued by the Initiator to determine the number of available Slots in the Responder for this Virtual Connection (to know how many Clear\_To\_Sends the Initiator can issue). (See 6.1.1.1 page 12, and table 5 page 34, Com1.)

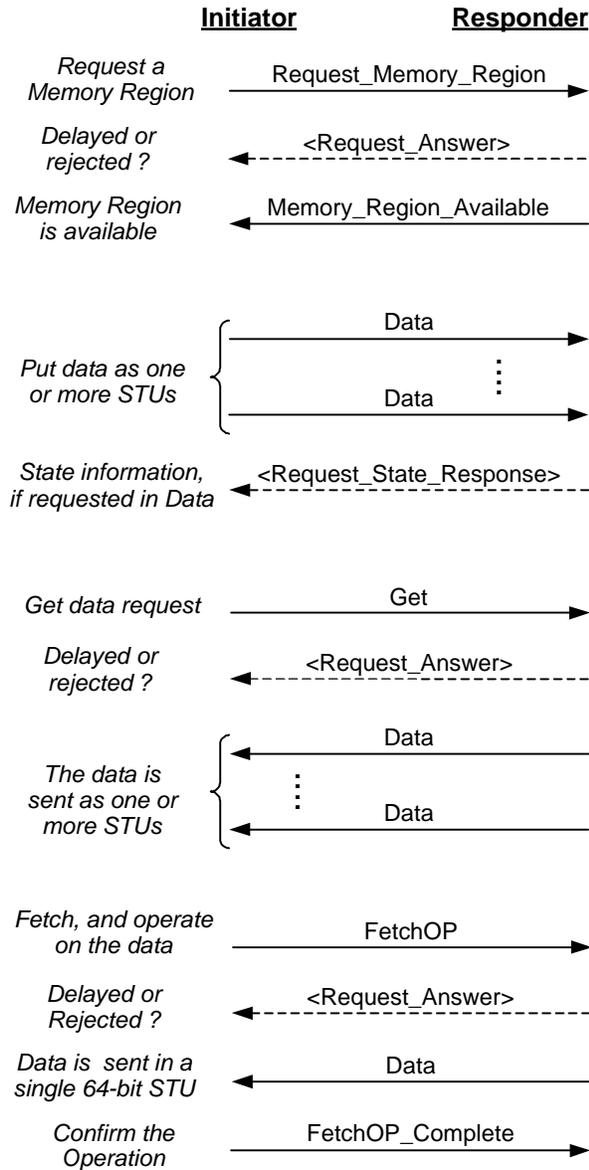
#### 6.1.4 Put, Get, and FetchOp sequences

The Put, Get, and FetchOp sequences, as shown in the figure 10 example and detailed in table 8 (see page 38), shall be preceded by a sequence that allocates a persistent memory region in the Responder. A Virtual Connection shall exist before a persistent memory region allocation sequence is initiated.

Once allocated, the persistent memory region is available for multiple Put, Get, and FetchOp sequences from the Initiator. By assigning unique values to the G-id and F-id parameters, multiple Get and FetchOp operations may be outstanding to the same persistent memory region. The Put, Get, or FetchOp Blocks shall be contained within a persistent memory region. A Virtual Connection can have multiple persistent memory regions allocated.

### 6.1.4.1 Allocate a persistent memory region

**Request\_Memory\_Region** may be issued by the Initiator to request that the Responder allocate a persistent memory region. (See table 8 page 38, PG1 and PG2.)



**Figure 10 – Put, Get, and FetchOp examples**

– Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Responder. D bits specify the Data Channel to be used.

– R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

– T\_len specifies the persistent memory region's length (see 6.2.3 page 24).

– I-id assigns the Initiator's **sequence identifier** (see 6.2.1 page 23).

**Request\_Answer** may be issued by the Responder to reject or pause the `Request_Memory_Region`. If rejected, then this is the end of the Put, Get, and FetchOp sequences for this persistent memory region.

– Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator. R = 1 specifies that the `Request_Memory_Region` has been rejected or the Responder does not support persistent memory operations. R = 0 specifies that the `Request_Memory_Region` has been recognized; final action is pending (i.e., a `Memory_Region_Available` or `Request_Answer` with R = 1).

– I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

– I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).

**Memory\_Region\_Available** shall be sent by the Responder upon receipt of an acceptable `Request_Memory_Region` operation. (Unacceptable requests result in `Request_Answer` with `Reject = 1`.) Once established, the persistent memory region (see 6.2.12 page 26), shall remain available (for Put, Get, and FetchOp operations), until terminated by an End or Disconnect sequence (which may come from either end of the Virtual Connection). The Put, Get, and FetchOp operations to the persistent memory region are not flow controlled other than by the Slot accounting rules (see 5.2.5 page 10).

– Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.

– I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- R-Mx assigns the Responders Memory Index for this persistent memory region (see 6.2.2 page 24).
- R-Bufx assigns the Responder's first Buffer Index for this persistent memory region (see 6.2.8 page 25).
- R-Offset assigns the Responder's initial Offset for this persistent memory region (see 6.2.8 page 25).
- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).
- R-id assigns the Responder's **sequence identifier** (see 6.2.1 page 23).

#### 6.1.4.2 Put sequences

A Put sequence moves a single Block from the Initiator (the data Source), to the Responder (the data Destination). The Block size may be the same, or smaller than, the size of the persistent memory region. If smaller, different data Destination Bufx and Offset values than those specified in the Memory\_Region\_Available operation, may be used. The Responder shall only accept data into the pre-allocated persistent memory region. (See table 8 page 38, PG3 and PG4.)

**Data** operations (one for each STU of the Block), shall be issued by the Initiator to send the Block being "Put", from the data Source (the Initiator), to the data Destination (the Responder).

- Flags (see 8.2 page 29): T = 1 specifies that the data shall be delivered silently. I = 1 specifies that an interrupt shall be generated at the Responder. S = 1 specifies that the Responder shall reply with a Request\_State\_Response upon successful receipt of this STU. L = 1 marks the last STU of the Block. D bits echo the Data Channel assignment.
- STU\_num specifies the number for this STU (see 6.2.7 page 25).
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- R-Mx echoes the Responder's Memory Index (see 6.2.2 page 24).

- R-Bufx assigns the Responder's Buffer Index for this STU (see 6.2.8 page 25).
- R-Offset assigns the Responder's Offset for this STU (see 6.2.8 page 25).
- Sync assigns the Initiator's Sync value (see 6.2.9 page 25).
- B\_num specifies the Block number for this Block (see 6.2.4 page 24).
- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).
- Opaque contains the Opaque data for this STU (see 6.2.10 page 26).

**Request\_State\_Response** shall be issued by the Responder if S = 1 in the previous Data operation.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.
- R-Slots specifies the number of available Slots in the Responder for this Virtual Connection (see 5.2.5 page 10).
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- B\_seq specifies the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly (see 6.2.4 page 24).
- Sync echoes the Initiator's Sync value (see 6.2.9 page 25).
- B\_num indicates if the Block, specified in the Data operation with S = 1, was received correctly (see 6.2.4 page 24).
- I-id echoes the Initiator's **sequence identifier** (see 6.2.1 page 23).
- R-id echoes the Responder's **sequence identifier** (see 6.2.1 page 23).

An **End** operation may be issued by either end device to abort the Put sequence and terminate the persistent memory region. (See 6.1.1.4 page 14, and table 5 page 34, Com4.)

### 6.1.4.3 Get sequences

A Get sequence moves a single Block from the Responder (the data Source), to the Initiator (the data Destination). The Block size may be the same, or smaller than, the size of the persistent memory region. If smaller, different data Source Bux and Offset values than those specified in the Memory\_Region\_Available operation may be used. The Responder shall only send data from the pre-allocated persistent memory region. (See table 8 page 38, PG5.)

**Get** may be issued by the Initiator. The Get specifies both the Initiator's and Responder's Memory Index, Buffer Index, and Offset values.

- Flags (see 8.2 page 29): F = b'000'. I = 1 specifies that an interrupt shall be generated at the Responder. D bits echo the Data Channel assignment.
- T\_len assigns the Block length (see 6.2.3 page 24). Note that a 16-bit length parameter is used, rather than the 64-bit length parameter of most other operations.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- I-Mx assigns the Initiator's Memory Index for this Block (see 6.2.2 page 24).
- R-Bux assigns the Responder's first Buffer Index for this Block (see 6.2.8 page 25).
- R-Offset assigns the Responder's initial Offset for this Block (see 6.2.8 page 25).
- I-Bux assigns the Initiator's first Buffer Index for this Block (see 6.2.8 page 25).
- I-Offset assigns the Initiator's initial Offset for this Block (see 6.2.8 page 25).
- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).
- G-id assigns the Initiator's Get identifier (see 6.2.1 page 23).

**Request\_Answer** may be issued by the Responder to reject or pause the Get operation. If rejected, then this is the end of the Get sequence.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator.

R = 1 specifies that the Get operation has been rejected. R = 0 specifies that the Get has been recognized; final action is pending (i.e., a Data operation or Request\_Answer with R = 1).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- G-id echoes the Initiator's Get identifier (see 6.2.1 page 23).
- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).

**Data** operations (one for each STU of the Block), shall be issued by the Responder to send the Block from the data Source (the Responder), to the data Destination (the Initiator). The data Destination shall place the STU data in the pre-allocated memory area pointed to by the I-Bux and I-Offset parameters. The data Source shall only send data from the pre-allocated persistent memory region.

- Flags (see 8.2 page 29): T = 1 specifies that the data shall be delivered silently. I = 1 specifies that an interrupt shall be generated at the Initiator. L = 1 marks the last STU of the Block. D bits echo the Data Channel assignment.
- STU\_num specifies the number for this STU (see 6.2.7 page 25).
- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- I-Mx echoes the Initiator's Memory Index (see 6.2.2 page 24).
- I-Bux assigns the Initiator's Buffer Index for this STU (see 6.2.8 page 25). If this is the first STU of the Block, then I-Bux echoes the I-Bux value in the Get operation.
- I-Offset assigns the Initiator's Offset for this STU (see 6.2.8 page 25). If this is the first STU of the Block, then I-Offset echoes the I-Offset in the Get operation.
- Sync assigns the Responder's Sync value (see 6.2.9 page 25).
- G-id echoes the Initiator's Get identifier (see 6.2.1 page 23).

- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).

An **End** operation may be issued by either end device to abort the Get sequence and terminate the persistent memory region. (See 6.1.1.4 page 14, and table 5 page 34, Com4.)

#### 6.1.4.4 FetchOp sequences

A FetchOp sequence fetches data from, and then operates on, a 64-bit aligned, 64-bit data Block in an established persistent memory region in the Responder. Since the length is fixed at 64 bits, no T\_len parameter is used. The fetch and operation of a FetchOp shall be atomic. The data received by the Initiator shall be the value before the operation is performed. A FetchOp may be retried if the associated Data operation is not returned within the Timeout period (see 10.1 page 40). The Responder shall provide the ability to return the same value, upon receipt of subsequent FetchOps with the same F-id, until the operation is acknowledged with a FetchOp\_Complete. Upon receipt of FetchOp\_Complete, the Responder shall release its ability to retry the original FetchOp. (See table 8 page 38, PG6.)

**FetchOp** may be issued by the Initiator. The FetchOp specifies both the Initiator's and Responder's Buffer Index and Offset values, and the function to be performed on the 64-bit Block.

- Flags (see 8.2 page 29): F = function to be performed. I = 1 specifies that an interrupt shall be generated at the Responder. D bits echo the Data Channel assignment.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- I-Mx assigns the Initiator's Memory Index for this Block (see 6.2.2 page 24).
- R-Bufx assigns the Responder's Buffer Index for this Block (see 6.2.8 page 25).
- R-Offset assigns the Responder's Offset, evenly divisible by 8, for this Block (see 6.2.8 page 25).
- I-Bufx assigns the Initiator's Buffer Index for this Block (see 6.2.8 page 25).

- I-Offset assigns the Initiator's Offset for this Block (see 6.2.8 page 25).

- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).

- F-id assigns the Initiator's FetchOp identifier (see 6.2.1 page 23).

**Request\_Answer** may be issued by the Responder to reject or pause the FetchOp. If rejected, then this is the end of the FetchOp sequence. FetchOp operations shall be rejected by the Responder with a Request\_Answer operation with Reject = 1 if: the Responder does not implement atomic FetchOp operations, or a bad parameter, e.g., Bufx or Offset, was supplied in the FetchOp operation.

- Flags (see 8.2 page 29): I = 1 specifies that an interrupt shall be generated at the Initiator. R = 1 specifies that the FetchOp has been rejected. R = 0 specifies that the FetchOp has been recognized; final action is pending (i.e., a Data operation or Request\_Answer with R = 1).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).

- F-id echoes the Initiator's FetchOp identifier (see 6.2.1 page 23).

- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).

A **Data** operation shall be issued by the Responder to send the 64-bit Block in one STU from the data Source (the Responder), to the data Destination (the Initiator). The data Destination shall place the STU data in the pre-allocated memory area pointed to by the I-Bufx and I-Offset parameters. The data Source shall only send data from the pre-allocated persistent memory region.

- Flags (see 8.2 page 29): T = 1 specifies that the data shall be delivered silently. I = 1 specifies that an interrupt shall be generated at the Initiator. L shall = 1 to specify that this is the last STU of the Block. D bits echo the Data Channel assignment.

- Param field = x'0000', i.e., a FetchOp contains only one STU, so STU\_num = x'0000' (see 6.2.7 page 25).

- I-Port, R-Port, and I-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- I-Mx echoes the Initiator's Memory Index (see 6.2.2 page 24).
- I-Bufx echoes the Initiator's Buffer Index for this STU (see 6.2.8 page 25).
- I-Offset echoes the Initiator's Offset for this STU (see 6.2.8 page 25).
- Sync assigns the Responder's Sync value (see 6.2.9 page 25).
- F-id echoes the Initiator's FetchOp identifier (see 6.2.1 page 23).
- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).

**FetchOp\_Complete** shall be issued by the Initiator to indicate receipt of the 64-bit data Block. Upon receipt, the Responder shall release its ability to retry the original FetchOp.

- Flags (see 8.2 page 29): F = b'111'. I = 1 specifies that an interrupt shall be generated at the Responder.
- R-Port, I-Port, and R-Key echo the Port and Key values assigned during Virtual Connection setup (see 5.1.1 page 8).
- Sync echoes the Responder's Sync value (see 6.2.9 page 25).
- R-id echoes the Responder's sequence identifier (see 6.2.1 page 23).
- F-id echoes the Initiator's FetchOp identifier (see 6.2.1 page 23).

An **End** operation may be issued by either end device to abort the FetchOp sequence and terminate the persistent memory region. (See 6.1.1.4 page 14, and table 5 page 34, Com4.)

## 6.2 Data movement parameters

### 6.2.1 Sequence identifiers (F-id, G-id, I-id, and R-id)

Like the Ports and Keys, each end device shall also select its own 32-bit sequence identifier for a data movement on the Virtual Connection. `x'FFFFFFFF` is a reserved value for I-id and R-id. Each sequence identifier shall monotonically increase (to avoid aliasing). The sequence identifiers are:

- F-id = FetchOp sequence identifier. F-id shall be assigned by the Initiator and passed to the Responder in FetchOp operations. The Responder shall return the F-id in all subsequent operations concerning this FetchOp sequence.
- G-id = Get sequence identifier. G-id shall be assigned by the Initiator and passed to the Responder in Get operations. The Responder shall return the G-id in all subsequent operations concerning this Get sequence.
- I-id = Initiator's sequence identifier. I-id shall be assigned by the Initiator and passed to the Responder in Request\_To\_Send, Request\_To\_Receive, and Request\_Memory\_Region operations. The Responder shall return the I-id in all subsequent operations concerning this Transfer or persistent memory region.
- R-id = Responder's sequence identifier. R-id shall be assigned by the Responder and passed to the Initiator in Clear\_To\_Send, Request\_To\_Send, and Memory\_Region\_Available operations. The Initiator shall return the R-id in all subsequent operations concerning this Transfer or persistent memory region.

In a Request\_State operation, D\_id field = `x'FFFFFFFF` means that the Responder shall not look for a current Transfer and shall only return the current number of empty Slots for this Virtual Connection. If a Request\_State operation contains an R-id value that the Responder does not recognize, perhaps due to prior completion or termination of the Transfer, then the Responder shall return `x'FFFFFFFF` in the S\_id field of the Request\_State\_Response operation.

### 6.2.2 Memory Index (Mx)

Like the Ports, Keys, and **sequence identifiers**, each end device shall also select its own 16-bit Memory Index (Mx). The Mx parameter provides a mechanism to identify an area of memory associated with a Block or persistent memory region. A data Destination shall assign locally significant Mx values in each Clear\_To\_Send, Memory\_Region\_Available, Get, and FetchOp operation that the data Destination issues. Each assignment may use a different Mx value. The Data operations on this memory region shall use the assigned Mx value. The Mx values are opaque at the data Source, i.e., have no meaning.

### 6.2.3 Transfer length (T\_len)

The 64-bit Transfer length parameter (T\_len) in Read and Write sequences specifies the total number of data payload bytes in the Transfer. T\_len does not include the Schedule Header or any LLP headers. T\_len = all zeros shall indicate an unlimited size Transfer. An unlimited size Transfer may be terminated by an End sequence (see 6.1.1.4 page 14).

The 64-bit T\_len parameter in a Request\_Memory\_Region operation specifies the size of the persistent memory **region**.

A 16-bit T\_len parameter specifies the Block size in **Get** sequences, exclusive of the Schedule Header or any LLP headers. The Block size in **Get** sequences shall be  $\leq$  the size of the persistent memory **region**.

### 6.2.4 Blocks, B\_num and B\_seq

Scheduled Transfer flow control, striping, acknowledgments, and resource allocation are all done on a Block basis. Block numbers (B\_num) for Read sequences, Write sequences, or a set of Puts to a persistent memory region, shall be numbered starting at zero and shall increment by one for each following Block. B\_num shall wrap from x'FFFFFFFE' to x'00000000'.

B\_num = x'FFFFFFF' is reserved for use as a flag by Request\_State and Request\_State\_Response operations. In a Request\_State operation, B\_num = x'FFFFFFF'

indicates that the requestor is not asking if a particular Block has been received correctly. In a Request\_State\_Response operation of a Request Block state, B\_num = x'FFFFFFF' indicates that the Block (identified by the B\_num parameter in the Request\_State operation), has not been correctly received by the data Destination.

Blocks shall be enabled for transmission in sequential order unless both end devices indicated Out\_of\_Order capability during the Virtual Connection setup. Note that Out\_of\_Order capability is necessary for retransmission to correct flawed Blocks. **A user sending more than  $2^{32}$  Blocks in a Transfer is advised to consider the possibility of B\_num aliasing (i.e., having two Blocks with the same B\_num outstanding simultaneously).**

The Blocks associated with Read and Write sequences are not persistent. This means that once exposed with a Clear\_To\_Send operation, a Block can be used only once and may be allocated for other uses after that Block is complete.

Request\_State\_Response operations indicate (in the B\_seq parameter), the highest numbered Block of the specified Transfer received correctly where all lower numbered Blocks are also received correctly. For example, if Blocks 0 through 9 have been received and only Block 6 had an error, then the Request\_State\_Response operation would have B\_num = 5. B\_seq = x'FFFFFFF' indicates that no Blocks have been received by the data Destination for this Transfer.

Request\_State\_Response operations can be requested by setting the Send\_State flag bit in Data operations or by sending Request\_State operations. In addition, Request\_State operations can ask if a particular Block (B\_num) was received correctly for the specified Transfer. Use of these mechanisms allows the data Source to verify correct reception and to identify flawed Blocks for potential retransmission.

### 6.2.5 Maximum Block size (Max\_Block)

**The maximum number of bytes that the data Source would like to transfer in a single Block of a Read or Write Transfer is indicated in the Request\_To\_Send operation when the Transfer is initiated. This number is the Max\_Block**

parameter and is expressed as a power of two, i.e.,  $2^{\text{Max\_Block}}$  where  $8 \leq \text{Max\_Block} \leq 48$ . Max\_Block can limit the number of data Destination bytes to be exposed (see 6.2.6 page 25).  $\text{Max\_Block} \leq \{[\text{maximum number of STUs per Block (i.e., } 2^{16}), \text{ minus the number of non-payload carrying Data operations (see 8.3.2 page 30, and 8.3.4 page 31)]} \times \text{Max\_STU size for this direction of the Virtual Connection (see 5.2.4 page 10)}\}$ . The data Source may specify smaller Max\_Block values, e.g., to accommodate smaller STU sizes for this particular Transfer. Intermediate devices with smaller buffer sizes may lower the Max\_Block value.

### 6.2.6 Blocksize

The maximum number of bytes in a Block in a Read or Write Transfer is established in a Clear\_To\_Send operation when the Transfer is initiated. This number is the Blocksize parameter and is expressed as a power of two, i.e.,  $2^{\text{Blocksize}}$  where  $8 \leq \text{Blocksize} \leq 48$ . All of the Blocks of a Transfer shall be the same size, except for the first and/or last Block of a Transfer which can be smaller. Blocksize shall be  $\leq \text{Max\_Block}$  for this Transfer (see 6.2.5 page 24).

The size of the first Block shall be:

$$\text{Blocksize} - (\text{Offset mod Blocksize})$$

unless the Transfer length is less than this value in which case the first Block contains the entire Transfer. When  $\text{Blocksize} \geq \text{Bufsize}$ , this rule forces the first Block to end on a buffer boundary and makes all subsequent Offsets zero. When  $\text{Bufsize} > \text{Blocksize}$ , this rule forces the first Block to end on one of the hypothetical  $2^k$  Block boundaries within the buffer that would exist if the Offset were zero, thus making all subsequent Offsets some multiple of the Blocksize. The last Block will be whatever completes the Transfer.

### 6.2.7 STUs and STU\_num

For performance reasons the STUs shall be transmitted in order. STU numbers (STU\_num) for a Block shall start with zero and increment by one for each following STU. STU\_num shall not wrap within a Block. The last STU of a Block shall be marked with Last = 1. No STU shall extend past a data Destination's buffer boundary, Block boundary, or Transfer boundary. Out of

order delivery may cause errors or reduce performance (see 10.7.5 page 42). There is no requirement that STU sizes be consistent throughout a Block or Transfer, but STU sizes shall be no larger than specified in 5.2.4 page 10.

### 6.2.8 Bufx and Offset

Bufx contains a Buffer Index. If more than one Buffer Index is required for a Block, i.e., buffer size (Bufsize) is less than Blocksize, then the Bufx parameter in a Clear\_To\_Send, Get, or FetchOp operation shall specify the initial Bufx, and any additional Bufx values shall be sequential.

Offset may be used to start at other than the first byte of a buffer. For the first STU of a Block, the Offset shall be the same as received in the Clear\_To\_Send, Get, or FetchOp for the Block. Subsequent STUs of the Block shall adjust the Bufx and Offset based on the data Destination's buffer size and the STU size used by the data Source.

The Offset associated with the first block of a Transfer (F\_Offset) is included in all Clear\_To\_Send operations. This allows the data Destination to compute the starting address for any Block without having received the Clear\_To\_Send for the first Block. Clear\_To\_Send operations can occur out of order, e.g., as the result of striping (see annex B page 55).

Best performance will usually be achieved when an Offset of zero is specified. Use of non-zero Offset may degrade performance, depending upon underlying hardware transfer mechanisms.

### 6.2.9 Sync

The 32-bit Sync parameter shall be used to associate a Request\_State\_Response operation with one of many Data or Request\_State solicitations (see 5.2.5 page 10 concerning updating a local end device's image of the number of available Slots at the remote end device for this Virtual Connection). Sync shall also be used to associate a FetchOp\_Complete operation with a Data operation.

The Sync parameter provides a mechanism for a

data Source to identify and track subsequent operations within a sequence. These Sync values have no meaning at the data Destination. A data Source shall assign a locally significant Sync value in each Data operation that the data Source issues. Each assignment may use a different Sync value. The associated Request\_State\_Response and FetchOp\_Complete operations shall echo the Sync value.

A local end device (regardless of whether it is the data Source or data Destination), shall assign a locally significant Sync value in each Request\_State operation that the local end device issues. Each assignment may use a different Sync value. The associated Request\_State\_Response shall echo the Sync value.

#### 6.2.10 Opaque data

Opaque data is four bytes of ULP peer-to-peer information carried in a Data operation's Schedule Header S\_id field. The Opaque data shall be delivered to the recipient's ULP when Silent = 0 or Send\_State = 1 (see 8.2 page 29). The Opaque data shall be passed, unmodified by any intermediate device, from the Source to the Destination. Note that the Opaque data uses Slot resources while the data payload uses Bufx resources.

#### 6.2.11 Blocks enabled (CTS\_req)

In Read and Write sequences, the data Source specifies in the Request\_To\_Send operation the number of outstanding Blocks that the data Source would like to see exposed at any given time for maximum performance. Since each Block is exposed for a single use with a Clear\_To\_Send operation, this parameter is named CTS\_req. CTS\_req = x'0000' means don't care. The CTS\_req is "advice" to improve performance; it is not mandatory that the other end comply by issuing that number of Clear\_To\_Send operations. Specifying the number of simultaneously enabled Blocks is useful for pipelining and striping where multiple Blocks can be en-route simultaneously (see annex B page 55).

#### 6.2.12 Persistent memory

The memory region associated with a Request\_Memory\_Region operation is persistent. That means that once allocated, the memory region remains available for multiple Put, Get, and FetchOp sequences until released by an End or Port teardown sequence.

For Get and FetchOp sequences, the Responder's transmit and receive buffer sizes shall be the same size. Note that the Initiator must know the Responder's transmit buffer size to correctly calculate the R-Bufx and R-Offset values, and only an end device's receive buffer size is exchanged during Virtual Connection setup (see 5.1.1 page 8, and 5.2.3 page 10).

### 6.3 Packing examples

Figure 11 shows three possibilities for packing the same Transfer into a data Destination's buffers. All three examples show a group of seven of the data Destination's buffers on the top line. Each buffer is pointed to by a Bufx, and the data in the first buffer starts at an Offset. The Transfer is the shaded bar, with transmission going from left to right. The Block boundaries are shown above the shaded bar, and the resulting STU boundaries are shown below the shaded bar.

Example (a), at the top, shows the case where the buffers and Blocks are the same size. Notice that the first Block is smaller than the other Blocks by the Offset. Offset = zero for the other Blocks. The last Block of the Transfer is also smaller, i.e., the Transfer did not end on a Block boundary. While the STU boundaries lined up nicely, the issuer could have used multiple STUs, but the STUs cannot be larger than Max\_STU. In this example, the STU boundaries (except the first and the last) all line up with the Block and buffer boundaries. However, there is no such restriction in ST. The sender may send arbitrary-sized STUs at any point during the Transfer as

long as the STUs do not cross Block, buffer, or Transfer boundaries, and do not exceed  $2^{\text{Max\_STU}}$ .

Example (b) shows multiple Blocks per receiver buffer. The Blocks that do not start on a buffer boundary would use the Offset parameter to position the data.

Example (c) shows the Block size covering two of the receiver's buffers.

In summary, STUs cannot cross Block, buffer, or Transfer boundaries. Relationships include:

$\text{STU size} \leq 2^{\text{Max\_STU}}$  (Max\_STU: see 5.2.4 page 10.)

$\text{Max\_STU} \leq \text{Bufsize}$  (Bufsize: see 5.2.3 page 10)

$\text{Max\_STU} \leq \text{Blocksize}$  (Blocksize: see 6.2.6 page 25)

$\text{Blocksize} \leq \text{Max\_Block}$  (Max\_Block: see 6.2.5 page 24)

Note that the Blocksize can be larger, smaller, or the same as Bufsize.

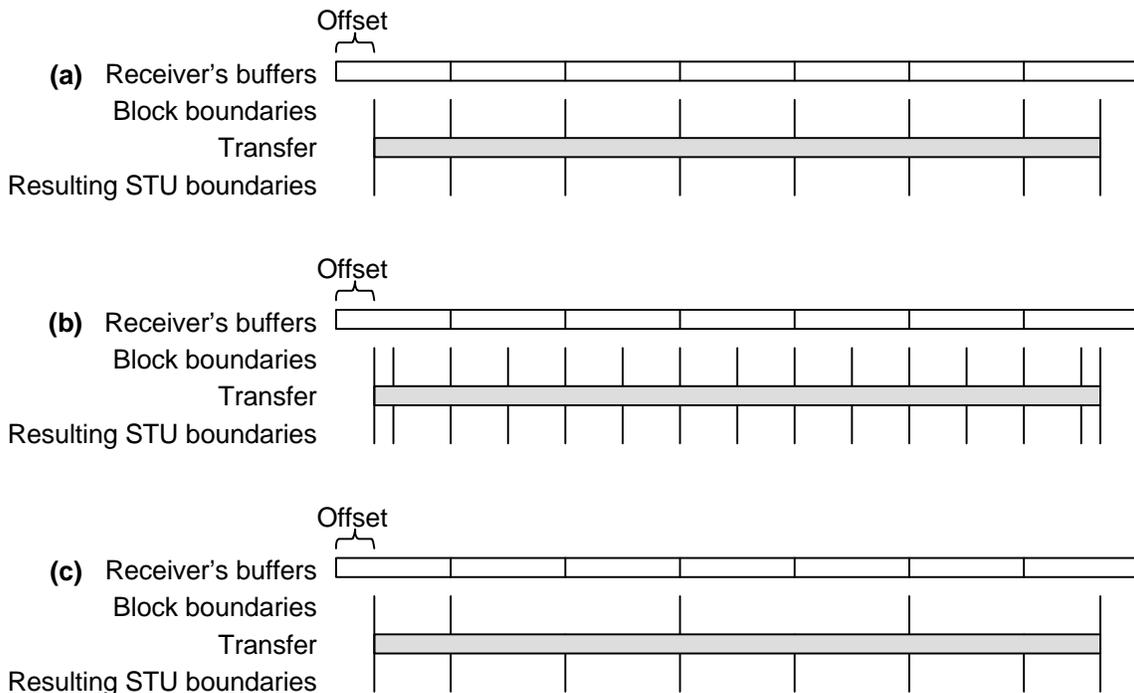


Figure 11 – Data packing examples

## 7 Operations management

### 7.1 Flow control

Data flow control in Read and Write sequences is achieved with Clear\_To\_Send operations; each Clear\_To\_Send received gives the data Source permission to send one Block one time. There is no equivalent flow control for Put, Get, and FetchOp operations.

Operation flow control is achieved by an operation's issuer not overrunning the Slots value (see 5.2.5 page 10).

### 7.2 Status operations

Request\_State and Request\_State\_Response operations are used to request and supply status information about the state of the remote end device. They can be used to see which Blocks have been received correctly for a specific Transfer, and the number of empty Slots available for this Virtual Connection. The Sync parameter (see 5.2.5 page 10) is used to provide a common reference point for the local and remote end devices, i.e., to match Request\_State and Request\_State\_Response operations.

### 7.3 Rejected operations

If the receiving end device is unable to execute an operation, then the receiving device shall set the Reject flag bit = 1 in the response. Table 1 shows the response when an operation is rejected. The recovery actions taken when an operation is rejected are beyond the scope of this standard.

**Table 1 – Response to a rejected operation**

Rejected operation	Response (w/ Reject=1)
Request_Connection	Connection_Answer
Request_To_Send	Request_Answer
Request_To_Receive	Request_Answer
Request_Memory_Region	Request_Answer
Get	Request_Answer
FetchOp	Request_Answer

### 7.4 Interrupts

An Interrupt causes a signal to be delivered to the receiving end device ULP. An Interrupt can be requested with any operation by setting Interrupt = 1.

## 8 Schedule Header

The Schedule Header is shown in figure 12 as a group of 32-bit words. The Schedule Header fields are named for the most common parameter for which the field is used. Many of the fields have different uses depending on the operation type, and some operations do not use one or more of the fields at all. The usage for each field is specified in tables 4-8.

If an operation does not use a particular Schedule Header field, then that field shall be transmitted as zeros. If a parameter does not completely fill a field then the parameter shall be right justified with leading zeros used to pad out the field.

Op	Flags	Param	Bytes
			00-03
D_Port		S_Port	04-07
D_Key			08-11
Cksum		B_id	12-15
Bufx			16-19
Offset			20-23
Sync			24-27
B_num			28-31
D_id			32-35
S_id			36-39

**Figure 12 – Schedule Header contents**

## 8.1 Op codes

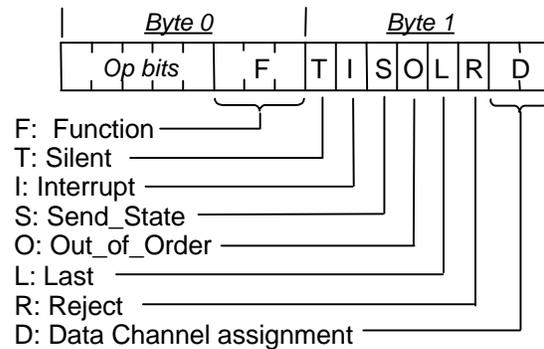
The operations, and their 5-bit Op code are listed in table 2. Unspecified Op values are reserved.

**Table 2 – Op codes and operations**

Op	Operation
x'01'	Request_Connection
x'02'	Connection_Answer
x'03'	Request_Disconnect
x'04'	Disconnect_Answer
x'05'	Disconnect_Complete
x'13'	Request_Memory_Region
x'14'	Memory_Region_Available
x'15'	Get, FetchOp, FetchOp_Complete
x'16'	Request_To_Send
x'17'	Request_Answer
x'18'	Request_To_Receive
x'1A'	Clear_To_Send
x'1B'	Data
x'1C'	Request_State
x'1D'	Request_State_Response
x'1E'	End
x'1F'	End_Ack

## 8.2 Flags

Figure 13 shows the flags, and their relative position within the Flags field. The flag functions are detailed below for the case where the bit = 1. The Flags field column in tables 4-8 specify the flags that are valid for each operation.



**Figure 13 – Flags summary**

**Function:** In FetchOp operations, the Function flags shall have the following meanings. The unspecified values are reserved.

- b'000xxxxxxx' = NOP (i.e., Get)
- b'001xxxxxxx' = Fetch and Increment
- b'010xxxxxxx' = Fetch and Decrement
- b'011xxxxxxx' = Fetch and Clear
- b'111xxxxxxx' = FetchOp Complete

In Request\_Connection and Connection\_Answer operations, the Function flags specify the issuing end device's attributes:

- b'x00xxxxxxx' = Does not support persistent memory
- b'x01xxxxxxx' = Supports persistent memory but not FetchOp operations
- b'x11xxxxxxx' = Supports persistent memory and FetchOp operations
- b'1xxxxxxx' = Little endian ULP architecture

**Silent** (b'xxx1xxxxxx') = Requests silent delivery of a Data operation. When Silent = 1 the data transfer to the data Destination Bufx is carried out normally, but the Schedule Header shall not be delivered to any ULP. This provides the basis for remote memory write semantics where the intent is to modify the contents of a remote memory without executing software in the remote host computer. This also provides a means for reducing overhead by suppressing all but the final Schedule Header delivery to the ULP during a lengthy Scheduled Transfer. Silent is overridden when Send\_State = 1.

**Interrupt** (b'xxxx1xxxxxx') = Requests that a signal or interrupt be generated and delivered to the appropriate ULP. The Interrupt flag is

independent of the Silent flag, i.e., Interrupt = 1 calls for a signal whether or not Silent = 1.

NOTE 1 – The Silent and Interrupt flags together provide for three delivery modes for Data operations: silent, polled, or interrupt-driven. If Silent = 1, the data payloads are delivered silently and do not consume a Slot (see 5.2.5 page 10). If Silent = 0, then the ULP is informed by the same means used for all other Schedule Headers and the Schedule Header for this operation is delivered to the ULP. This mode is suitable for polled interfaces. If Interrupt = 1, then a signal is delivered.

**Send\_State** (b'xxxxx1xxxx') = Requests that the receiving ULP respond with a Request\_State\_Response upon successful receipt of this STU. Send\_State is valid in all Data operations. Send\_State = 1 overrides the Silent flag actions, and always consumes a Slot.

**Out\_of\_Order** (b'xxxxx1xxxx') = The Source is able to send and receive Blocks in any order.

**Last** (b'xxxxxxx1xxx') = Marks the last STU of a Block.

**Reject** (b'xxxxxxxx1xx') = The Request\_Connection, Request\_To\_Send, Request\_To\_Receive, Get, Request\_Memory\_Region, or FetchOp operation has been rejected.

**Data Channel assignment:** The Data Channel to be used to carry Data operations. The Data Channel value is assigned in a Request\_To\_Send, Request\_To\_Receive, or Request\_Memory\_Region operation, and is the Data Channel to be used for Data operations associated with this Transfer. The Data Channel Assignment is not checked at the data Destination.

b'xxxxxxxx01' = Data Channel 1

b'xxxxxxxx10' = Data Channel 2

b'xxxxxxxx11' = Data Channel 3

The maximum STU size sent on Data Channels 1 and 2 shall be  $2^{17}$  bytes (i.e., 128 Kbytes). The maximum STU size sent on Data Channel 3 shall be  $2^{31}$  bytes (i.e., 2 gigabytes).

NOTE 2 – Data Channel assignment value b'00' is reserved.

## 8.3 Checksum (optional)

The 16-bit end-to-end checksum (Cksum), or x'0000' in the absence of a checksum, shall be transmitted in every operation, and optionally checked (see 10.4 page 41) at each Destination.

*Open Issue – The text in 8.3, and figure 14, are new and need to be checked for correctness and completeness.*

### 8.3.1 Checksum algorithm

The checksum shall be computed as:

– Adjacent bytes to be checksummed shall be paired to form 16-bit integers. If an odd number of bytes is to be checksummed, then x'00' shall be used as the pad in the last 16-bit integer.

– To generate the checksum, the Cksum field itself shall be set = x'0000', and the 16-bit 1's complement sum shall be computed over the 16-bit integers. The 1's complement of this sum shall be the calculated checksum value. The transmitted Cksum shall contain the calculated checksum value, unless the calculated checksum value = x'0000' in which case transmit Cksum = x'FFFF'.

NOTE – The checksum algorithm is similar to that used by the Internet Protocol (i.e., IP, RFC 791), with the addition of the optional reserved value x'0000' to indicate the absence of a checksum. The User Datagram Protocol (i.e., UDP, RFC 768), implements the same option, but its checksum algorithm is slightly different because of the calculation of the pseudo-header as part of the checksum. Also, note RFC 1071, *Computing the Internet Checksum*, and RFC 1936, *Implementing the Internet Checksum in Hardware*.

### 8.3.2 Transmitting checksums

If a Source end device does not support checksums, then it shall transmit Cksum = x'0000' in every Schedule Header.

On Source end devices that support checksums, the checksums for Control operations shall be calculated over all of the bytes in the Schedule Header, and the bytes in the optional payload, if present.

On Source end devices that support checksums, a Data operation's Cksum value shall = x'0000', or shall be the calculated checksum value over the Schedule Header(s) bytes and the data payload bytes for this segment of a Block. The first segment of a Block begins with the first Data operation of the Block and ends with the first Data operation that contains a non-zero Cksum. The second segment begins immediately after the first, and ends with the next Data operation containing a non-zero Cksum, and so on. A Block may consist of one segment, or of multiple segments. Computing checksums over segments of a Block, instead of the entire Block, provides stronger checking by covering a smaller number of bytes. It is also permissible to use Data operations without data payload bytes to carry checksums. This technique provides a way to send checksums as a trailer, rather than having to store-and-forward the data payload bytes to put the checksum in the header. These Data operations without data payloads consume STU numbers just like all other Data operations. The rules for Data operations are:

- If a Data operation does not carry a checksum, then transmit Cksum = x'0000'.
- If a Data operation carries a checksum, then Cksum = calculated checksum value for this segment of the Block (Cksum ≠ x'0000' indicates the end of this segment).
- The last Data operation of a Block (marked with Last = 1), shall carry a checksum if checksums are supported.

### 8.3.3 Receiving checksums

Destinations that support checksums shall calculate checksums the same as specified in 8.3.1 and shall check all checksums ≠ x'0000'. A received Cksum = x'0000' shall be ignored. To check, the 1's complement sum is computed over the same set of bytes as specified in 8.3.2, including the Cksum field(s). If the result = x'FFFF' (i.e., - 0 in 1's complement arithmetic), the check succeeds.

### 8.3.4 Data checksum example

Figure 14 shows an example Block transmission, with the Block's data payload bytes contained in three of the five Data operations. The Block was broken into two segments to limit the number of

bytes covered by a checksum. The first segment's checksum (i.e., x'abcd' in Data operation 3) covers the bytes in:

- Schedule Headers 1, 2, and 3,
- Data payload bytes A and B.

The second segment's checksum (i.e., x'wxyz' in Data operation 5), covers the bytes in:

- Schedule Headers 4 and 5,
- Data payload bytes C.

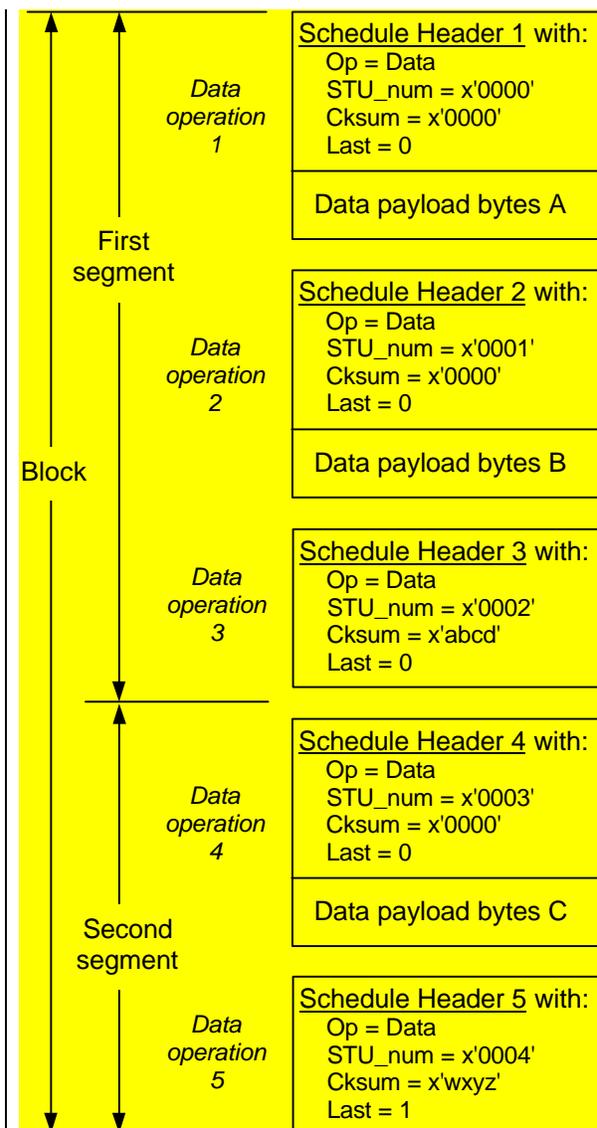


Figure 14 – Data checksum example

## 9 Operations summary

### 9.1 Operation sequence tables

Tables 4-8 define the parameters that shall be carried in each field of the Schedule Header for each operation. Within the operations, the following prefixes are used:

I- = associated with the Initiator

R- = associated with the Responder

Other rules associated with the tables include:

- Operations contained within <...> are conditional, and may not occur.
- The entries under the Flags parameter are abbreviations for the individual flag bits as shown in figure 13.
- Multiword parameters and field names are joined with an underscore, e.g., D\_Port.
- Values in bold italics are assigned by the specific operation and may be used by later operations.
- A \* marks a field carrying an unused value; that field shall be transmitted as zeros and shall not be checked at the receiver.
- If a parameter does not completely fill a field then the parameter shall be right justified with leading zeros used to pad out the field.

### 9.2 Avoiding alias operations

Moving data to or from a wrong location, or applying the wrong operation to a set of data is undesirable. The Scheduled Transfer parameters were specifically made large enough, and their usage specified, so that the possibility of applying an operation to the wrong data stream is infinitesimal.

Operations in ST tend to be unique to a specific layer of the protocol. Table 3 is a categorization of the hierarchy of operations, grouped by shared parameters and common fields. The sizes of the parameters used to qualify a particular group of operations are given, e.g.,  $2^{32}$  Virtual Connections must occur before a host's Key value can be duplicated as an alias operation. Note that a lower level operation is qualified by the upper level parameters, e.g., Block operations are also qualified by the Ports, Keys, and I-id and R-id.

*Open Issue – The text in 9.2 and Table 3 is new and needs to be checked for content, correctness and completeness. There may well be other ways that this information should be presented, or other information that should be included. This is just Tolmie's first cut at it.*

**Table 3 – Anti-aliasing tools summary**

Operation	Parameters	Anti-aliasing tools
<b>Connection operations</b>		
Request_Connection Connection_Answer Request_Disconnect Disconnect_Answer Disconnect_Complete	I-Port I-Key R-Port R-Key	<ol style="list-style-type: none"> <li>32-bit Key for each new Virtual Connection to a specific host will not duplicate a local Key value to this same host in use within the last 10 minutes.</li> <li>16-bit Port numbers, although more constrained in practice than Keys, provide an additional discrimination parameter.</li> </ol>
<b>Transfer operations</b>		
Request_To_Send Request_To_Receive Request_Memory_Region Memory_Region_Available Request_State Request_State_Response Request_Answer (to operations within this group) End End_Ack	I-id R-id	<ol style="list-style-type: none"> <li>32-bit I-id and R-id monotonically increase for each Transfer (associated with Read and Write sequences).</li> <li>32-bit I-id and R-id monotonically increase for each persistent memory region allocated (associated with Put, Get, and FetchOp sequences).</li> </ol>
<b>Block operations</b>		
Clear_To_Send Put Get FetchOp FetchOp_Complete Request_Answer (to operations within this group)	B_num F-id G-id	<ol style="list-style-type: none"> <li>32-bit B_num monotonically increases for each Block of a Transfer (associated with Read and Write sequences).</li> <li>The user should consider B_num aliasing if a Transfer contains more than <math>2^{32}</math> Blocks.</li> <li>32-bit B_num monotonically increases for each Put to a persistent memory region.</li> <li>32-bit F-id monotonically increases for each FetchOp to a persistent memory region.</li> <li>32-bit G-id monotonically increases for each Get to a persistent memory region.</li> </ol>
<b>STU operations</b>		
Data	STU_num	<ol style="list-style-type: none"> <li>16-bit STU_num monotonically increases for each STU of a Block</li> <li>STU_num cannot wrap within a Block</li> </ol>

**Table 4 – Connection management sequences**

Operation	Issued by	Op	Flags	Param	D_Port	S_Port	D_Key
<b><i>(C1) A Virtual Connection is set up between the Initiator and Responder end devices</i></b>							
Request_Connection	Initiator	x'01'	<b><i>FIO</i></b>	<b><i>I-Slots</i></b>	<b><i>R-Port</i></b>	<b><i>I-Port</i></b>	*
Connection_Answer	Responder	x'02'	<b><i>FIOR</i></b>	<b><i>R-Slots</i></b>	I-Port	<b><i>R-Port</i></b>	I-Key
<b><i>(C2) A Virtual Connection teardown sequence can be initiated from either end</i></b>							
Request_Disconnect	Initiator	x'03'	<b><i>I</i></b>	*	R-Port	I-Port	R-Key
Disconnect_Answer	Responder	x'04'	<b><i>I</i></b>	*	I-Port	R-Port	I-Key
Disconnect_Complete	Initiator	x'05'	<b><i>I</i></b>	*	R-Port	I-Port	R-Key
NOTE - The Initiator in C1 and C2 can be either party of the Virtual Connection.							

**Table 5 – Common control sequences**

Operation	Issued by	Op	Flags	Param	D_Port	S_Port	D_Key
<b><i>(Com1) Request Slot state: free Slots (a Transfer does not need to be in progress)</i></b>							
Request_State	Initiator	x'1C'	<b><i>I</i></b>	*	R-Port	I-Port	R-Key
Request_State_Response	Responder	x'1D'	<b><i>I</i></b>	<b><i>R-Slots</i></b>	I-Port	R-Port	I-Key
<b><i>(Com2) Request Transfer state: free Slots, highest Block received OK</i></b>							
Request_State	Initiator	x'1C'	<b><i>I</i></b>	*	R-Port	I-Port	R-Key
Request_State_Response	Responder	x'1D'	<b><i>I</i></b>	<b><i>R-Slots</i></b>	I-Port	R-Port	I-Key
<b><i>(Com3) Request Block state: free Slots, highest Block received OK, specific Block OK?</i></b>							
Request_State	Initiator	x'1C'	<b><i>I</i></b>	*	R-Port	I-Port	R-Key
Request_State_Response	Responder	x'1D'	<b><i>I</i></b>	<b><i>R-Slots</i></b>	I-Port	R-Port	I-Key
<b><i>(Com4) Terminate the Transfer and release resources</i></b>							
End	Initiator	x'1E'	<b><i>I</i></b>	*	R-Port	I-Port	R-Key
End_Ack	Responder	x'1F'	<b><i>I</i></b>	*	I-Port	R-Port	I-Key
NOTE - The Initiator in any sequence in this table can be either party of the Virtual Connection.							

Table 4 (cont.) – Connection management sequences

Cksum	B_id	Bufx	Offset	Sync	B_num	D_id	S_id
(See 5.1.1 page 8.)							
Cksum	<i>EtherType</i>	<i>I-Bufsize</i>	<i>I-Key</i>	<i>I-Max_STU</i>		*	*
Cksum	*	<i>R-Bufsize</i>	<i>R-Key</i>	<i>R-Max_STU</i>	*	*	*
(See 5.1.2 page 9)							
Cksum	*	*	I-Key	*	*	*	*
Cksum	*	*	R-Key	*	*	*	*
Cksum	*	*	I-Key	*	*	*	*

Table 5 (cont.) – Common control sequences

Cksum	B_id	Bufx	Offset	Sync	B_num	D_id	S_id
(See 6.1.1.1 page 12.)							
Cksum	*	*	*	<b>Sync</b>	*	x'FFFF'	*
Cksum	*	*	*	Sync	*	x'FFFF'	*
(See 6.1.1.2 page 12.)							
Cksum	*	*	*	<b>Sync</b>	x'FFFFFFFF'	R-id	I-id
Cksum	*	*	<b>B_seq</b>	Sync	x'FFFFFFFF'	I-id	R-id
(See 6.1.1.3 page 13.)							
Cksum	*	*	*	<b>Sync</b>	<b>B_num</b>	R-id	I-id
Cksum	*	*	<b>B_seq</b>	Sync	B_num	I-id	R-id
(See 6.1.1.4 page 14.)							
Cksum	*	*	*	*	*	R-id	I-id
Cksum	*	*	*	*	*	I-id	R-id

**Table 6 – Write sequences**

Operation	Issued by	Op	Flags	Param	D_Port	S_Port	D_Key
<b><i>(W1) The Initiator requests to write data (a Transfer) to the Responder</i></b>							
Request_To_Send	Initiator	x'16'	<i>ID</i>	<i>CTS_req</i>	R-Port	I-Port	R-Key
<Request_Answer>	Responder	x'17'	<i>IR</i>	*	I-Port	R-Port	I-Key
<b><i>(W2) The Responder exposes a memory region (a Block) to the Initiator</i></b>							
Clear_To_Send	Responder	x'1A'	<i>I</i>	<b>Blocksize</b>	I-Port	R-Port	I-Key
<b><i>(W3) The Initiator sends data (an STU) to the Responder</i></b>							
Data	Initiator	x'1B'	<i>TISLD</i>	<i>STU_num</i>	R-Port	I-Port	R-Key
<b><i>(W4) The Responder sends state information, if so requested in the Data operation</i></b>							
<Request_State_Response>	Responder	x'1D'	<i>I</i>	<i>R-Slots</i>	I-Port	R-Port	I-Key
NOTE - The Initiator in this table is the end device that issues the Request_To_Send operation.							

**Table 7 – Read sequences**

Operation	Issued by	Op	Flags	Param	D_Port	S_Port	D_Key
<b><i>(R1) The Initiator requests to read data (a Transfer) from the Responder</i></b>							
Request_To_Receive	Initiator	x'18'	<i>ID</i>	*	R-Port	I-Port	R-Key
<Request_Answer>	Responder	x'17'	<i>IR</i>	*	I-Port	R-Port	I-Key
<b><i>(R2) The Responder echoes the Initiator's request as a request to write data</i></b>							
Request_To_Send	Responder	x'16'	<i>ID</i>	<i>CTS_req</i>	I-Port	R-Port	I-Key
<Request_Answer>	Initiator	x'17'	<i>IR</i>	*	R-Port	I-Port	R-Key
<b><i>(R3) The Initiator exposes a memory region (a Block) to the Responder</i></b>							
Clear_To_Send	Initiator	x'1A'	<i>I</i>	<b>Blocksize</b>	R-Port	I-Port	R-Key
<b><i>(R4) The Responder sends data (an STU) to the Initiator</i></b>							
Data	Responder	x'1B'	<i>TISLD</i>	<i>STU_num</i>	I-Port	R-Port	I-Key
NOTE - The Initiator in this table is the end device that issues the Request_To_Receive operation.							

Table 6 (cont.) – Write sequences

Cksum	B_id	Bufx	Offset	Sync	B_num	D_id	S_id
(See 6.1.2 page 14.)							
Cksum	<b>Max_Block</b>	*	*	<i>T_len</i>		*	<i>I-id</i>
Cksum	*	*	*	*	*	I-id	*
(See 6.1.2 page 15.)							
Cksum	<b>R-Mx</b>	<b>R-Bufx</b>	<b>R-Offset</b>	<b>F-Offset</b>	<b>B_num</b>	I-id	<b>R-id</b>
(See 6.1.2 page 15.)							
Cksum	R-Mx	<b>R-Bufx</b>	<b>R-Offset</b>	<b>Sync</b>	B_num	R-id	<b>Opaque</b>
(See 6.1.2 page 16.)							
Cksum	*	*	<b>B_seq</b>	Sync	B_num	I-id	R-id

Table 7 (cont.) – Read sequences

Cksum	B_id	Bufx	Offset	Sync	B_num	D_id	S_id
(See 6.1.3 page 16.)							
Cksum	*	*	*	<i>T_len</i>		*	<i>I-id</i>
Cksum	*	*	*	*	*	I-id	*
(See 6.1.3 page 17.)							
Cksum	<b>Max_Block</b>	*	*	<i>T_len</i>		I-id	<b>R-id</b>
Cksum	*	*	*	*	*	R-id	*
(See 6.1.3 page 17.)							
Cksum	<b>I-Mx</b>	<b>I-Bufx</b>	<b>I-Offset</b>	<b>F-Offset</b>	<b>B_num</b>	R-id	I-id
(See 6.1.3 page 18.)							
Cksum	I-Mx	<b>I-Bufx</b>	<b>I-Offset</b>	<b>Sync</b>	B_num	I-id	*

**Table 8 – Put, Get, and FetchOp sequences**

Operation	Issued by	Op	Flags	Param	D_Port	S_Port	D_Key
<b><i>(PG1) The Initiator requests a persistent memory region on the Responder</i></b>							
Request_Memory_Region	Initiator	x'13'	<b><i>ID</i></b>	*	R-Port	I-Port	R-Key
<Request_Answer>	Responder	x'17'	<b><i>IR</i></b>	*	I-Port	R-Port	I-Key
<b><i>(PG2) The Responder allocates a persistent memory region (a Block) to the Initiator</i></b>							
Memory_Region_Available	Responder	x'14'	<b><i>I</i></b>	*	I-Port	R-Port	I-Key
<b><i>(PG3) The Initiator Puts data (an STU) in the Responder's persistent memory region</i></b>							
Data	Initiator	x'1B'	<b><i>TISLD</i></b>	<b><i>STU_num</i></b>	R-Port	I-Port	R-Key
<b><i>(PG4) The Responder sends state information, if so requested in the Data operation</i></b>							
<Request_State_Response>	Responder	x'1D'	<b><i>I</i></b>	<b><i>R-Slots</i></b>	I-Port	R-Port	I-Key
<b><i>(PG5) The Initiator Gets data from the Responder's persistent memory region</i></b>							
Get	Initiator	x'15'	<b><i>FID</i></b>	<b><i>T_len</i></b>	R-Port	I-Port	R-Key
<Request_Answer>	Responder	x'17'	<b><i>IR</i></b>	*	I-Port	R-Port	I-Key
Data	Responder	x'1B'	<b><i>TILD</i></b>	<b><i>STU_num</i></b>	I-Port	R-Port	I-Key
<b><i>(PG6) The Initiator fetches and operates on data in the Responder's persistent memory</i></b>							
FetchOp	Initiator	x'15'	<b><i>FID</i></b>	*	R-Port	I-Port	R-Key
<Request_Answer>	Responder	x'17'	<b><i>IR</i></b>	*	I-Port	R-Port	I-Key
Data	Responder	x'1B'	<b><i>TILD</i></b>	x'0000'	I-Port	R-Port	I-Key
FetchOp_Complete	Initiator	x'15'	<b><i>FI</i></b>	*	R-Port	I-Port	R-Key
NOTE - The Initiator is the end device that issues the Request_Memory_Region operation.							

**Table 8 (cont.) – Put, Get, and FetchOp sequences**

<b>Cksum</b>	<b>B_id</b>	<b>Bufx</b>	<b>Offset</b>	<b>Sync</b>	<b>B_num</b>	<b>D_id</b>	<b>S_id</b>
(See 6.1.4.1 page 19.)							
Cksum	*	*	*	<i>T_len</i>		*	<i>I-id</i>
Cksum	*	*	*	*	*	I-id	*
(See 6.1.4.1 page 19.)							
Cksum	<b>R-Mx</b>	<b>R-Bufx</b>	<b>R-Offset</b>	*	*	I-id	<b>R-id</b>
(See 6.1.4.2 page 20.)							
Cksum	R-Mx	<b>R-Bufx</b>	<b>R-Offset</b>	<b>Sync</b>	<b>B_num</b>	R-id	<b>Opaque</b>
(See 6.1.4.2 page 20.)							
Cksum	*	*	<b>B_seq</b>	Sync	B_num	I-id	R-id
(See 6.1.4.3 page 21.)							
Cksum	<b>I-Mx</b>	<b>R-Bufx</b>	<b>R-Offset</b>	<b>I-Bufx</b>	<b>I-Offset</b>	R-id	<b>G-id</b>
Cksum	*	*	*	*	*	G-id	R-id
Cksum	I-Mx	<b>I-Bufx</b>	<b>I-Offset</b>	<b>Sync</b>	*	G-id	R-id
(See 6.1.4.4 page 22.)							
Cksum	<b>I-Mx</b>	<b>R-Bufx</b>	<b>R-Offset</b>	<b>I-Bufx</b>	<b>I-Offset</b>	R-id	<b>F-id</b>
Cksum	*	*	*	*	*	F-id	R-id
Cksum	I-Mx	I-Bufx	I-Offset	<b>Sync</b>	*	F-id	R-id
Cksum	*	*	*	Sync	*	R-id	F-id

## 10 Error processing

Table 10 is a summary of the logged errors. The logging shall be on a per-Port basis, and shall be available to the ULP that is using the Port. The nature and size of the logs are system dependent. **The scheme for logging multiple errors in a single operation is implementation dependent.**

### 10.1 Operation timeout

Errors other than syntactic errors are manifested as missing operations, occurring when the underlying physical medium discard or damage a transmission. Such errors are detected by Op\_timeout, which is system and/or Port dependent. Op\_timeout\_Occurances shall be logged. Example means for determining the Op\_timeout value for a Virtual Connection **may include, or be the sum of:**

- a time longer than the measured round-trip time through the software path (use a Request\_State / Request\_State\_Response pair to measure on a per-Port basis); or
- a long fixed time period; or
- a time equal to the maximum queuing delay for a maximum size message (e.g., for a Control operation queued behind a large Transfer).

When reliable data movement operations are required by the ULP, each operation that expects a response shall be guarded with a timeout whose value is Op\_timeout. Data transmissions (i.e., Data operations) **associated with Read and Write sequences** are an exception to this timeout mechanism and are referred to the ULP for resolution (see 10.7.8 page 42). The ULP that issues a Clear\_To\_Send or a Request\_To\_Receive is responsible for timing out these operations. The ULP may or may not use Op\_timeout to time out Data operations.

Another system and/or Port dependent parameter, Max\_Retry, specifies the maximum number of times to retry an operation. If enabled, an operation **may** be re-tried up to Max\_Retry times if the sending end device does not receive the expected response (see table 9). If Max\_Retry is reached without success, then the operation is considered to be aborted and control

shall be passed to the ULP. Max\_Retry\_Occurances shall be logged.

### 10.2 Operation Pairs

Each Scheduled Transfer operation is defined as part of a two-way handshake or a three-way handshake. Thus, for each command operation there is a corresponding response operation, and for some response operations there is also a corresponding completion operation. Table 9 lists the operation pairs – command and response, or response and completion – that shall be retried if the associated response is not received within an Op\_timeout.

Additionally, Request\_State\_Response is a corresponding pair for Data operations which have Send\_State = 1. If the Request\_State\_Response is not received, then the data Source may send a Request\_State to obtain the state information.

**Table 9 – Operation pairs guarded by Op\_timeout with retry**

Operation	Response(s)
Data (w/ Send_State=1)	Request_State_Response
Disconnect_Answer	Disconnect_Complete
End	End_Ack
FetchOp	Data, or Request_Answer
Get	Data, or Request_Answer
Request_Connection	Connection_Answer
Request_Disconnect	Disconnect_Answer
Request_State	Request_State_Response
Request_To_Send	Request_Answer or Clear_To_Send
Request_To_Receive	Request_Answer or Request_To_Send

### 10.3 Duplicate operations

*Open Issue – Greg Chesson has an action item to draft some text on how you differentiate duplicate operations from legal operations.*

### 10.4 Checksum errors

If an erroneous checksum is detected (see 8.3.3 page 31), then the operation shall be discarded and a Cksum\_Error shall be logged.

### 10.5 Syntax errors

#### 10.5.1 Undefined Opcode

An operation with an undefined Opcode value shall be discarded, an Undefined Opcode\_Error shall be logged, and the Opcode shall be logged in Undefined Opcode\_Value.

#### 10.5.2 Unexpected Opcode

Most of the operations require previous operations to set up state on each device. If a device receives an out of sequence Opcode (e.g., receiving a Connection\_Answer without having sent the initiating Request\_Connection), the operation shall be discarded, an Unexpected Opcode\_Error shall be logged, and the Opcode shall be logged in Unexpected Opcode\_Value.

### 10.6 Virtual Connection errors

#### 10.6.1 Invalid Key or Port

All operations, excluding Request\_Connection and Disconnect operations, should have a Key (see 5.2.2 page 10) value that validates the operation for the Virtual Connection. Operations with an invalid Key shall not be executed, and an Invalid\_Key\_Error shall be logged.

All operations, excluding Disconnect operations, should have a valid Destination Port value (see 5.2.1 page 9). Operations with an invalid Destination Port value shall not be executed, and an Invalid\_Port\_Error shall be logged.

If a Request\_Disconnect is received and the Port and/or Key values are invalid, then a

Disconnect\_Answer shall be issued. If a Disconnect\_Answer is received and the Port and/or Key values are invalid, then a Disconnect\_Complete shall be issued. In both cases, the R-Port, I-Port, R-Key and I-Key, values in the received operation shall be used to form the operation issued.

#### NOTES

1 – Multiple contiguous invalid Key and/or Port values may indicate a problem with the link or a malicious host on the network. The supervising process should be informed.

2 – Since the Disconnect operations have a complete set of parameters for both the Initiator and Responder, legal Disconnect responses can be generated, even if one end device has lost the state information for the Virtual Connection, e.g., due to a power-down.

#### 10.6.2 Slots exceeded

Operations that exceed the number of Slots (see 5.2.5 page 10) for the Virtual Connection may not be executed, and a Slots\_Exceeded\_Error shall be logged.

#### 10.6.3 Unknown EtherType

If a Request\_Connection operation contains an unknown EtherType (see 5.2.6 page 11), the receiver shall issue a Connection\_Answer with Reject = 1 and shall log an Unknown\_EtherType\_Error.

#### 10.6.4 Illegal Bufsize

If a Request\_Connection contains a Bufsize (see 5.2.3 page 10) value that is  $< 8$  or  $> 63$ , (i.e., Buffer size  $< 2^8$  bytes, or  $> 2^{63}$  bytes), then the receiver shall respond with a Connection\_Answer with Reject = 1. If a Connection\_Answer contains a Bufsize value that is  $< 8$  or  $> 63$ , then the receiver shall respond with a Request\_Disconnect. In either case, an Illegal\_Bufsize\_Error shall be logged.

#### 10.6.5 Illegal STU size

The maximum STU sizes (Max\_STU) for each end device were determined during the Virtual Connection setup (see 5.1.1 page 8, and 5.2.4 page 10). If the received STU in a Data operation is greater than the maximum STU size,

then the STU shall be discarded and an Illegal\_STU\_Size\_Error shall be logged.

## 10.7 Scheduled Transfer errors

### 10.7.1 Invalid sequence identifier

Many Scheduled Transfer operations use a sequence identifier (see 6.2.1 page 23), in the D\_id field for quickly accessing state information for a sequence. An operation with an invalid sequence identifier shall be discarded and an Invalid\_D-id\_Error shall be logged.

### 10.7.2 Invalid Memory Index (Mx)

Data operations echo previously assigned Memory Index (Mx) values (see 6.2.2 page 24). If the Mx value in a Data operation does not match a valid Mx value, then the Data operation shall be discarded and an Invalid\_Mx\_Error shall be logged.

### 10.7.3 Bad Data Channel specification

During Request\_To\_Send, Request\_To\_Receive, and Request\_Memory\_Region operations, the initiating device declares the LLP Data Channel that will carry Data operations for the sequence. Some Data Channels may not be available for Data operations depending on the LLP (e.g., b'00' is not a valid choice on HIPPI-6400 as it indicates VC0 which is reserved for Control operations). If the Data Channel value is in error, then the receiver shall issue an appropriate response with Reject = 1.

### 10.7.4 Out of Range B\_num, Bufx, Offset, or STU\_num

If the Block number (see 6.2.4 page 24) in a received:

- Clear\_To\_Send operation is outside the calculated number of Blocks for the Transfer;
- Data or Request\_State operation, to other than a persistent memory region, has not been previously exposed by a Clear\_To\_Send operation;

then the offending operation shall be discarded and an Out\_Of\_Range\_B\_num\_Error shall be logged.

If a Data, Get, or FetchOp operation contains a Bufx and/or Offset (see 6.2.8 page 25) that exceeds the buffer range allocated by the data Destination, then the receiver shall discard the operation and shall log an Out\_Of\_Range\_Bufx\_Error.

If a Data, Get, or FetchOp operation contains an Offset (see 6.2.8 page 25) larger than the buffer size, the receiver shall discard the operation and shall log an Oversized\_Offset\_Error.

If a Data operation contains an STU\_num (see 6.2.7 page 25) that is not one greater than the previous STU for this Block, then the STU is out of order. The receiver may discard the STU and log an Out\_Of\_Order\_STU\_Error if it cannot accommodate out of order STU delivery.

### 10.7.5 Block out of order error

If a Data operation contains a B\_num that is not one greater than the previous B\_num for this Transfer or persistent memory region, and Out\_of\_Order (see 8.2 page 30) capability was not specified during the Virtual Connection setup (see 5.1.1 page 8), then the data Destination shall log an Out\_Of\_Order\_B\_num and may terminate the Transfer or persistent memory region with an End sequence.

### 10.7.6 Illegal Blocksize

If a Clear\_To\_Send operation contains a Blocksize (see 6.2.6 page 25) value that is < 8 or > 48, (i.e., Block size < 2<sup>8</sup> bytes, or > 2<sup>48</sup> bytes), then the receiver shall discard the operation and shall log an Illegal\_Blocksize\_Error.

### 10.7.7 Undefined Flag

If a received operation contains a flag value that is not defined for that operation, then the flag shall be ignored and an Improper\_Flag\_Use\_Error should be logged.

### 10.7.8 Missing Blocks

If the data Destination detects that a Block of a Transfer is missing, it may re-issue the associated Clear\_To\_Send operation to request retransmission of the Block from a data Source that supports Out\_of\_Order (see 5.1.1 page 8,

and 8.2 page 30). Other actions to be taken if a Block is missing are beyond the scope of this standard.

**Table 10 – Summary of logged errors**

<b>Name</b>	<b>Occurs in operation</b>
Cksum_Error	all
Illegal_Blocksize_Error	CTS
Illegal_Bufsize_Error	CA, RC
Illegal_STU_Size_Error	Data
Improper_Flag_Use_Error	all
Invalid_D-id_Error	all with a non-zero D_id
Invalid_Key_Error	all except RC
Invalid_Mx_Error	Data
Invalid_Port_Error	all
Max_Retry_Occurance	End, DA, RC, RD, RS, RTR, RTS
Op_timeout_Occurance	End, DA, RC, RD, RS, RTR, RTS
Out_Of_Order_B_num	Data
Out_Of_Order_STU_Error	Data
Out_Of_Range_B_num_Error	CTS, Data, RS, RSR
Out_Of_Range_Bufx_Error	Data
Oversized_Offset_Error	Data
Slots_Exceeded_Error	all with Op ≥ x'06'
Undefined_Opcode_Error	not applicable
Undefined_Opcode_Value	not applicable
Unexpected_Opcode_Error	all except RC
Unexpected_Opcode_Value	all except RC
Unknown_EtherType_Error	RC
Operation abbreviations: CA = Connection_Answer CTS = Clear_To_Send DA = Disconnect_Answer RC = Request_Connection RD = Request_Disconnect RS = Request_State RSR = Request_State_Response RTR = Request_To_Receive RTS = Request_To_Send	

## Annex A (normative)

### Using lower layer protocols

This Scheduled Transfer Protocol (ST) may be used with a variety of lower layer and physical media protocols. Mappings to some of the more common protocols are specified below. This is not intended to be an all inclusive set of protocols, i.e., ST may be used with other LLPs than those listed. Specific items addressed by each mapping include:

- **Connection control information (CCI)**, such as physical layer addresses,
- protocol data unit (PDU) size restrictions,
- and the mappings for the ST Control and Data Channels.

However, the methods used to pass this information between ST and the LLP are outside the scope of this standard.

For Request\_Connection and Connection\_Answer operations, the CCI for the Virtual Connection being set up is passed to the specified LLP and may be stored in the Virtual Connection Descriptor (see figure 5 page 7). Examples of CCI parameters include, but are not limited to:

- LLP-specific destination address;
- LLP-specific source address;
- quality of service.

ST does not provide the initial CCI; it may come from the ULP or from another protocol. The CCI is not carried in the Schedule Header. However, an ST implementation would typically retain the CCI for further operations on the Port. Note that some situations, e.g., striping, may use other than the retained addresses.

#### A.1 HIPPI-6400-PH as the LLP

ANSI X3.xxx defines HIPPI-6400-PH, portions of which are repeated here as an aid to the reader. As shown in figure A.1, ST operations shall be

carried over HIPPI-6400-PH with the first eight bytes of the Schedule Header occupying the last eight bytes of the HIPPI-6400-PH Header micropacket.

HIPPI-6400-PH specifies that its ULP (ST in this case), provide information to be used to generate the MAC header. The ULP provides the Destination address (D\_ULA) in a Request\_Connection operation, and may provide the Source address (S\_ULA). In the corresponding Connection\_Answer, the received S\_ULA would be used as the D\_ULA, and the ULP may provide the S\_ULA value. (See HIPPI-6400-PH 5.3.1.) In ST these parameters are in the CCI. Included are:

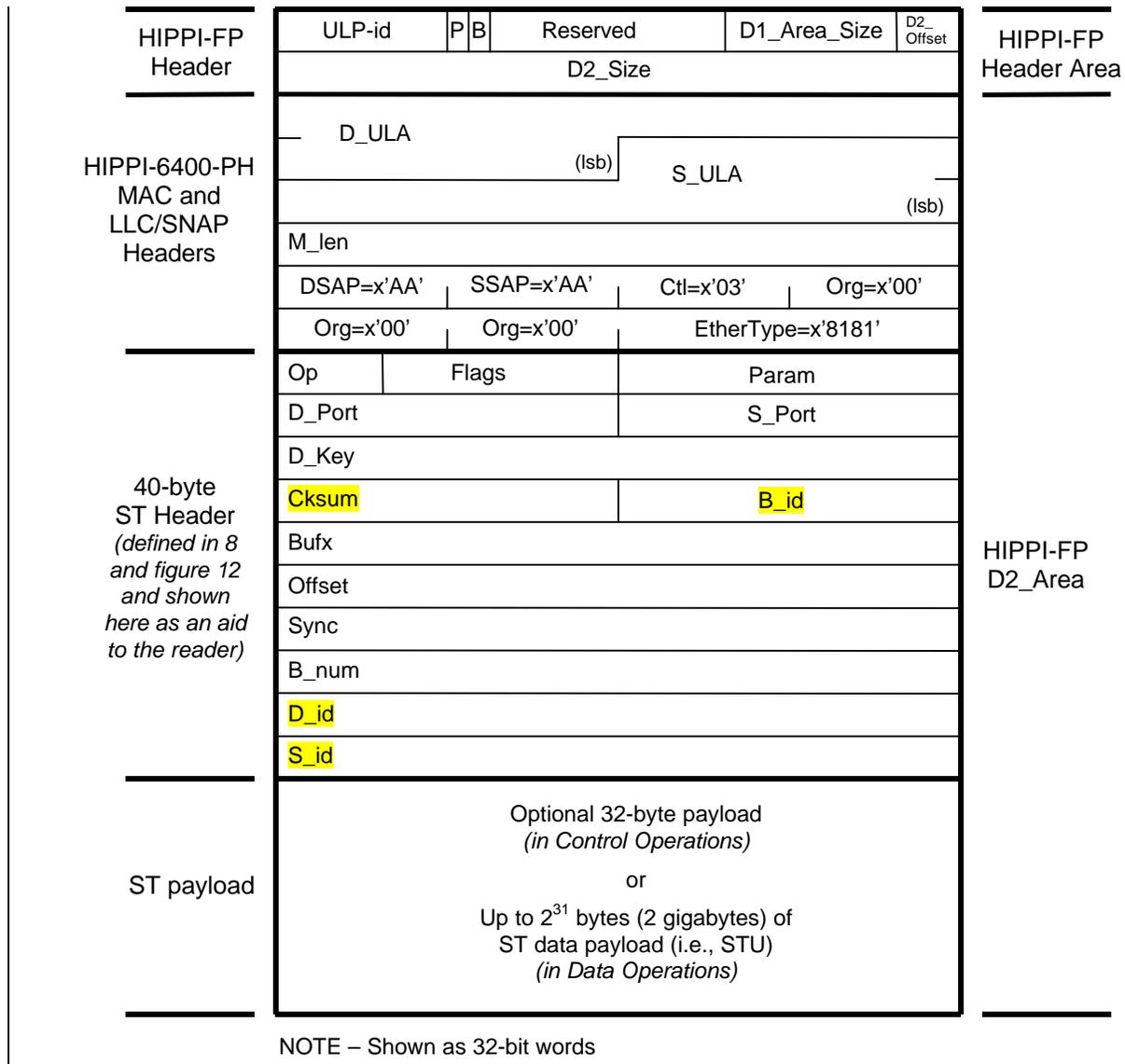
- LLC/SNAP header with:
  - DSAP = x'AA' (SNAP);
  - SSAP = x'AA' (SNAP);
  - Ctl = x'03' (unnumbered packets);
  - Org = x'00', x'00', x'00' (generic packets);
  - EtherType = x'8181' (Scheduled Transfer).
- Destination physical address (D\_ULA),
- optionally the Source physical address (S\_ULA).

All ST Control operations shall be carried on HIPPI-6400-PH Virtual Channel VC0. Data operations shall use Virtual Channel 1, 2, or 3 as specified in the ST Data Channel Assignment flag bits (see 8.2 page 30).

The buffers available in some HIPPI-6400-PH intermediate device implementations (e.g., for translators, routers, etc.), may limit the STU and Block sizes. These size restrictions shall be resolved with the Max\_STU and Max\_Block parameters (see 5.2.4 page 10, and 6.2.5 page 24).

M\_len (in the HIPPI-6400-PH MAC Header), specifies the number of bytes following M\_len, exclusive of any padding in the last micropacket. Hence, M\_len will have the following values:





**Figure A.2 – An ST operation carried in a HIPPI-FP packet**

The HIPPI-6400-PH MAC and LLC/SNAP Headers are defined in ANSI X3.xxx, portions of which are repeated here as an aid to the reader. As shown in figure A.2, the MAC and LLC/SNAP headers shall precede the Schedule Header to facilitate translation to other protocols. The same CCI information as specified in A.1 above shall be used to create the MAC and LLC/SNAP headers. The following additional parameters shall be included in the CCI for use by the HIPPI-FP protocol:

- ULP-id = x'0C' signifying HIPPI-6400 Encapsulation,

- 12-bit Destination Addresses as specified by ANSI X3.222, High-Performance Parallel Interface - Physical Switch Control (HIPPI-SC).

All ST Control operations shall specify Virtual Channel VC0. Data operations shall specify Virtual Channel 1, 2, or 3 as specified in the ST Data Channel Assignment flag bits (see 8.2 page 30) and carried in a Request\_To\_Send operation (see 6.1.2 page 14, and 6.1.3 page 17). Note that HIPPI-FP and ANSI X3.183, High-Performance Parallel Interface – Mechanical, Electrical, and Signalling Protocol Specification

(HIPPI-PH), also known as HIPPI-800, do not provide multiple channels. Therefore, all of the Control and Data operations share the HIPPI-800 physical link, and a long Data operation can block delivery of Control operations until the Data operation completes. **Small STUs may be used to avoid having a large STU delay a Control operation.** The Virtual Channel specifications are needed when going from HIPPI-800 to HIPPI-6400.

The buffers available in some HIPPI-FP intermediate device implementations (e.g., for translators, routers, etc.), may limit the STU and Block sizes. These size restrictions shall be resolved with the Max\_STU and Max\_Block parameters (see 5.2.4 page 10, and 6.2.5 page 24).

### A.3 Ethernet as the LLP

Figure A.3 shows a 40-byte ST header immediately following the 14-byte Ethernet 802.3 MAC header and 8-byte 802.1 SNAP header. An 802.3 MAC header is needed because the ST header does not contain a length field, and because the number of non-pad bytes in an Ethernet frame cannot be inferred from the physical frame size. The SNAP header is required because the ST header does not contain an EtherType. The SNAP header values shall be identical to those defined for use with HIPPI-6400-PH as the LLP (see A.1 page 44), i.e.,:

- DSAP = x'AA' (SNAP);
- SSAP = x'AA' (SNAP);
- Ctl = x'03' (unnumbered packets);
- Org = x'00', x'00', x'00' (generic packets);
- EtherType = x'8181' (Scheduled Transfer).

The payload bytes are optional. If the ST header contains a Control operation, then the payload shall be either zero bytes or 32 bytes (see 4.2 page 5). If the ST header contains a Data

operation, then the payload can be any size up to 1024 bytes.

Ethernet frames must be an even number of bytes and must also satisfy a minimum length requirement. In conventional implementations these constraints are satisfied by logic in low-level device drivers as well as physical-layer hardware. Since ST is an upper-layer client of the physical layer, the padding details are not described here.

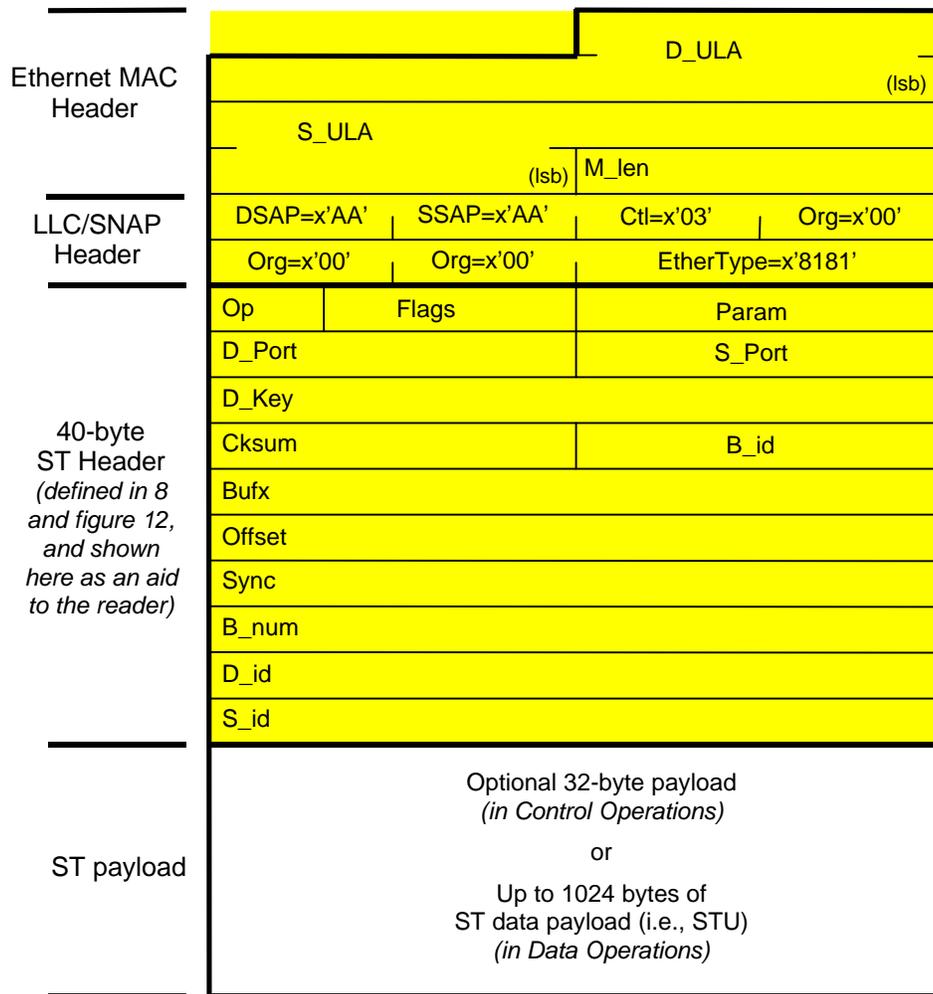
The length of an ST message on Ethernet is determined from the 16-bit 802.3 length field which specifies the number of bytes following the length field exclusive of any padding. Possible values for the length are:

- len = 48 for Control operations without an optional payload.
- len = 80 for Control operations with payload
- len = 48 + (size of payload) for Data operations

The EtherTypes used with ST on Ethernet shall be as specified in 5.2.6 page 11.

Ethernet does not provide virtual circuits or virtual channels, therefore both Control and Data operations are queued and processed in FIFO order. Control operations and Data operations may be interleaved: there is no requirement for all the STUs of a multi-STU Block to be transmitted contiguously on the medium. **It may be desirable to use small STUs to avoid having a large STU delay a Control operation.**

ST requires in-order transmission and delivery of STUs within a Block. It is expected that many implementations will handle out-of-order Blocks. If an Ethernet environment cannot preserve STU ordering, then the Blocksize (see 6.2.6 page 25) should be set to 1024 bytes, making use of out-of-order Block processing.



NOTE – Shown as 32-bit words

**Figure A.3 – An ST operation carried in an Ethernet packet**

*Open Issue – While clause A.4, the ST over ATM definition, has not been included in the document before, it has been available and reviewed as a separate document. Hence, it is not completely highlighted or marked with margin bars in this revision; only changes since the review at the January meeting are so marked.*

#### A.4 ATM LAN Emulation as the LLP

The ATM Forum specifies the emulation of IEEE 802 LANs using ATM networks (AF-LANE-0021.000, January, 1995; AF-LANE-0084.000, July 1997). This method is used for the configuration and operation of emulated LANs over which ST packets can be transmitted. Additionally, LAN Emulation (LANE) provides for virtual LANs that may span networks other than ATM using bridge technology. These other networks are referred to as "legacy LANs" in the specification and include Ethernet and Token Ring. It is feasible that this bridging method could be extended to other networks, HIPPI-6400-PH in particular. While the definitive specifications are in the LANE documents, parts of LANE are included here as an aid to the reader.

Two types of LANs are emulated: Ethernet and Token Ring. For Ethernet, both the original "DIX" (Digital/Intel/Xerox) Ethernet standard, and the IEEE 802.3 LAN standard are emulated. For Token Ring, both the 4 Mbit/s 802.5 and the 16 Mbit/s 802/5 standards are emulated. ST implementations do not need to support all of these LAN types to be compliant. For each of these four network types, the encapsulation as well as the minimum and maximum frame sizes are respected to enable bridging to real LANs of the same type.

For emulated LANs that are deployed on homogeneous ATM networks, a frame size as defined in RFC 1626 ("Default IP MTU for use over ATM AAL5") is specified using the 802.3 LAN encapsulation. The configuration, management, and control of emulated LANs are described in the ATM LANE specifications.

The CCI information that needs to be passed from ST to the LAN emulation layer includes the EtherType = x'8181', the destination address, and optionally the source address. How the addresses are obtained is outside the scope of this document. It should be noted, however, that LAN emulation provides a broadcast service upon which upper level address resolution can be based. Furthermore, the resolution of MAC addresses to ATM addresses is handled transparently by the LAN emulation layer.

ST defines Control Channels and Data Channels. ATM Virtual Connections (VC's) are used to implement these channels. An implementation of ST over ATM should provision these VC's to respect their respective usage: low latency message transmission and reception for the Control Channel, high bandwidth for the Data Channel. The ATM Forum LAN Emulation Standard, Version 2 provides for multiple VC's per LAN Emulation Client (LEC). However, the upper level protocol implementation (ST in this case) must be aware of this facility in order to request and use it.

The ST protocol enables high performance implementations where the sending/receiving network interface adapters can directly transfer data to/from the network from/to application buffers with a minimum of processor intervention. Any implementation of ST over ATM should preserve this property. More specifically, network interface adapters should:

- directly perform the high frequency operations of the ST protocol (e.g. Slot accounting);
- alternate the transmission of control and data transfers so as to reduce the latency of control messages;
- when the ATM network interface adapter is used for ST traffic as well as other protocols, discriminate between ST traffic and other traffic. In general, the manner in which data is moved to/from memory from/to the network by the adapter will depend on the protocol encapsulated by the frames. The ATM VC number may be used if VC's are dedicated to particular protocols. Otherwise, the frame contents need be examined.

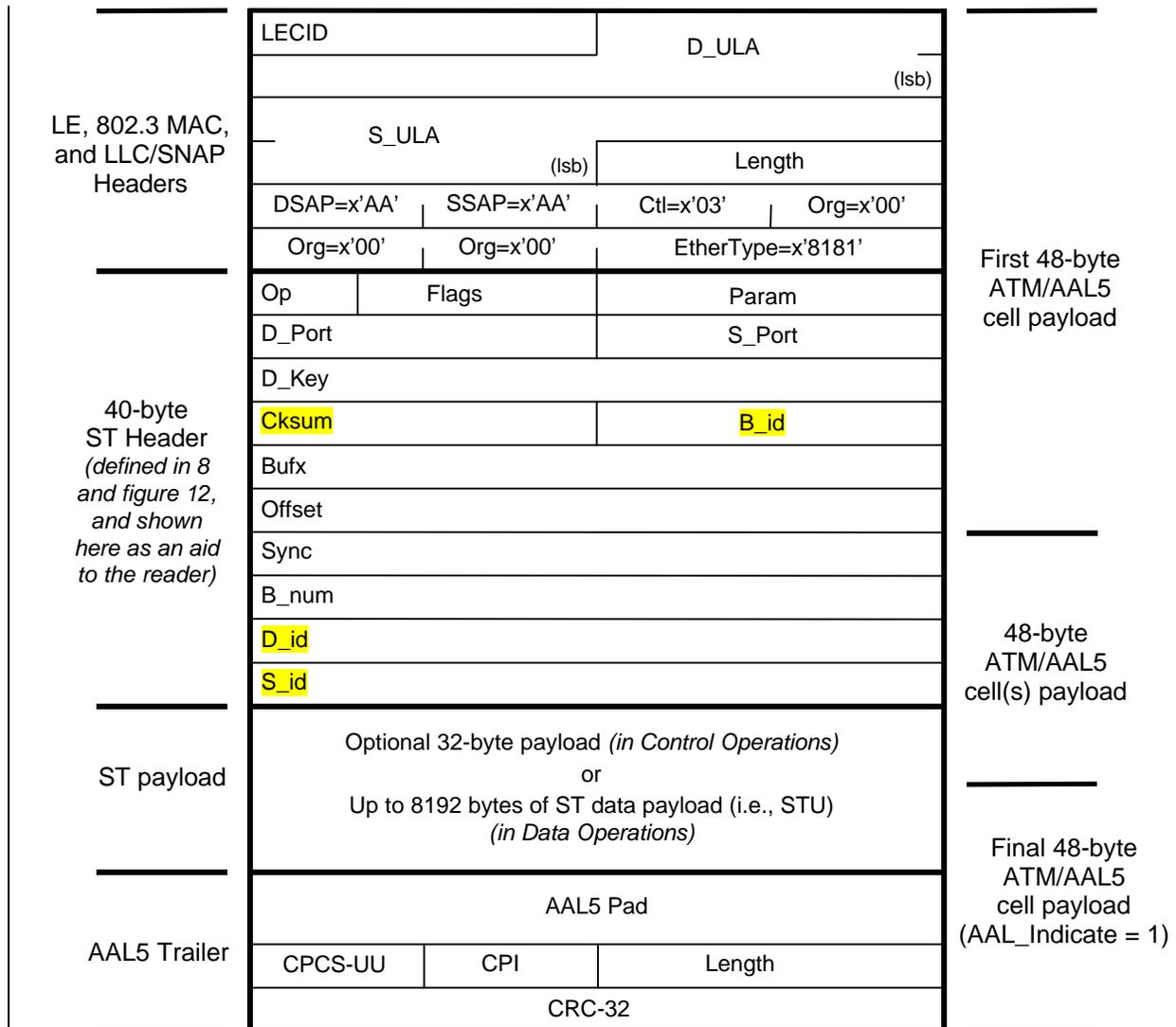
Acronyms specific to ATM and ATM LANE are specified in the ATM LANE specifications. Those used in this annex are repeated here as an aid to the reader.

<b>ATM</b>	Asynchronous Transfer Mode
<b>CPCS-UU</b>	Common Part, Convergence Sublayer - User-to-User Indication
<b>CPI</b>	Common Part Indicator
<b>CRC-32</b>	32-bit Cyclic Redundancy Check
<b>DIX</b>	Digital / Intel / Xerox
<b>LECID</b>	LAN Emulation Client Identifier
<b>LANE</b>	LAN Emulation

### A.4.1 Mapping to the IEEE 802.3 LAN standard

Figure A.5 shows the mapping for carrying ST over ATM for IEEE 802.3 networks. The 16-bit Length parameter in the 802.3 MAC Header includes the LLC/SNAP Header, the ST Header, and any ST payload. This Length parameter

shall be passed to the ULP, and used by the ULP to determine the size of the payload. The maximum frame size for IEEE 802.3 encapsulation, specified in RFC 1626, is 9234 bytes. Since ST specifies maximum STU size as a power of 2, this results in a maximum STU of 8192 bytes.



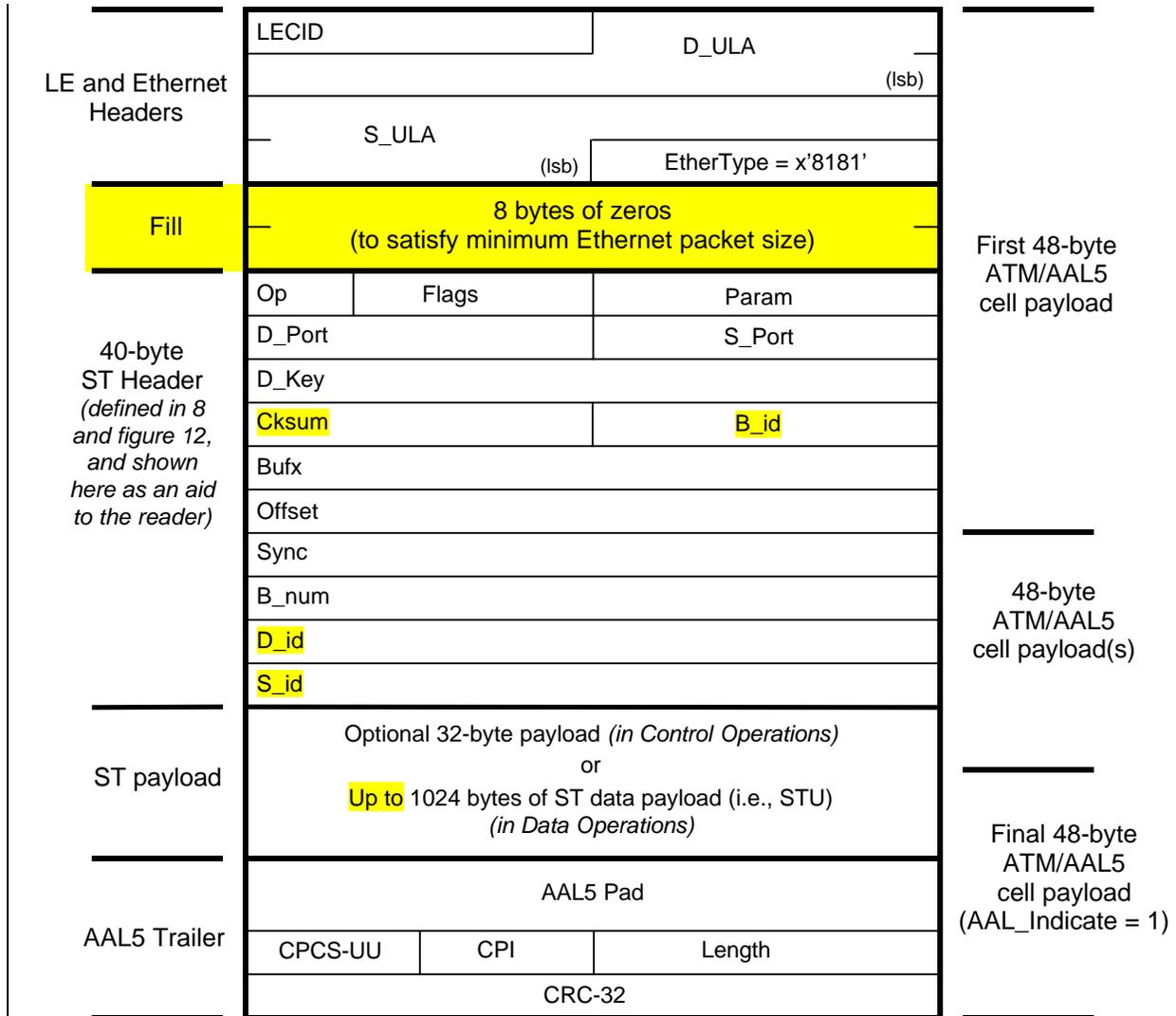
NOTE – Shown as 32-bit words

Figure A.4 – An ST operation carried in an ATM LANE IEEE 802.3 network emulation frame

### A.4.2 Mapping to the DIX Ethernet standard

Figure A.5 shows the mapping for carrying ST over ATM for original "DIX" (Digital/Intel/Xerox) Ethernet standard networks.

The minimum Ethernet data frame size is 46 bytes, six bytes more than the 40 byte ST header. Hence, eight bytes of zero fill shall be placed before the ST Header (necessary for Control operations, and done for Data operations for consistency). The ST payload in Data operations shall be  $\leq 1024$  bytes.



NOTE – Shown as 32-bit words

Figure A.5 – An ST operation carried in an ATM LANE DIX Ethernet emulation frame

### A.4.3 Mapping to Token Ring networks

Figure A.6 shows the mapping for carrying ST over ATM for IEEE 802.5 (4 Mbit/s) and 802/5 (16 Mbit/s) Token Ring networks. The AC Pad is not used and shall be ignored. The FC field is set according to AF-LANE-0021 and IEEE 802.5-

1992, and shall specify an LLC frame. The optional Token Ring Routing Information Field (16 to 46) bytes may be present. The LLC/SNAP values shall be determined by the LLC Layer. The maximum ST data payload for a 4 Mbit/s 802.5 Token Ring is 4096 bytes; for a 16 Mbit/s 802/5 Token Ring it is 16,384 bytes.

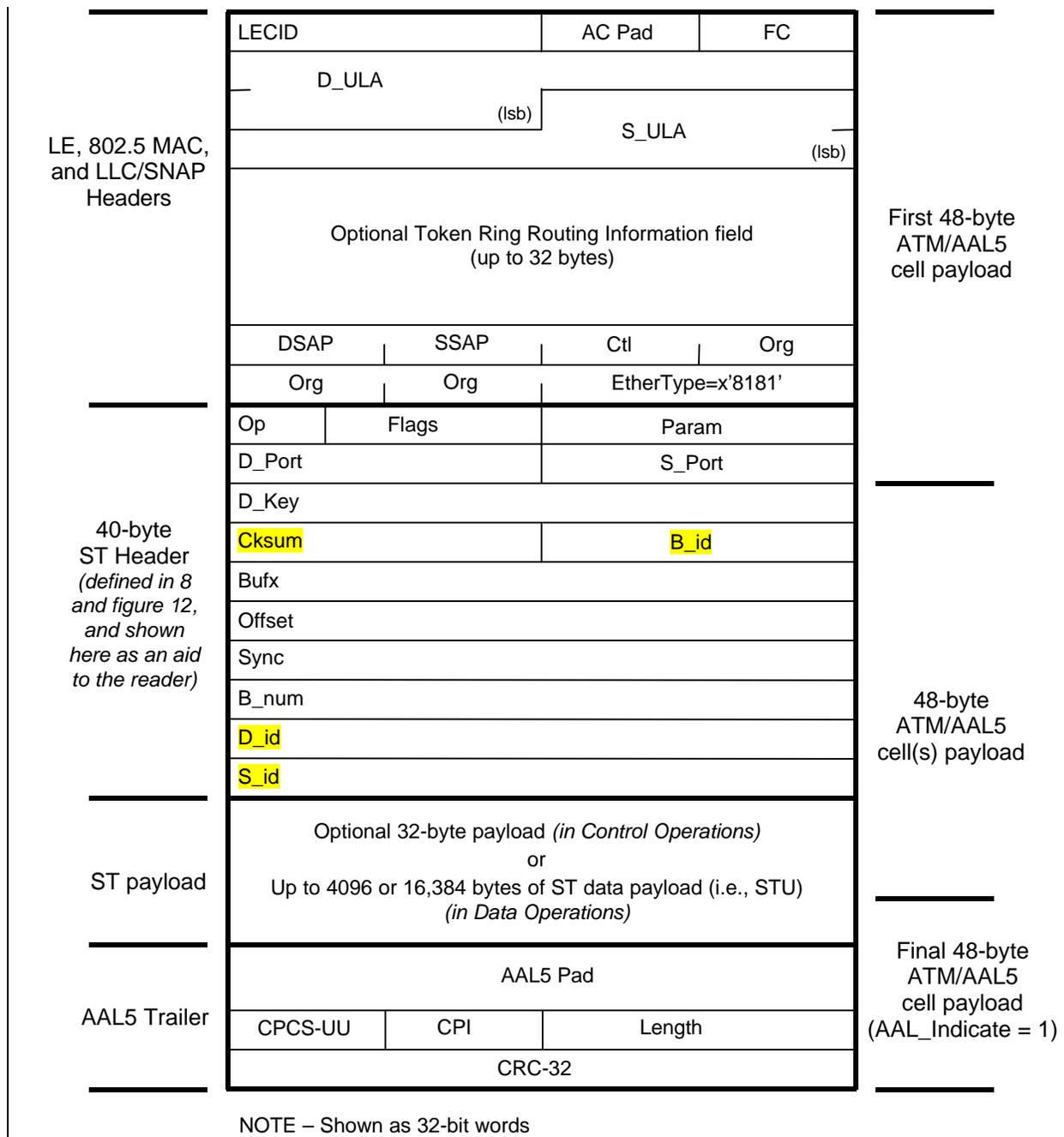


Figure A.6 – An ST operation carried in an ATM LANE IEEE 802.5 emulated frames

## A.5 Fibre Channel as the LLP

*Open Issue – This section is based on Jerry Leitherer's previous work. It needs to be carefully reviewed to make sure that Don Tolmie did not introduce inadvertent errors, or delete something critical.*

ANSI X3.230-1994 and ANSI X3.230/AM1:1996 specify Fibre Channel – Physical Interface (FC-PH), the Fibre Channel (FC) link and protocol layers. Within FC-PH, Annex S contains the service interface, connecting to FC-PH ULPs. One such ULP is ANSI X3.287-1996, Fibre Channel – Link Encapsulation (FC-LE), which specifies ISO/IEC 8802-2 (ANSI/IEEE 802.2) Logical Link Control Protocol Data Units over FC. Scheduled Transfer is a ULP on top of FC-LE.

One ST Data or Control operation shall be transmitted as one FC Sequence. An FC Sequence consists of one or more FC frames. FC frames can carry up to 2112 bytes of payload. A Data operation (an STU), shall be  $\leq 2^{23}$  bytes. This size is derived from a minimum allowable "maximum frame payload size" of 128 bytes (FC implementations are not required to support the 2112 byte maximum size), and the 16-bit FC SEQ\_CNT parameter which increments for each frame of a Sequence.

An ST Block consists of one or more STUs, hence one or more FC Sequences. Out of order STUs (i.e., FC Sequences) within a Block may be discarded (see 10.7.4 page 42). Sequence ordering (i.e., STU ordering), will be maintained if an FC Class 1 or Class 2 device supports Discard multiple Sequences with retransmission Error Policy. If Discard multiple Sequences with retransmission Error Policy is not used, or Class 3 is used, then the Blocksize shall be the same as the STU size (i.e., one STU per Block).

An ST Transfer (associated with Read and Write sequences), may use one or more FC-PH Exchanges. An FC-PH Exchange consists of one or more related non-concurrent FC Sequences, hence multiple Exchanges permit concurrent Block transfers.

When transporting ST, FC Class 1, Class 2, or Class 3 service may be used on one or more FC links. When more than one FC link is employed, striping should follow the principles outlined in Annex B (see page 55). FC Class 1, 2, and 3, do

not provide virtual circuits or virtual channels. Sequences are queued and processed in FIFO order when a single Class of service and one physical FC link is employed. More than one Class of service may be used to create behavior similar to virtual channels.

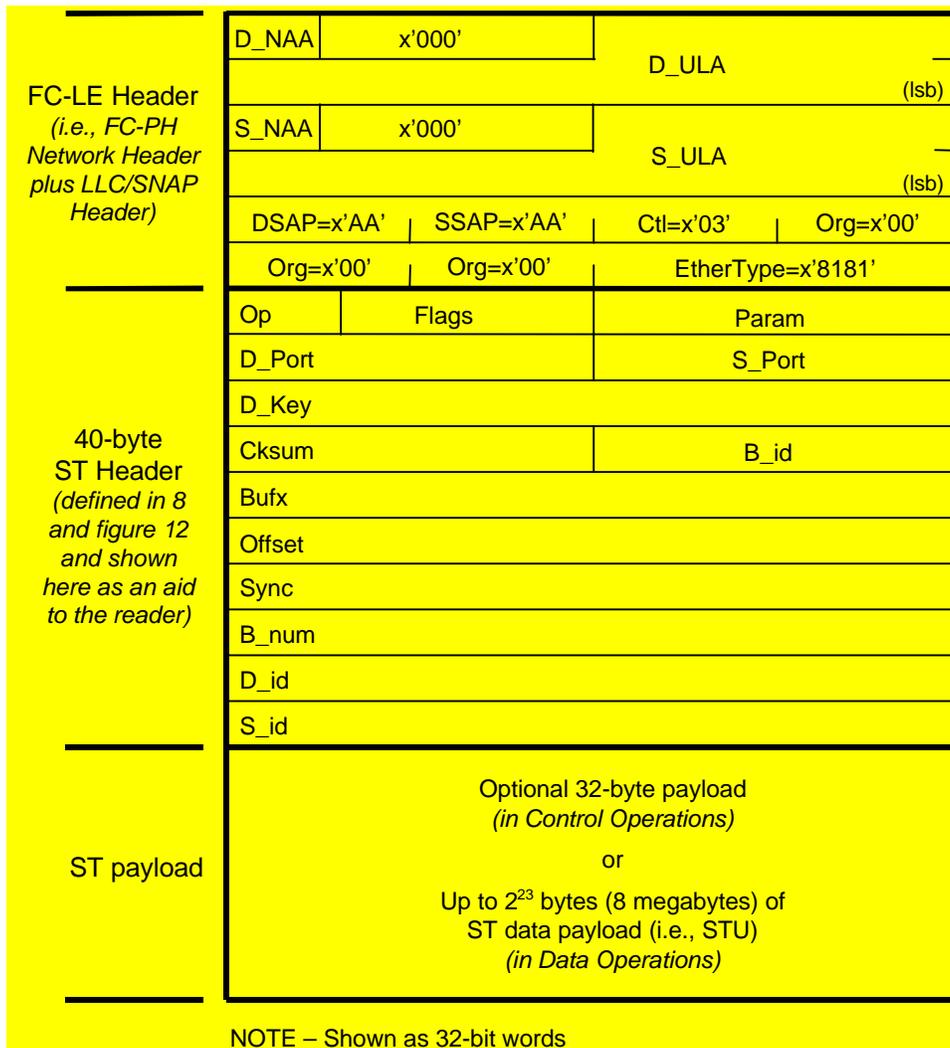
- Class 1 provides a connection oriented, dedicated link, between two end devices with Sequences transmitted in FIFO order. A Class 1 hazard is that an ST Control operation could get queued and delayed behind a long data Transfer.

- Class 2 and Class 3 are connectionless; multiplexing frames, on frame boundaries, to multiple end devices or a single end device. Class 2 and Class 3 allow for concurrent transfer of data and Control operations. Class 2 provides acknowledgement upon delivery. Class 3 provides datagram service, i.e., unacknowledged delivery.

- Class 1 with the Intermix option permits Class 2 and Class 3 frames to be interleaved, on frame boundaries, with Class 1 frames.

FC-PH uses 24-bit addresses for routing within a FC fabric region (i.e., an FC LAN segment). Address resolution between the 24-bit addresses and the 48-bit ULAs is addressed in FC-LE. The implementation of these services is outside the scope of this document. Although FC-LE encapsulation of ST permits bridging and routing, no attempt is made to describe bridging or routing beyond a fabric region as described for FC-LE.

Figure A.7 shows the ST Header following the FC-LE Header, which is composed of the FC-PH Network Header (optional in FC-PH), and the LLC/SNAP Header. In the FC-LE Header, D\_NAA = x'1' and S\_NAA = x'1', i.e., the D\_ULA and S\_ULA are IEEE 802.1A 48-bit addresses.



**Figure A.7 – An ST operation carried in a Fibre Channel Sequence**

## Annex B (informative)

### ST striping

#### B.1 Striping principles

ST is capable of supporting multiple physical interfaces for a single Transfer (see figure B.1). This striping capability may be of benefit when a single interface is not able to support required data rates. It may be especially useful where data is moved from many slower interfaces to a single faster interface or vice-versa. It may also be used with multiple interfaces at both the Source and Destination. Mechanisms to set up, select, and control the underlying physical interfaces are beyond the scope of this standard.

The Block is the basic striping unit. Each Block contains sufficient information to completely identify an individual Transfer and the Block's location within the Transfer. The only difference between striped and non-striped operation is the selection of port MAC addresses to allow concurrent data movement. Striping is not done on an STU basis because striped STUs can not be guaranteed to be delivered in-order as required by ST.

There are a few conventions that should be followed to facilitate striping:

- Block sizes (when striping is desirable) must be small enough to support concurrency and allow each channel to have at least one Block to send.
- Sufficient Clear\_To\_Send operations should be kept outstanding by a data receiver to allow concurrent Data operations.
- The interface adapter(s) must be capable of handling multiple Blocks simultaneously. This may require communication between interfaces (or their software drivers) within a system.
- The return physical address (e.g., Source ULA), for each operation is specified by the LLP source address for that operation. ST implementers should not assume that the source LLP address for a given Port will remain constant.

- The Destination must signify that it supports delivery of Blocks in any order (i.e., Out\_of\_Order = 1, see 8.2 page 30) during the Virtual Connection setup.

#### B.2 Many-to-one striping

Part a of figure B.1 shows using a number of lower-throughput interfaces, aggregated together, to communicate with one higher-throughput interface (using a translator or bridge). Striping the lower-throughput interfaces together can allow legacy systems to communicate quickly over newer network infrastructures. In this case, action to implement striping is required only on the side of the lower-throughput interface.

After the Ports are assigned, data movement is initiated with a Request\_To\_Send operation. As Clear\_To\_Send operations are received, the system with multiple lower-throughput ports can move a Block of data for each Clear\_To\_Send received. As many Blocks can be in transit concurrently as there are ports to carry them and Clear\_To\_Send operations authorizing them.

The system receiving these Blocks processes them normally, placing them into memory as their Bufx and Offset values dictate.

#### B.3 One-to-many striping

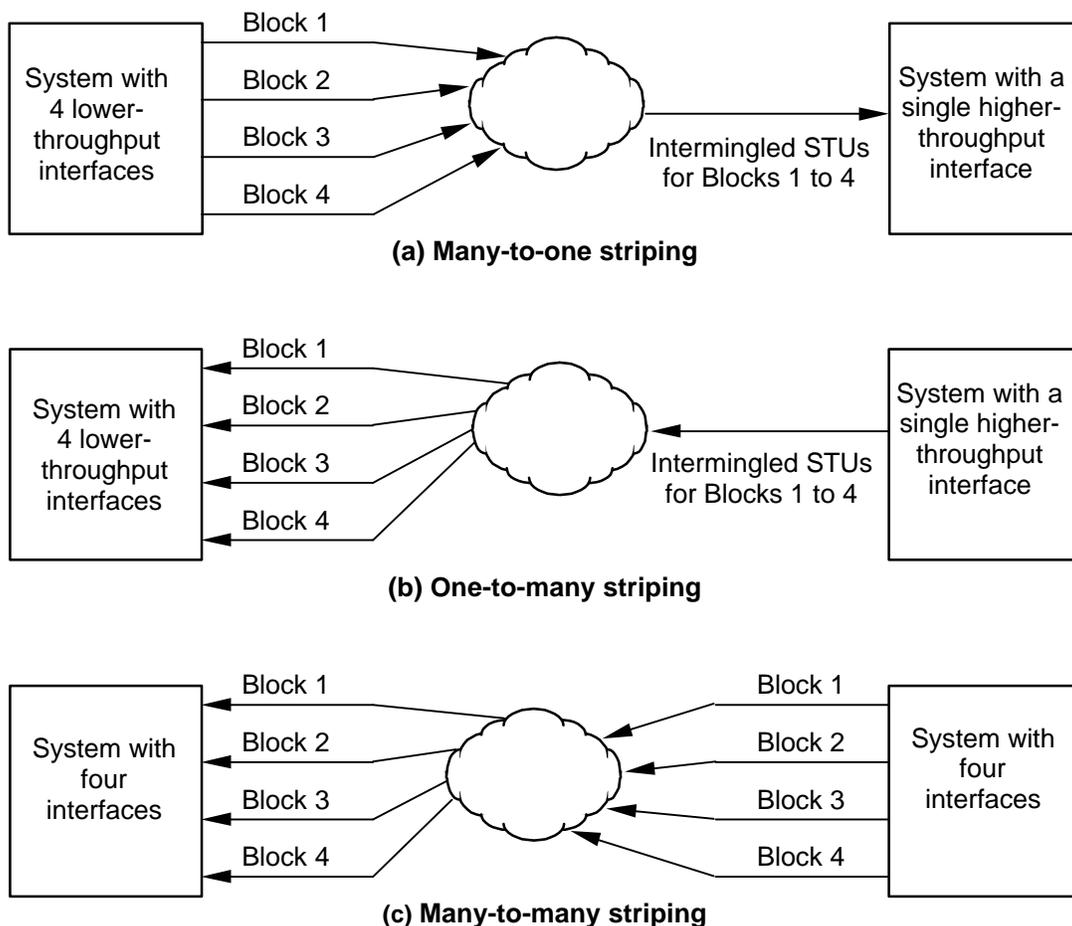
Part b of figure B.1 shows how Transfers made from one higher-throughput interface can also be spread across more than one lower-throughput interface without any special action on the part of the higher-throughput system.

After the Ports are assigned, the Transfer is initiated with a Request\_To\_Send operation from the higher-throughput interface. The lower-throughput interface that has done the Port assignment will return a Clear\_To\_Send. Each Clear\_To\_Send issued should be sent from the interface desiring the data.

An alternative is to send all of the Clear\_To\_Send operations from a single interface and substitute the desired physical return address (e.g., Source physical address) for the Clear\_To\_Send's Source physical address (making it appear that the Clear\_To\_Send's Source physical address was generated by the interface desiring the data). Subsequent Data operations may then be done concurrently and will use a Source physical address from the Clear\_To\_Send operation as the Destination physical address. Using this substitution method in combination with a dedicated control channel may also prevent or reduce blocking effects where the underlying physical medium suffers from high latency.

### B.4 Many-to-many striping

Part c of figure B.1 shows many-to-many striping as the combination of the one-to-many and many-to-one striping. The system receiving data indicates its desire to receive in a striped fashion by issuing multiple Clear\_To\_Send operations with differing return interface addresses. The system sending data chooses to stripe by sending from multiple interfaces that are capable of reaching the proper destination.



NOTE: A Clear\_To\_Send for each Block is sent in the reverse direction on the same path that each Block traverses (or is made to appear that way) for figure B.1 (a-c).

**Figure B.1 – ST Striping Configurations**

**Annex C**  
(informative)

**Scheduled Transfer Protocol examples**

*NOTE – This annex has not been updated for some time, and hence is quite out of date, e.g., names, functions, etc.. Rather than leave it in place and possibly confuse readers, it has been removed until it can be brought up to date. Jim Pinkerton has offered to draft text based on his work at SGI.*

**Annex D**  
(informative)

**State tables**

*Open Issue – Jeffrey Chung of SGI has an action item to provide the state tables. Note that the state tables are informative rather than normative.*