

MSQS-2 Ad Hoc Group Status

April 2013

- Met on Tuesday, April 9th from 1:30 to 4:30
- Presentations
 - **Jitter Tracking Requirements for 32GFC Optical RX, Dave Lewis, JDSU, 13-159v0**
 1. The 8GFC and 4GFC sinusoidal jitter templates were reviewed. The corner frequency was compared to 10GASE-R.
 2. It was recommended that the corner frequency should not be baud rate/1667 for both 16GFC and 32GFC. The divisor is legacy and it isn't clear where the number came from.
 3. 16GFC is already complete so the sinusoidal jitter mask will not be changed for 16GFC.
 4. The 32GFC numbers are the same as OIF and is baud rate/2578 so it is slightly lower frequency than what was requested in the presentation.
 - **CTLEs used in sampling scopes, Joachim Vobis, Agilent, 13-158v0**
 1. CTLE and CRU implementation was reviewed for a typical sampling scope.
 2. The clock recovery is before the CTLE software implementation therefore an open eye is required for the CRU in this implementation.
 3. If the eye is not open then either a hardware CTLE needs to be implemented or an alternative is to provide another clock for the CRU that is from a channel that has the same reference clock.
 4. An annex will be added to MSQS-2 that explains the subtleties of how to deal with closed or almost closed eyes with respect to figure 6.1 in MSQS-2.

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- **Presentations**

- **Review MSQS-2 rev 0.6, Richard Johnson, Finisar, 13-009v1**

1. Revision 0.6 of the MSQS-2 document was reviewed. All of the major clauses are in place and no significant changes are expected.
2. Richard reviewed areas of the document where he has questions.
3. A informal letter ballot will be done on revisions 0.6 of MSQS-2 and the comments will be resolved at the next meeting in preparation for T11.2 letter ballot.

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- **Future meeting schedule for MSQS-2 ad hoc**
 - Future meeting schedule
 - Tuesday 6/4/13 from 1:30 to 5:00 in Coeur d' Alene, ID
 - Request two conference calls