

<u>Date</u>	<u>Revision</u>
05/31/14	First release for 14-018v1 (FC-FS-4 Rev 0.5)

Comments on FC-FS-4 Rev 0.5 14-018v1						
Sequence	Tech/Edit	Page(PDF)	Line number	Index	Comment	Proposed Solution
IBM-27	Edit	184	?	10.6.4.2	Transition T6:T1 mentions the “1 us” timer. This should instead be more precisely identified as the “Bypass_Timer”.	Update as follows: “Transition T6:T1: The <b>Bypass_Timer</b> timer has expired...”
IBM-28	Edit	111	?	Figure 29	Arrow from “RS-FEC_codeword” to “Reed-Solomon Encoder” block is not in the correct place.	Reformat the diagram for proper arrow placement.
IBM-29	Tech (?)	111	?	Figure 29	The bit stream order into the Receiver illustrated at the bottom of the figure is reversed from the original proposed solution (see 14-156v0, Slide 3). Because the direction of the bit stream arrows is opposite compared to the Transmitter illustration (Figure 28), illustrating the same bit stream order (bit 5279 on the top, bit 0 on the bottom ) in Figures 28 and 29 is incorrect. Note further that this is also inconsistent with 802.3bj D3.2, Figure 91-7 - Receive bit ordering (compared to Figure 91-6 – Transmit bit ordering).	Reversal of the bit stream order in Figure 29 with bit 0 on the top and bit 5279 on the bottom (consistent with the original proposed solution) is needed to make these figures consistent.
IBM-30	Tech	120	?	5.6.2.5	128GFC Transmit bit order text and figure not yet included	Incorporate text and figure appearing in 14-156v0, Slides 4 and 5.
IBM-31	Tech	121	?	5.6.2.11	128GFC Receive bit order text and figure not yet included	Incorporate text and figure appearing in 14-156v0, Slides 4 and 6.
IBM-32	Tech	N/A	?	N/A	RS-FEC codeword examples not yet included	Incorporate 14-161v0 (RS-FEC Codeword Examples) as an informative annex. (NOTE: 14-161v0 was developed to close out FC-FS-4 Action Item 1404-07...)