

IEEE 802.3 Liaison Report to T11 (April 2022)

t112022.00147v0

April 7, 2022

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Includes activity for March 2022 802.3 plenary and February, March 2022 802.3 TF meetings

IEEE 802.3 Standards in force

- The current version in force is IEEE Std 802.3-2018 – Approved 14 June 2018, Published 31 August 2018
 - This incorporates 802.3bw, 802.3bp, 802.3bq, 802.3br, 802.3by, 802.3bz, 802.3bn, 802.3bu, 802.3bv, 802.3bs, 802.3cc, and Corrigendum 1.
- IEEE Std 802.3bt-2018, Power over Ethernet over 4 Pairs, approved 27 September 2018, published 31 January 2019
- IEEE Std 802.3cb-2018, 2.5 Gb/s and 5 Gb/s Operation over Backplane, approved 27 September 2018, published 4 January 2019
- IEEE Std 802.3cd-2018, Media Access Control Parameters for 50 Gb/s and Physical Layers and Management Parameters for 50 Gb/s, 100 Gb/s, and 200 Gb/s Operation, approved 6 December 2018, published 15 February 2019
- IEEE Std 802.3cn-2019, Physical Layers and Management Parameters for 50 Gb/s, 200 Gb/s, and 400 Gb/s Operation over Single-Mode Fiber, approved 7 November 2019, published 20 December 2019
- IEEE Std 802.3cg-2019, Physical Layer Specifications and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balanced Pair of Conductors, approved 7 November 2019, published 5 February 2020
- IEEE Std 802.3cm-2020, Physical Layer and Management Parameters for 400 Gb/s over Multimode Fiber, approved 31 January 2020, published 30 March 2020
- IEEE Std 802.3cq-2020, Maintenance #13: Power over Ethernet 2 pairs, approved 30 January 2020, published 13 March 2020
- IEEE Std 802.3ch-2020, Physical Layer Specifications and Management Parameters for 2.5 Gb/s, 5 Gb/s, and 10 Gb/s Automotive Electrical Ethernet, approved 4 June 2020, published 30 June 2020
- IEEE Std 802.3ca-2020, Physical Layer Specifications and Management Parameters for 25 Gb/s and 50 Gb/s Passive Optical Networks, approved 4 June 2020, published 3 July 2020
- IEEE Std 802.3cr-2021 Maintenance #14: Isolation, approved 9 February 2021, published 24 February 2021
- IEEE Std 802.3cu-2021, Physical Layers and Management Parameters for 100 Gb/s and 400 Gb/s Operation over Single-Mode Fiber at 100 Gb/s per wavelength, approved 9 February 2021, published 26 February 2021.
- IEEE Std 802.3cv-2021, Maintenance #15: Power over Ethernet, approved 9 May 2021, published 28 May 2021.
- IEEE Std 802.3ct-2021, 100 Gb/s Ethernet over DWDM systems, approved 16 June 2021, published 9 July 2021.
- IEEE Std 802.3cp-2021, Bidirectional 10 Gb/s, 25 Gb/s, and 50 Gb/s Optical Access PHYs, approved 16 June 2021, published 16 July 2021.
- Ethernet MIBs –IEEE Std 802.3.1-2013 published on 2 August 2013
- Ethernet YANG models – IEEE Std 802.3.2-2019 approved 26 March 2019, published 21 June 2019

IEEE 802.3 Task Forces, Study Groups, CFIs

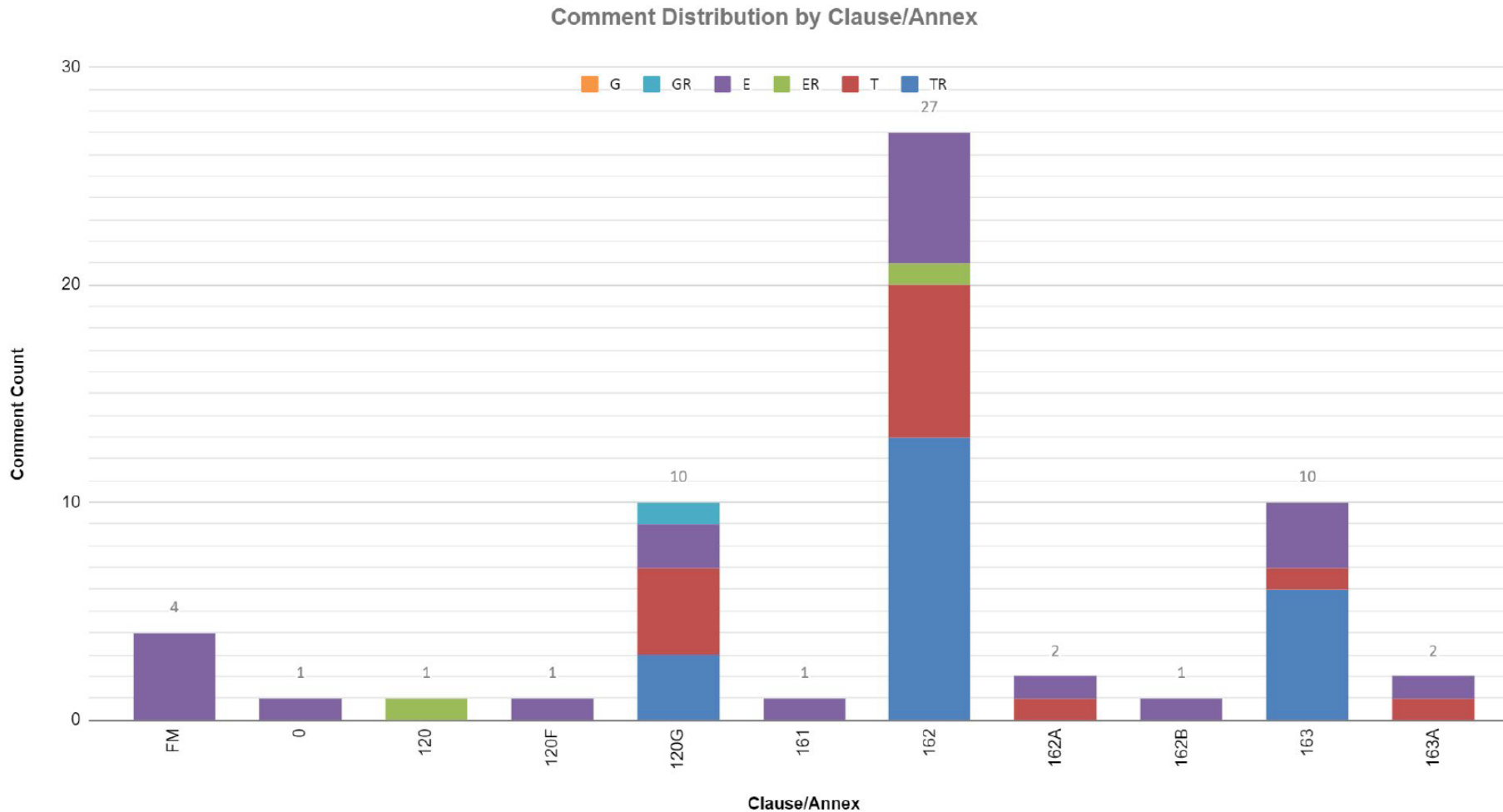
- [P802.3ck](#) 100 Gb/s per Lane Electrical Task Force
- [P802.3cs](#) Increased-reach Ethernet optical subscriber access (Super-PON) Task Force
- [P802.3cw](#) 400 Gb/s Ethernet over DWDM systems Task Force
- [P802.3cx](#) Improving PTP Timestamping Accuracy Task Force
- [P802.3cy](#) Greater than 10 Gb/s Electrical Automotive Ethernet Task Force
- [P802.3cz](#) Multi-Gigabit Optical Automotive Ethernet Task Force
- [P802.3da](#) 10 Mb/s Single Pair Multidrop Segments Enhancements Task Force
- [P802.3db](#) 100 Gb/s, 200 Gb/s, and 400 Gb/s Short Reach Fiber Task Force
- Revision to IEEE Standard 802.3-2018 ([P802.3dc](#))
- [P802.3dd](#) Power over Data Lines of Single Pair Ethernet (Maintenance #17) Task Force
- [P802.3de](#) Time Synchronization for Point-to-Point Single Pair Ethernet Task Force
- [P802.3df](#) 200 Gb/s, 400 Gb/s, 800 Gb/s and 1.6Tb/s Ethernet Task Force
- ~~[Beyond 400 Gigabit Ethernet Study Group](#)~~
- ~~[Enhancements to Point-to-Point Single Pair Ethernet Study Group](#)~~
- [Greater than 10 Mb/s Long-Reach Single Pair Ethernet](#) Study Group
- New Ethernet Applications Ad Hoc

P802.3ck 100 Gb/s per lane Electrical Task Force

- SA ballot complete
 - Created Draft 3.1 for first SA ballot recirculation.
 - First SA ballot recirculation Opened on 14 March 2022, Closed on 29 March 2022
 - 60 comments from 9 reviewers
- Schedule to resolve comments:
 - Tuesday, April 12 – 6:30-10:30 am PDT
 - Wednesday, April 13 – 6:30-10:30 am PDT
 - Tuesday, April 19 – 6:30-10:30 am PDT
 - Wednesday, April 20 – 7-10 am PDT
 - Thursday, April 21 – 7-10 am PDT – May run over
 - Tuesday, April 26 – 6:30-10:30 am PDT
 - Wednesday, April 27 – 6:30-10:30 am PDT – As long as it needs to run
- https://www.ieee802.org/3/ck/public/22_04/index.html

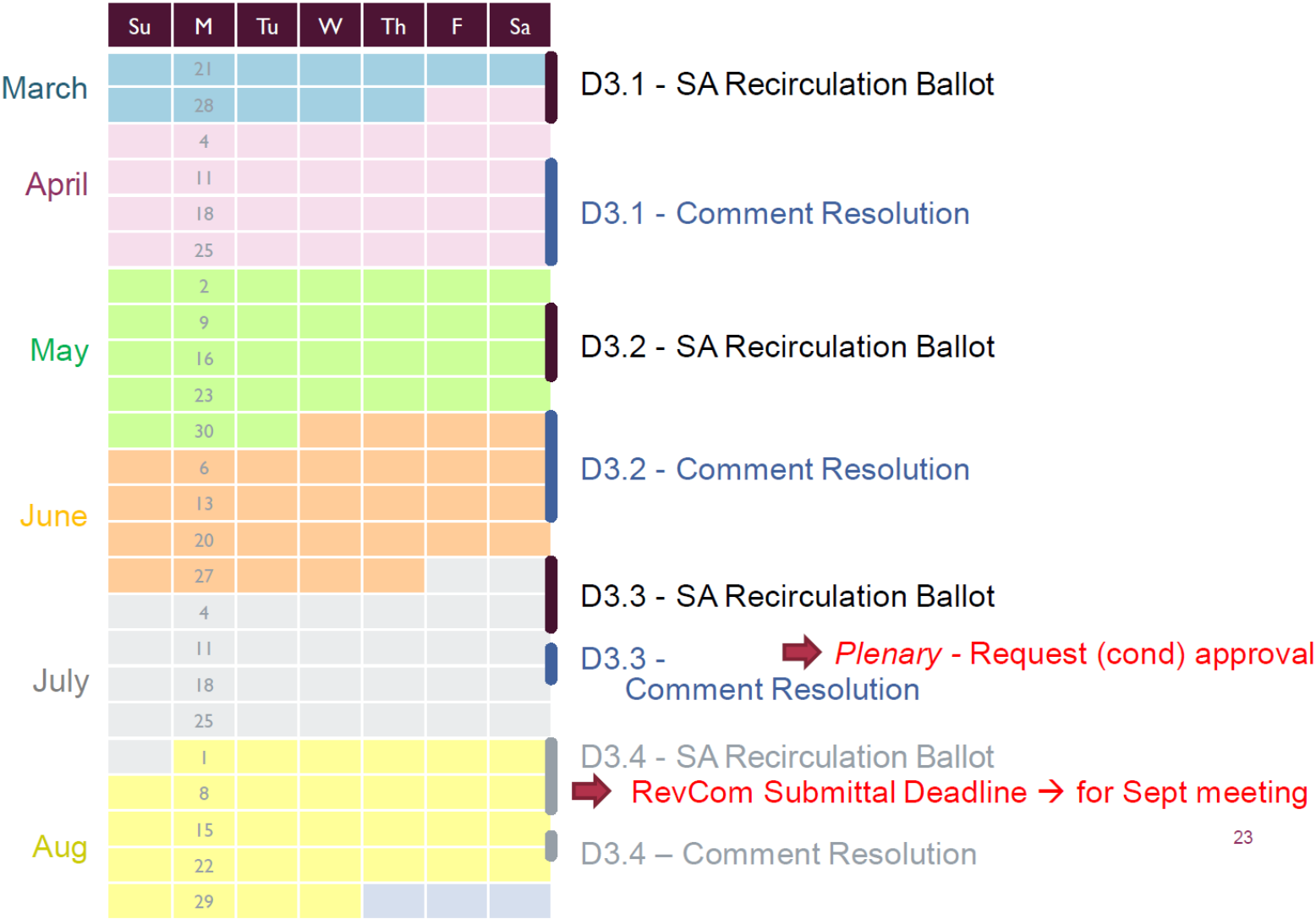
802.3ck Draft 3.1

Comment Summary by Clause/Annex



E/ER = editorial, T/TR = technical, G/GR = general

P802.3ck Proposed Schedule



P802.3ck 100 Gb/s per lane Electrical Task Force Adopted Objectives (1 of 2)

- Support a MAC data rate of 100 Gb/s, 200 Gb/s, and 400 Gb/s
- Support full-duplex operation only
- Preserve the Ethernet frame format utilizing the Ethernet MAC
- Preserve minimum and maximum FrameSize of current IEEE 802.3 standard
- Support the existing bit error ratios (BERs) at the MAC/PLS service interface (or the frame loss ratio equivalent) for 100 Gb/s, 200 Gb/s, and 400 Gb/s Ethernet
- Define a single-lane 100 Gb/s Attachment Unit interface (AUI) for chip-to-module applications, compatible with PMDs based on 100 Gb/s per lane optical signaling
- Define a single-lane 100 Gb/s Attachment Unit Interface (AUI) for chip-to-chip applications
- Define a single-lane 100 Gb/s PHY for operation over electrical backplanes supporting an insertion loss ≤ 28 dB at 26.56 GHz.
- Define a single-lane 100 Gb/s PHY for operation over twin-axial copper cables with lengths up to at least 2m

P802.3ck 100 Gb/s per lane Electrical Task Force

Adopted Objectives (2 of 2)

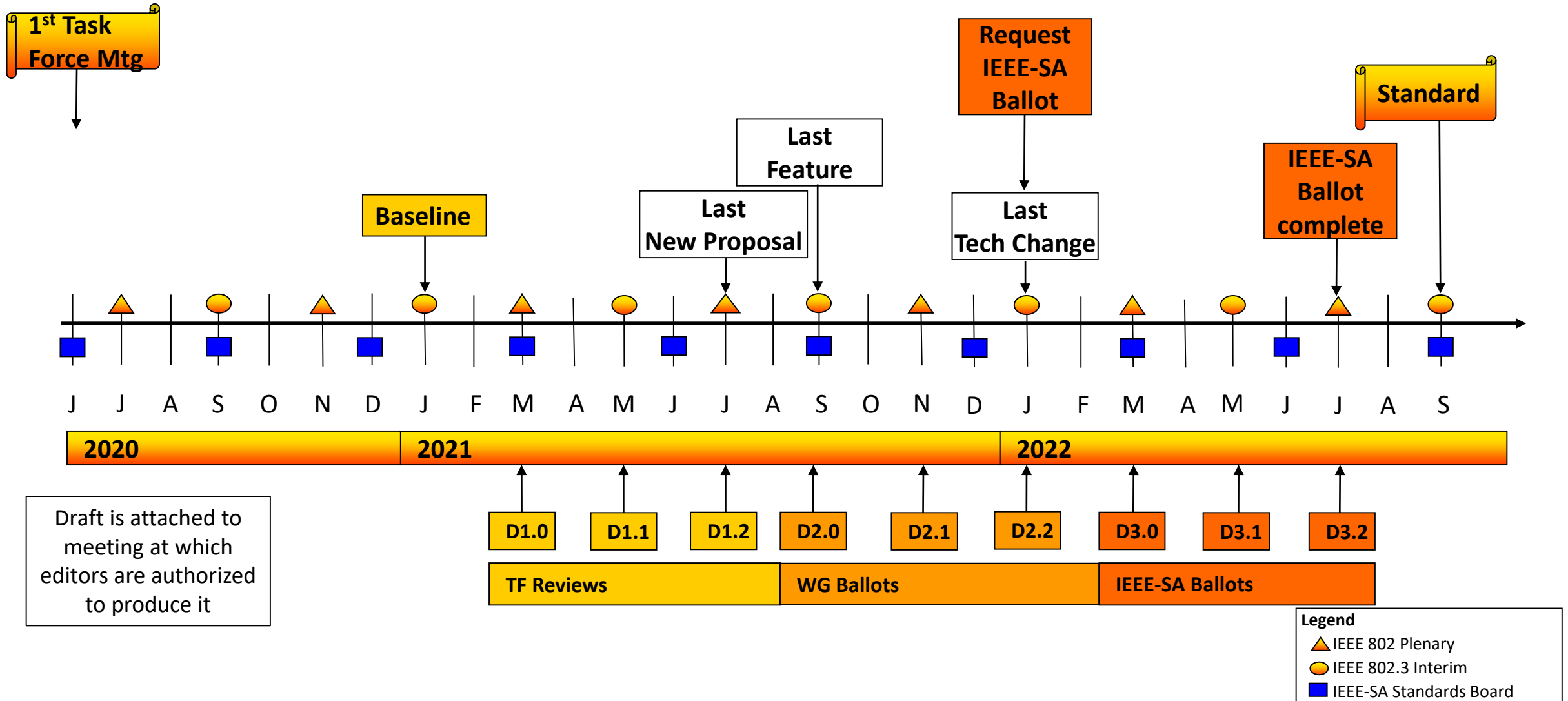
- Define a two-lane 200 Gb/s Attachment Unit interface (AUI) for chip-to-module applications, compatible with PMDs based on 100 Gb/s per lane optical signaling
 - Define a two-lane 200 Gb/s Attachment Unit Interface (AUI) for chip-to-chip applications
 - Define a two-lane 200 Gb/s PHY for operation over electrical backplanes supporting an insertion loss ≤ 28 dB at 26.56 GHz.
 - Define a two-lane 200 Gb/s PHY for operation over twin-axial copper cables with lengths up to at least 2m
-
- Define a four-lane 400 Gb/s Attachment Unit interface (AUI) for chip-to-module applications, compatible with PMDs based on 100 Gb/s per lane optical signaling
 - Define a four-lane 400 Gb/s Attachment Unit Interface (AUI) for chip-to-chip applications
 - Define a four-lane 400 Gb/s PHY for operation over electrical backplanes supporting an insertion loss ≤ 28 dB at 26.56 GHz.
 - Define a four-lane 400 Gb/s PHY for operation over twin-axial copper cables with lengths up to at least 2m

P802.3db 100 Gb/s, 200 Gb/s, and 400 Gb/s Short Reach Fiber Task Force

- 20th Task Force meeting (teleconference) 24 February 2022 Interim
 - Meeting Materials: <https://www.ieee802.org/3/db/public/February22/>
 - Meeting Minutes: [unapproved meeting minutes 3db 01 022422.pdf](#)
 - Resolved **2** (1 comment withdrawn) comments against Working Group recirculation ballot **D2.2**.
 - Authorized Standards Association ballot **D3.0**
 - Liaison to T11.2 to make **D3.0** available
- IEEE SA ballot/D3.0
 - Opened 2 March 2022. Closed 1 April 2022
 - 34 SA ballot comments, and 1 non-ballot comment, for total of **35** comments received
 - Key technical comments to be resolved by May 2022:
 - MDI specification for MPO-12 for 200G-SR & 400G-SR and for 200G-VR & 400G-VR is for angled/APC interface only. Replacing with flat/PC to address enterprise installed base
 - Propose lower max receiver reflectance to -15dB and set optical return loss tolerance to 15dB
 - Add a specification for K' to the transmit characteristics
 - Adopt the (optional) MMSE method for equalizer optimization in TDECQ to reduce test time
- TF **Updated** & Adopted [Timeline](#) at November 2021 Interim
 - **September 2021** date for authorizing D2.0, Working Group ballots (Last Feature, no TBDs, technically complete draft) met.
 - Added one more Working Group recirculation ballot, D2.2. Now completed.
 - Target date for authorizing Last Technical Change/technical stability (specified values will change based on comments after this date): **January 2022** met.
 - Target date for Standard: **September 2022**
 - Expect all technical specifications to be finalized by **June 2022** T11 Plenary meeting

IEEE P802.3db Task Force Timeline

Updated & Adopted by TF November 2021



P802.3db 100 Gb/s, 200 Gb/s, and 400 Gb/s Short Reach Fiber Task Force

Adopted Objectives (1 of 2)

1. Support a MAC data rate of 100 Gb/s, 200 Gb/s and 400 Gb/s
2. Support full-duplex operation only
3. Preserve the Ethernet frame format utilizing the Ethernet MAC
4. Preserve minimum and maximum FrameSize of current IEEE 802.3 standard
5. Provide appropriate support for OTN
6. Support a BER of better than or equal to 10^{-12} at the MAC/PLS service interface (or the frame loss ratio equivalent) for 100 Gb/s operation
7. Support a BER of better than or equal to 10^{-13} at the MAC/PLS service interface (or the frame loss ratio equivalent) for 200 Gb/s and 400 Gb/s operation

P802.3db 100 Gb/s, 200 Gb/s, and 400 Gb/s Short Reach Fiber Task Force

Adopted Objectives (2 of 2)

8. Define a physical layer specification that supports 100 Gb/s operation over 1 pair of MMF with lengths up to at least 50 m
9. Define a physical layer specification that supports 200 Gb/s operation over 2 pairs of MMF with lengths up to at least 50 m
10. Define a physical layer specification that supports 400 Gb/s operation over 4 pairs of MMF with lengths up to at least 50 m
11. Define a physical layer specification that supports 100 Gb/s operation over 1 pair of MMF with lengths up to at least 100 m
12. Define a physical layer specification that supports 200 Gb/s operation over 2 pairs of MMF with lengths up to at least 100 m
13. Define a physical layer specification that supports 400 Gb/s operation over 4 pairs of MMF with lengths up to at least 100 m

P802.3db Scheduled Future Meetings

- P802.3db meetings scheduled to resolve comments against D3.0:
 - Thursday, 14 April 2022: 12:00 noon – 3:00 PM EDT/UTC-4
 - Thursday, 21 April 2022: 1 PM – 3:00 PM EDT/UTC-4
 - Thursday, 28 April 2022: 1 PM – 3:00 PM EDT/UTC-4
 - Thursday, 5 May 2022: 1 PM – 3:00 PM EDT/UTC-4
- Meetings are as needed, and meeting times subject to change.
- Please see <https://www.ieee802.org/3/calendar.html> for latest updates

P802.3df 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet Task Force (Beyond 400 Gb/s Ethernet Study Group)

- P802.3df PAR approved **December 8**. Now a Task Force.
- Adopted Objectives:
https://www.ieee802.org/3/df/proj_doc/objectives_P802d3df_211118.pdf
- This is a big project - the Study Group has adopted 31 objectives
 - 26 physical layer and electrical interface specifications
- 200 Gb/s signaling MMF objectives are not being adopted –
 - Impact on FC-P1-9/256 GFC serial MMF variants.
- The webpage for the Study Group Meeting on 16 Nov 2021 has been uploaded -
https://www.ieee802.org/3/B400G/public/21_1116/index.html.

IEEE P802.3df Task Force Project information

- **Organization**
 - John D'Ambrosia, Chair, IEEE P802.3df Task Force
 - Mark Nowell, Vice-Chair, IEEE P802.3df Task Force
- **Task force web and reflector information:**
 - Home page: <https://www.ieee802.org/3/df/index.html>
 - Reflector Info - <https://www.ieee802.org/3/df/reflector.html>
 - TF Reflector: stds-802-3-b400g@listserv.ieee.org
 - Logic Reflector: stds-802-3-b400g-logic@listserv.ieee.org
 - Optical Reflector: stds-802-3-b400g-optx@listserv.ieee.org
 - Electrical Reflector: stds-802-3-b400g-elec@listserv.ieee.org
- **Project Documentation –**
 - PAR : https://www.ieee802.org/3/df/proj_doc/IEEE_P802.3df_PAR_11122021.pdf
 - CSD: <https://mentor.ieee.org/802-ec/dcn/21/ec-21-0306-00-ACSD-p802-3df.pdf>
 - Objectives: https://www.ieee802.org/3/df/proj_doc/objectives_P802d3df_211118.pdf
- **P802.3df TF meeting information may be found on:**
 - Public page: <https://www.ieee802.org/3/df/public/index.html>
 - 802.3 Calendar: <https://www.ieee802.org/3/calendar.html>

IEEE P802.3 df Objectives

- **Non-Rate Specific**
 - Support full-duplex operation only
 - Preserve the Ethernet frame format utilizing the Ethernet MAC
 - Preserve minimum and maximum FrameSize of current IEEE 802.3 standard
 - Support a BER of better than or equal to 10^{-13} at the MAC/PLS service interface (or the frame loss ratio equivalent)
 - Provide support to enable mapping over OTN
- **200 Gb/s Related**
 - Support a MAC data rate of 200 Gb/s
 - Support optional single-lane 200 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications
 - Define a physical layer specification that supports 200 Gb/s operation:
 - over 1 pair of copper twin-axial cables in each direction with a reach of up to at least 1.0 meter
 - over 1 pair of SMF with lengths up to at least 500 m
 - over 1 pair of SMF with lengths up to at least 2 km
- **400 Gb/s Related**
 - Support a MAC data rate of 400 Gb/s
 - Support optional two-lane 400 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications
 - Define a physical layer specification that supports 400 Gb/s operation:
 - over 2 pairs of copper twin-axial cables in each direction with a reach of up to at least 1.0 meter
 - over 2 pairs of SMF with lengths up to at least 500 m

IEEE P802.3 df Objectives

- **800 Gb/s Related**

- Support a MAC data rate of 800 Gb/s
- Support optional eight-lane 800 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications
- Support optional four-lane 800 Gb/s attachment unit interfaces for chip-to-module and chip-to-chip applications
- Define a physical layer specification that supports 800 Gb/s operation:
 - over 4 pairs of copper twin-axial cables in each direction with a reach of up to at least 1.0 meter
 - over eight lanes of twin axial copper cables with a reach up to at least 2 meters
 - over eight lanes over electrical backplanes supporting an insertion loss $\leq 28\text{dB}$ at 26.56GHz
 - over 8 pairs of MMF with lengths up to at least 50 m
 - over 8 pairs of MMF with lengths up to at least 100 m
 - over 8 pairs of SMF with lengths up to at least 500 m
 - over 8 pairs of SMF with lengths up to at least 2 km
 - over 4 pairs of SMF with lengths up to at least 500 m
 - over 4 pairs of SMF with lengths up to at least 2 km
 - over 4 wavelengths over a single SMF in each direction with lengths up to at least 2 km
 - over a single SMF in each direction with lengths up to at least 10 km
 - over a single SMF in each direction with lengths up to at least 40 km

IEEE P802.3 df Objectives

- **1.6 Tb/s Related**

- Support a MAC data rate of 1.6 Tb/s
- Support optional sixteen-lane 1.6 Tb/s attachment unit interfaces for chip-to-module and chip-to-chip applications
- Support optional eight-lane 1.6 Tb/s attachment unit interfaces for chip-to-module and chip-to-chip applications
- Define a physical layer specification that supports 1.6 Tb/s operation:
 - over 8 pairs of copper twin-axial cables in each direction with a reach of up to at least 1.0 meter
 - over 8 pairs of SMF with lengths up to at least 500 m
 - over 8 pairs of SMF with lengths up to at least 2 km

Adopted Physical Layer Objectives

Technology Reuse

Ethernet Rate	Assumed Signaling Rate	AUI	BP	Cu Cable	MMF 50m	MMF 100m	SMF 500m	SMF 2km	SMF 10km	SMF 40km
200 Gb/s	200 Gb/s	Over 1 lane		Over 1 pair			Over 1 Pair	Over 1 Pair		
400 Gb/s	200 Gb/s	Over 2 lanes		Over 2 pairs			Over 2 Pair			
800 Gb/s	100 Gb/s	Over 8 lanes	Over 8 lanes	Over 8 pairs	Over 8 pairs	Over 8 pairs	Over 8 pairs	Over 8 pairs		
	200 Gb/s	Over 4 lanes		Over 4 pairs			Over 4 pairs	1) Over 4 pairs 2) Over 4 λ 's		
	TBD								Over single SMF in each direction	Over single SMF in each direction
1.6 Tb/s	100 Gb/s	Over 16 lanes								
	200 Gb/s	Over 8 lanes		Over 8 pairs			Over 8 pairs	Over 8 pairs		

Leverage existing or work-in-progress 100 Gb/s per lane (e.g. 3cu, 3ck, 3db) to higher lane counts

Develop 200 Gb/s per lane electrical signaling for 1/2/4/8 lane variants of AUIs and electrical PMDs

Develop 200 Gb/s per optical fiber for 1/2/4/8 fiber based optical PMDs and 4 lambda WDM optical PMD

Potential for either direct detect and / or coherent signaling technology

Making it all work together

Technical Challenges

- Our immediate technical challenges
 - Co-existence of 100 Gb/s and 200 Gb/s Signaling
 - FEC Architecture (see slide #5)
 - Compatibility of modulation schemes between copper / optical signaling

- A breadth of physical layer specifications
 - For 800 Gb/s Ethernet - Leverage 100 Gb/s signaling from:
 - 802.3cu – 500m / 2km SMF objectives
 - 802.3ck – AUI, backplane, copper cable objectives
 - 802.3db – MMF objectives
 - For 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s –
 - Develop 200 Gb/s signaling and apply to all respective medium and interface objectives
 - For 800 Gb/s Ethernet 10km and 40km objectives
 - Other Signaling?

Ad Hocs

Announcement - <https://www.ieee802.org/3/B400G/email/msg00257.html>

1. Architecture and Logic Ad hoc – Mark Gustlin, Ad Hoc Chair

Charter: The Architecture and Logic Ad Hoc is chartered to address the following:

- 1) Develop terminology and definitions for terms related to architecture, including but not limited to FEC Architecture, FEC Scheme, End-to-end FEC, Segmented FEC, Concatenated FEC. Draft definitions to be presented to Task Force at May 2022 session.
- 2) Act as forum to discuss architectural requirements and consider proposals related to PCS, FEC, and PMA logic sublayers.

2. Electrical Ad Hoc – Kent Lusted, Ad Hoc Chair

Charter: The Electrical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to electrical interfaces and electrical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact electrical interfaces and electrical PMD sublayers
 - c. Electrical interfaces and electrical PMD nomenclature
- 2) Provide inputs based on electrical interfaces and electrical PMD sublayers into any relevant ad hocs

3. Optics Ad Hoc – Mark Nowell, Ad Hoc Chair

Charter: The Optical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to optical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact optical PMD sublayers
 - c. Optical PMD nomenclature
- 2) Provide inputs based on optical PMDs into any relevant ad hocs

802.3df ad hoc meetings

Future Ad hoc Meetings

1. Architecture and Logic Ad hoc

- 25 Apr 2022, 10am to 12pm, ET, [Call Info](#)
- 11 Apr 2022, 10am to 12pm, ET, [Call Info](#)

2. Electrical Ad Hoc

- 02 May 2022, 10am to 12pm, ET, [Call Info](#)
- 18 Apr 2022, 10am to 12pm, ET, [Call Info](#)

3. Optics Ad Hoc

- 28 Apr 2022, 10am to 12pm, ET, [Call Info](#)
- 14 Apr 2022, 10am to 12pm, ET, [Call Info](#)

802.3df conf calls

<https://www.ieee802.org/3/df/public/index.html>

- May 2022 Electronic Session [Meeting Announcement](#)
- Meeting 4 - 02 Jun 2022, 10am to 1pm ET, Call Info @ [802.3 Calendar](#)
- Meeting 3 - 24 May 2022, 10am to 1pm ET, Call Info @ [802.3 Calendar:](#)
- Meeting 2 - 18 May 2022, 10am to 1pm ET, Call Info @ [802.3 Calendar:](#)
- Meeting 1 - 17 May 2022, 10am to 1pm ET, Call Info @ [802.3 Calendar:](#)

Future Meetings

Meeting	Location	Dates
IEEE 802.3 May 2022 interim	Virtual	16-20 & 23-27 May 2022
IEEE 802 July 2022 plenary	Montreal, Quebec, Canada	11-14 July 2022
IEEE 802.3 September 2022 interim	TBD	5-9 & 12-16 September 2022
IEEE 802 November 2022 plenary	Bangkok, Thailand	14-17 November 2022

- The 802.3 working group does not intend to try to schedule a face-to-face interim meeting (January, May, or September) until IEEE 802 has successfully held a face-to-face plenary meeting (March, July or November).
- Earliest possible face-to-face or hybrid meeting is now July 2022, tentatively Montreal, Canada. Decision still not finalized.
- Upcoming meeting details at: <http://ieee802.org/3/interims/index.html>