
- **Storage Management Initiative Specification (SMI-S) v1.8.0**
  - SMI-S defines a method for the interoperable management of a heterogeneous Storage Area Network (SAN) and describes the information available to a WBEM Client from an SMI-S compliant CIM Server and an object-oriented, XML-based, messaging-based interface designed to support the specific requirements of managing devices in and through SANs.

- **Now published as ISO/IEC 24775:2021**
  - [https://www.iso.org/standard/81141.html](https://www.iso.org/standard/81141.html)

- Native NVMe-oF™ Drive Specification v1.1
  - This Native NVMe-oF™ Drive Specification describes the features and functions of a storage device class known as Native NVMe-oF Drives. It includes a taxonomy covering the scope of involved device capabilities.

- Swordfish Scalable Storage Management API Specification v1.2.2
  - The Swordfish Scalable Storage Management API ("Swordfish") uses RESTful interface semantics and a standardized data model to provide a scalable, customer-centric interface for managing storage and related data services.
SNIA Public Review Drafts

- Native NVMe-oF™ Drive Specification v1.1
- Swordfish Scalable Storage Management API Specification v1.2.2
- DRAFT CDMI Extensions and Profiles
  - Capabilities Selection Extension v2.0
  - CORS Extension v2.0
  - Data Affinity Extension v2.0
  - Jobs v2.0
  - Partial Upload Extension v2.0
- Computational Storage Architecture and Programming Model v0.5 rev 1

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High-performance storage applications running on Intel® Xeon® processors actively utilize I/O capabilities and I/O accelerating features of the platform by interfacing with NVMe devices. Such I/O-intensive applications may suffer from performance issues, which in a big picture can be categorized into three domains: I/O device bound – performance is limited by device capabilities core bound – performance is limited by algorithmic or microarchitectural code issues uncore bound – performance is limited by non-optimal interactions between devices and CPU. This talk focuses on the latter case. In Intel architectures, the term “core” covers execution units and private caches, and all the rest of the processor is referred as “uncore”, which includes on-die interconnect, shared cache, cross-socket links, integrated memory and I/O controllers, etc. Activities happening on the IO path in uncore cannot be monitored with traditional core-centric analyses, but there are pitfalls that require an uncore-centric view. Intel servers provide such a view by incorporating thousands of uncore performance monitoring events that can be collected in performance monitoring units (PMUs) associated with uncore IP blocks. However, using raw counters for performance analysis requires deep knowledge of hardware and appears incredibly challenging. In this talk, we will discuss platform-level activities induced by I/O traffic on Intel® Xeon® Scalable processors and summarize practices for best performance of storage applications. We will overview telemetry points staying on the IO traffic path and eventually present developing uncore-specific performance analysis methodology, that reveals platform-level inefficiencies, including poor utilization of Intel® Data Direct I/O Technology (Intel® DDIO).
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- SkyhookDM: storage and management of tabular data in Ceph.
- Data Preservation & Retention 101
- Amazon FSx For Lustre Deep Dive and its importance in Machine Learning
- NVMe and NVMe-oF Configuration and Manageability with Swordfish and Redfish

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