X3 Project 989
PL 181

IBM *International Business Machines Corporation*

Office of the Intellectual Property & Licensing Services
Corporate

500 Columbus Avenue, Thornwood, NY 10594

July 15, 1994

Ms. Frances Schrotter
American National Standards Institute
11 West 42nd Street
New York, NY 10036

    Re: Serial Storage Architecture Standard
          X3T10.1 Committee

Dear Ms. Schrotter:

IBM is pleased to advise ANSI that IBM will grant licenses under
the patents, and patents that issue on the patent applications,
listed on the sheet attached hereto. It is the belief of IBM
that these are the IBM patents that are relevant to the current
set of specifications for the Transport Layer of the SSA
standard.

The fee for the non-exclusive, irrevocable, paid-up and world-
wide license under the listed patents and applications, for SSA-
compliant implementations, will be $5,000. Each party to whom
such a license is granted must agree to provide IBM an option to
acquire a license under any patent(s) relevant to implementing
SSA, that such party has the right to license to other parties.
The terms and fee for such a license will be similar to the grant
IBM makes to such party in connection with the SSA standard.

Any party wishing to obtain the license must send to me a written
request for same, that provides the name of the state in which
the licensee is incorporated and the name/title and address of
the person or office to which IBM will send any notices under the
license agreement. The request should be sent to, and questions
may be directed to me at;

        Mr. John W. Lowe
        Intellectual Property & Licensing Services
        IBM Corporation
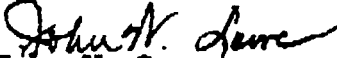   500 Columbus Avenue
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Please update ANSI's records regarding JPEG, JBIG, Fiber Channel
Standard, and any other standards which practice IBM patents, to
reflect this new address. IBM is pleased to have supported the
adoption and use of these standards by making licenses available
under its patents.

Sincerely,

John W. Lowe
Program Manager, Licensing

Attachment

cc: Mr. John P. Scheible
     Chairman, X3T10.1 Committee
     IBM Corporation

EXHIBIT A

1. US Patent 4,486,739 issued December 4, 1984, ("Byte Oriented DC Balanced 8B/10B Partitioned Block Transmission Code").
    IBM reference YO9820019.

2. US Patent 4,926,418 issued May 15, 1990, ("Fairness Algorithm For Full-Duplex Buffer Insertion Ring").
    IBM reference YO9890009.

3. US Patent 5,003,308 issued March 26, 1991, ("Serial Data Receiver")
    IBM reference UK9890010.

4. Patent Application 90GB-026336 filed December 4, 1990, published June 25, 1992 as WO9210894.
    ("Optimized Method of Data Communication And System Employing Same").
    IBM reference UK9900045.

5. Patent Application 90GB-026338 filed December 4, 1990, published June 17, 1992 as GB2250897, ("Method Of Error Recovery In A Data Communication System").
    IBM reference UK9900044.

6. Patent Application 91WO-G00254 filed February 19, 1991, published September 3, 1992 as WO9215058, ("Data Storage Subsystem")
    IBM reference UK900047.

7. Patent Application 91WO-G00256 filed February 19, 1991, published September 3, 1992 as WO9215054, ("Data Transfer Between A Data Storage Subsystem And A Host System").
    IBM reference UK9910010.

8. Patent Application 91EP-311325 filed December 5, 1991, published June 9, 1993 as EP-544954,
    ("Disk Drive Synchronization").
    IBM reference UK9910058.

9. Patent Application 92GB-013240 filed June 23, 1992, published January 5, 1994 as GB2268374,
    ("Network Addressing").
    IBM reference UK9920027.

10. Patent Application 92GB-013151 filed June 20, 1992, published January 5, 1994 as GB2268373, ("Error Recovery in an Information Communication System")
    IBM reference UK9920028

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